

NL-note Block Diagram

NL-note 91.4N401.001

Dec. 28, 2010

PCB Layer Stackup

L1:Component
L2:GND
L3:Signal 1
L4:Power
L5:Signal 2
L6:Signal 3
L7:GND
L8:Component

Battery Charger/Selector

BQ24742 74
INPUTS OUTPUTS
DOCK_PWR20 CHARGER_OUT12

System DC/DC
TPS51220ARSN 78
VINT20 VCC5M
VCC3M

CPU DC/DC
VT1316/VT1317 79
VCC5V_OUT VCCCPUCORE

GMCH GFX CORE
VT1317 81
VCC5V_OUT VCCGFXCORE

VCC1R5A
VT357FCX 86
VCC5V_OUT VCC1R5A

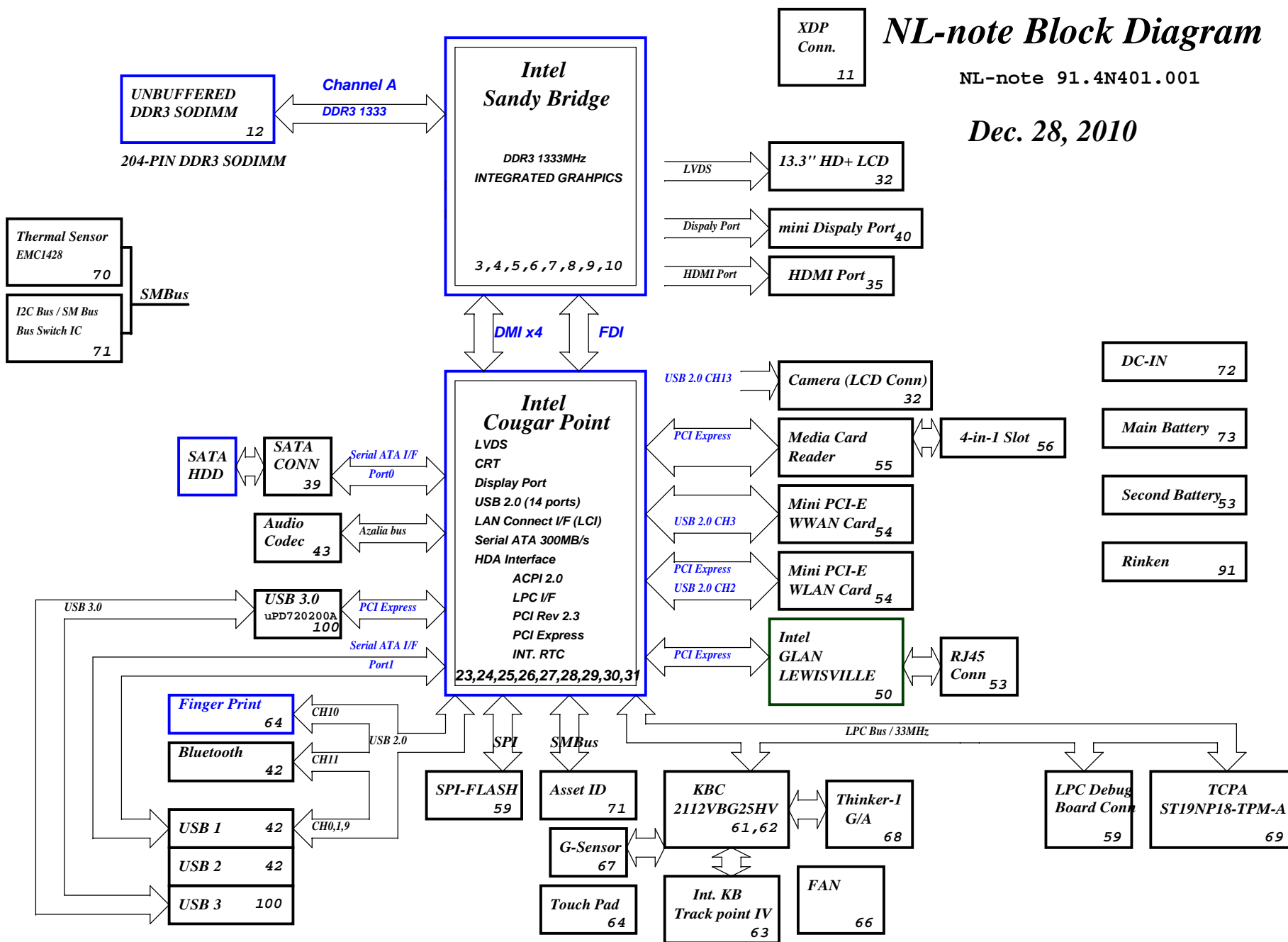
VCC0R75B
MAX1510 87
VCC1R5A VCC0R75B

VCC1R8B
TPS62060 89
VCC5V_OUT VCC1R8B

VCC1R05AMT
VT356FCX 85
VCC5V_OUT VCC1R05LAN

VCC1R05B_VTT
VT357FCX 84
VCC5V_OUT VCC1R05B_VTT

VCCSA
VT355FCX 90
VCC5V_OUT VCCSA



<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Block Diagram		
Size A3	Document Number NL-note	Rev SC
Date: Monday, March 07, 2011	Sheet 1 of 101	

RESISTOR

Symbol name	Value	Tolerance (J: 5%, F: 1%, D: 0.5%, B: 0.1 %)	Rating 0402=> 1/16W, 25V 0603 => 1/16W, 75V 0805 => 1/10W, 100V	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
10KR3	10K Ohm	If no letter, it means J: 5%	1/16W, 75V	0603
33D3R5	33.3 Ohm	If no letter, it means J: 5%	1/10W, 100V	0805
1KR3F	1K Ohm	F: 1%	1/16W, 75V	0603

The naming rule is value + R + size + tolerance
 For the value, it can be read by the number before R. (R means resistor)
 For the tolerance, it can be read from the last letter.
 For the rating, we don't show on the symbol name.
 For the size, R2=>0402, R3=>0603, R5=>0805,....

CAPACITOR

Symbol name	Value	Tolerance (M: +/-20, K: +/-10, Z: +80/-20)	Rating	Size 2=>0402, 3=>0603, 5=>0805, 6=>1206, 0=>1210
SCD1U10V2MX-1	0.1uF	M/X5R	10V	0402
SC10U6D3V5MX	10uF	M/X5R	6.3V	0805
SC2D2U16V5ZY	2.2uF	Z/Y5V	16V	0805

The naming rule is
Capacitor type + value + rating + size + tolerance + material
SCD1U10V2MX-1
SC=> SMT Ceramic, TC=> POS cap or SP cap
D1U => 0.1uF
10V => the voltage rating is 10V
2=> 0402, 3=>0603, 5=>0805
M=>tolerance M, K, Z
X=> X7R/X5R, Y=> Y5V
-1 => symbol version, nonsense to EE characteristic

PLANAR_ID[3..0]

IBEXPEAK-M	39	38	49	48	Planar ID Version	Planar PCB Version
PLANAR_IDn	3	2	1	0		
	0	0	0	0	NL-note SDV/UT	SA
	0	0	0	1	NL-note FVT	SB
	0	0	1	0	NL-note SIT	SC
	0	0	1	1	NL-note SIT-R	SD
	0	1	0	0	NL-note SVT	SE
	0	1	0	1		
	0	1	1	0		
	0	1	1	1		

EC HISTORY

[illegible]

<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Reference

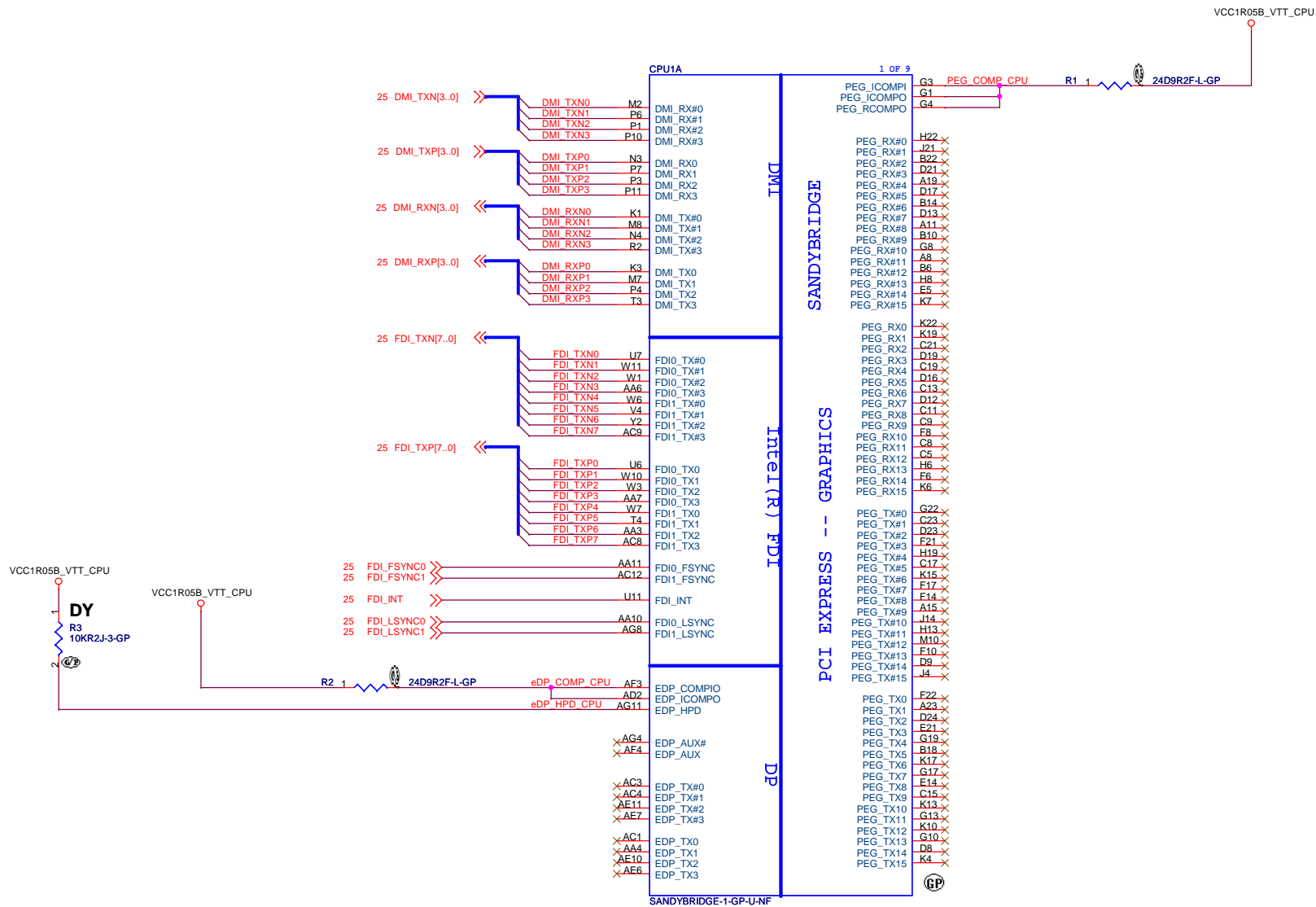
Size
A3

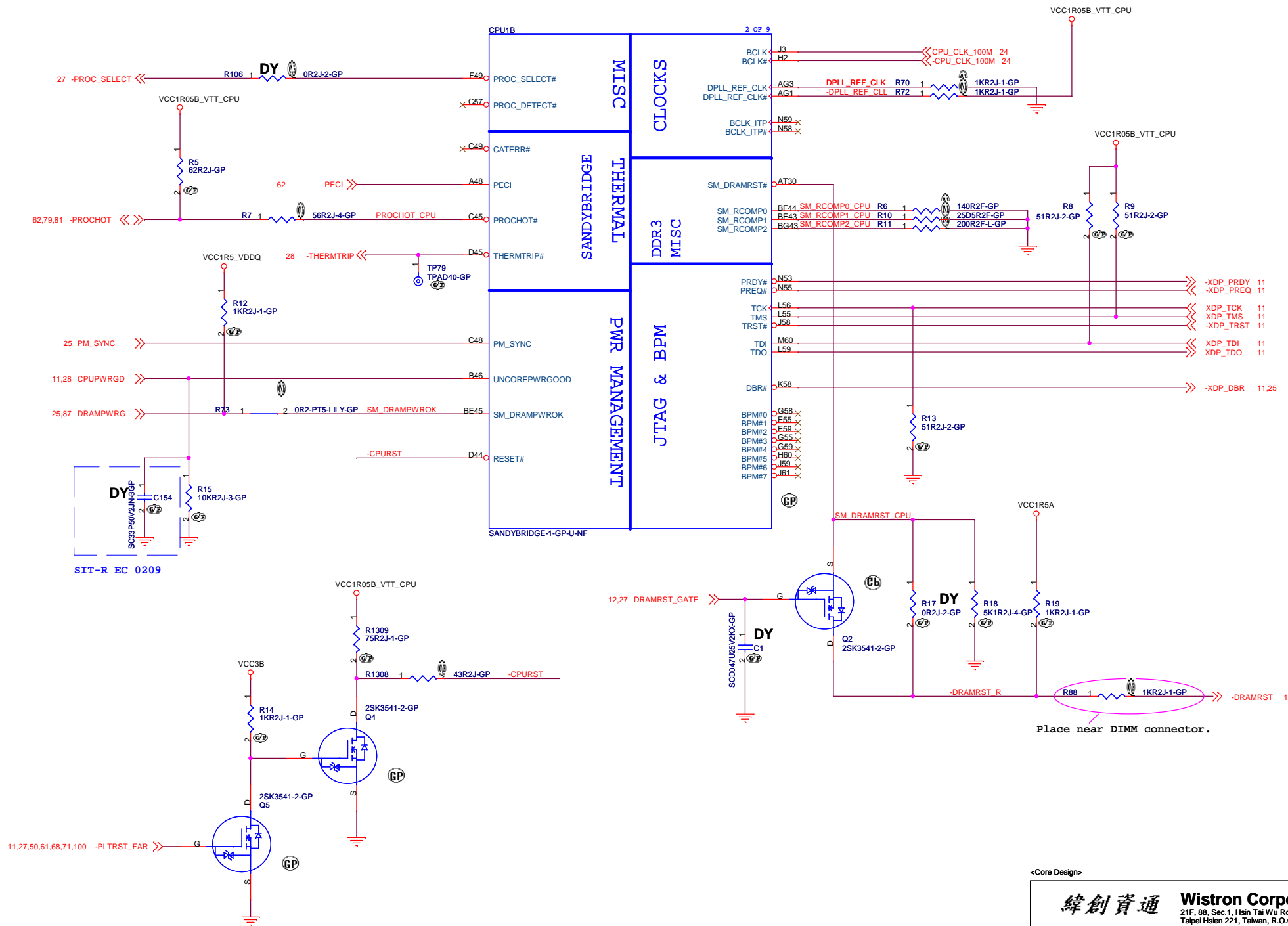
	Document Number
--	-----------------

NL-noteRev
SC

Date: Monday, March 07, 2011

Sheet 2 of 101





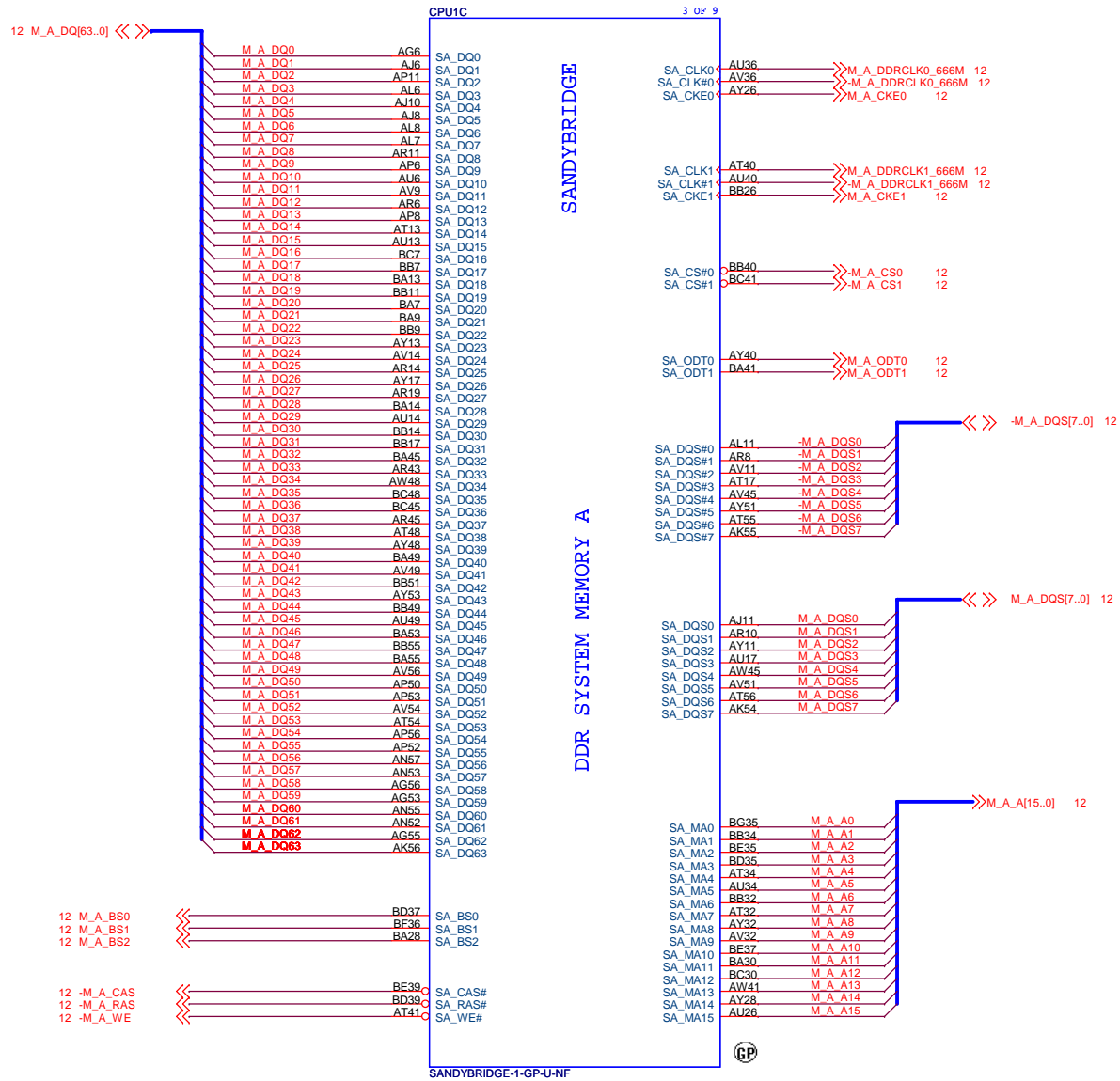
<Core Design>

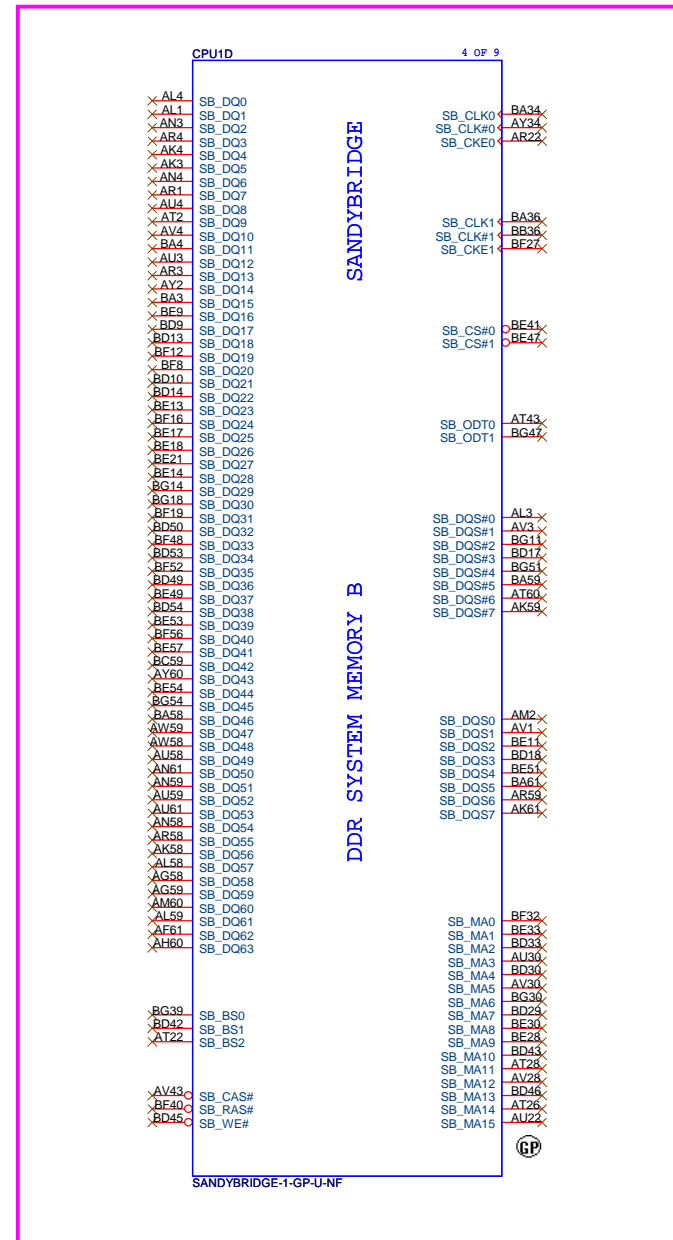
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **CPU(2/8):CLK/MISC/JTAG**

Size A3	Document Number NL-note	Rev SC
------------	-----------------------------------	------------------

Date: Monday, March 07, 2011 Sheet 4 of 101





<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	Notes
Title	Author	Date	Location	

CPU(4/8):DDR3 Channel-BSize
A3

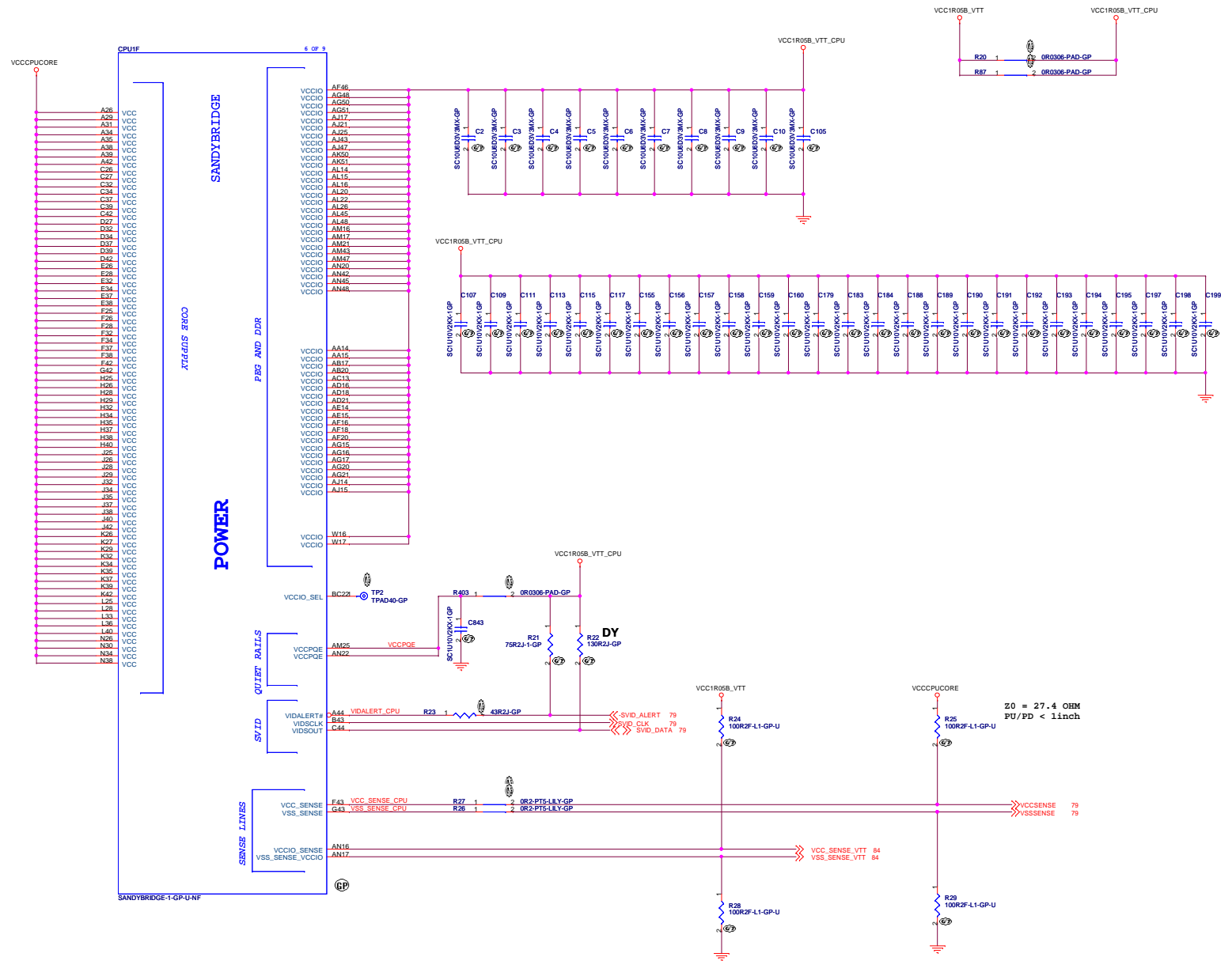
Document Number

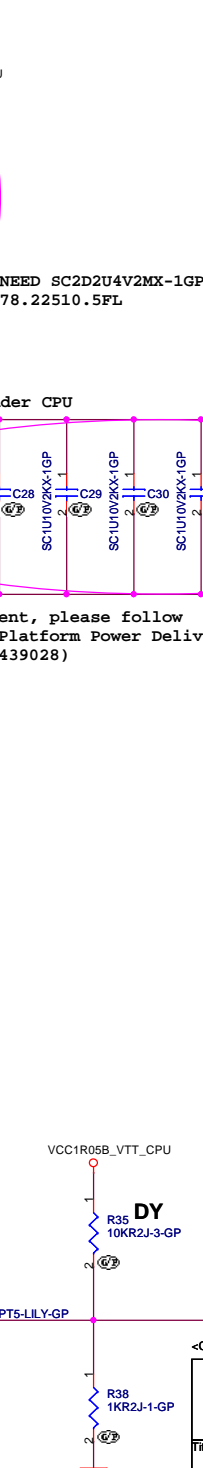
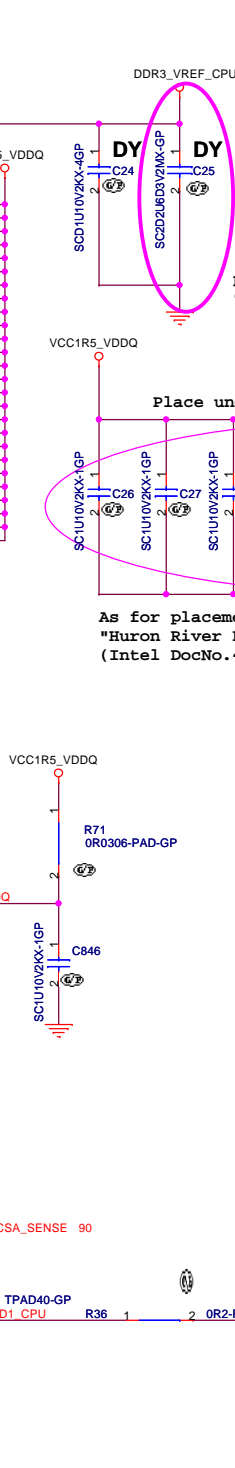
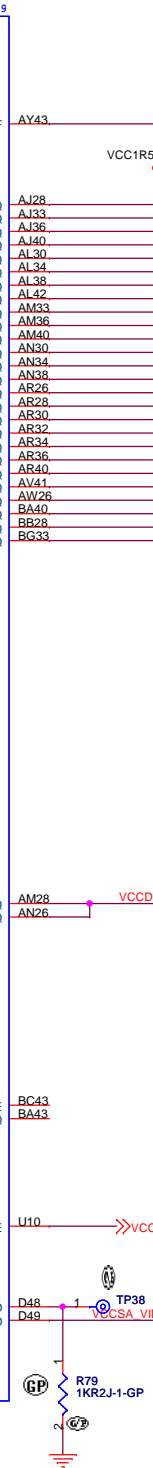
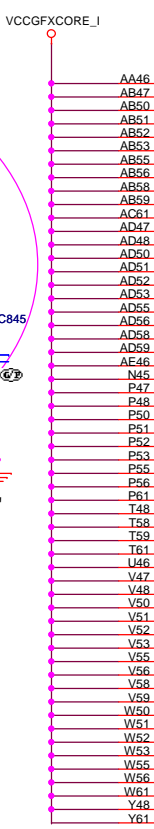
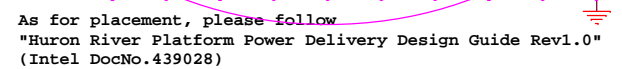
NL-note


Rev	SC
-----	----

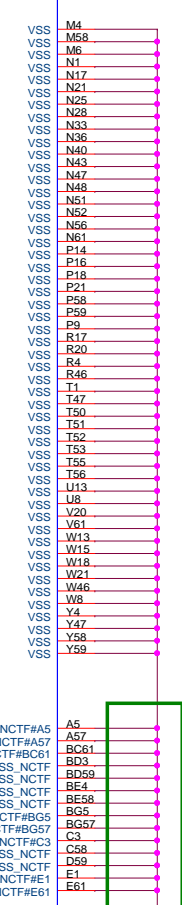
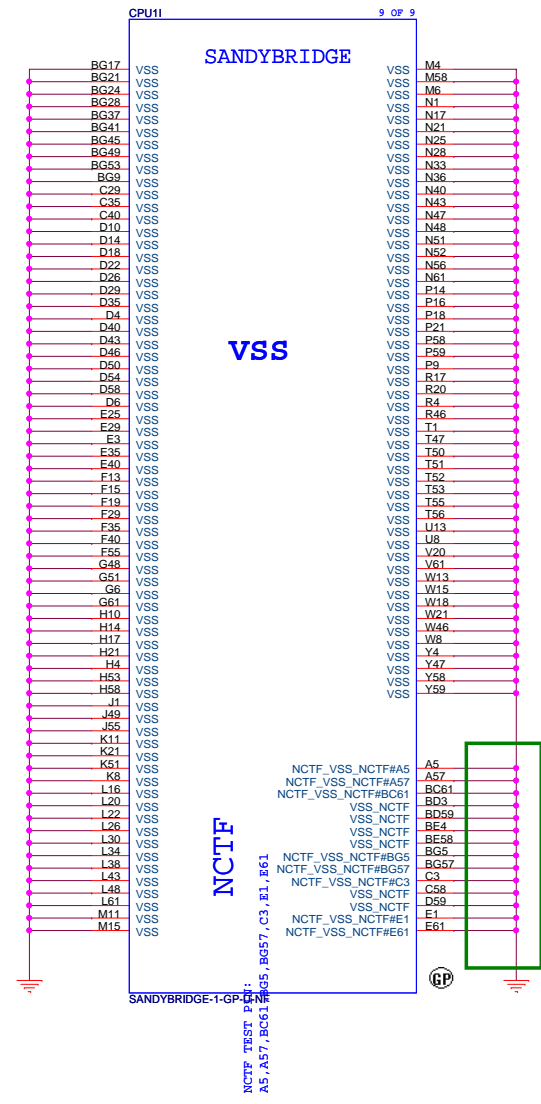
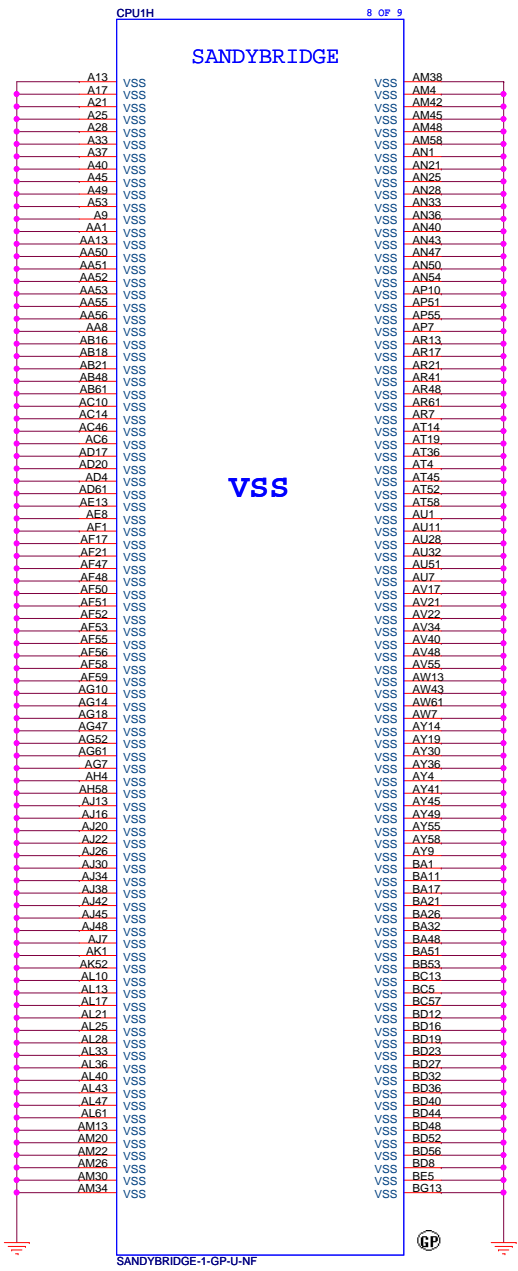
Date: Monday, March 07, 2011

Sheet 6 of 101



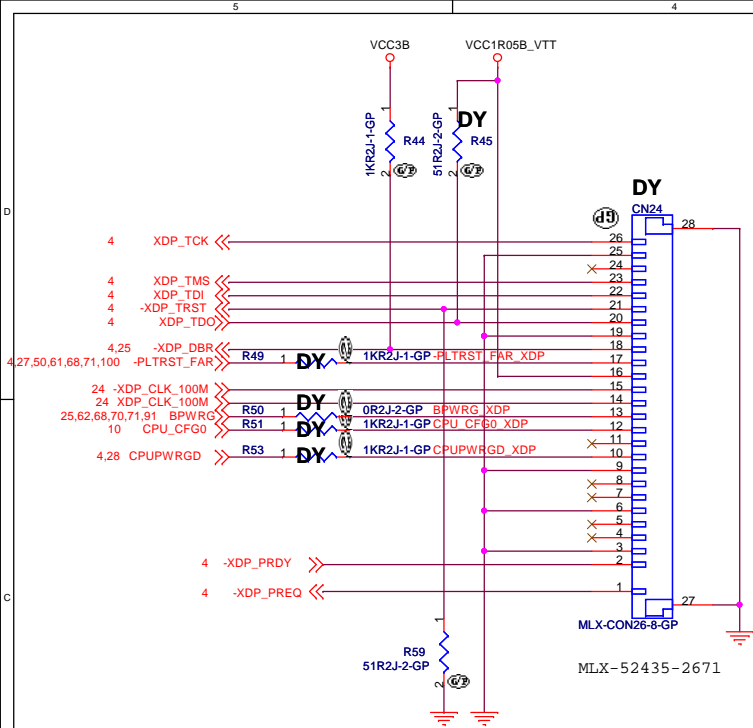


<div> <div>  <div> <div>緯創資通</div> <div> Wistron Corporation 21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C. </div> </div> </div> </div>			
<div> <div>Title</div> <div>CPU(6/8): Graphics Power</div> </div>			
<div>Size</div> <div>A3</div>	<div>Document Number</div> <div>NL-note</div>		<div>Rev</div> <div>SC</div>
<div> <div>Date: Monday, March 07, 2011</div> <div>Sheet 8 of 101</div> </div>			



<Core Design>

緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,		Taipet Hsien 221, Taiwan, R.O.C.	
Title CPU(7/8):GND			
Size	Document Number	Rev	SC
Custom	NL-note		
Date:	Monday, March 07, 2011	Sheet 9 of 101	

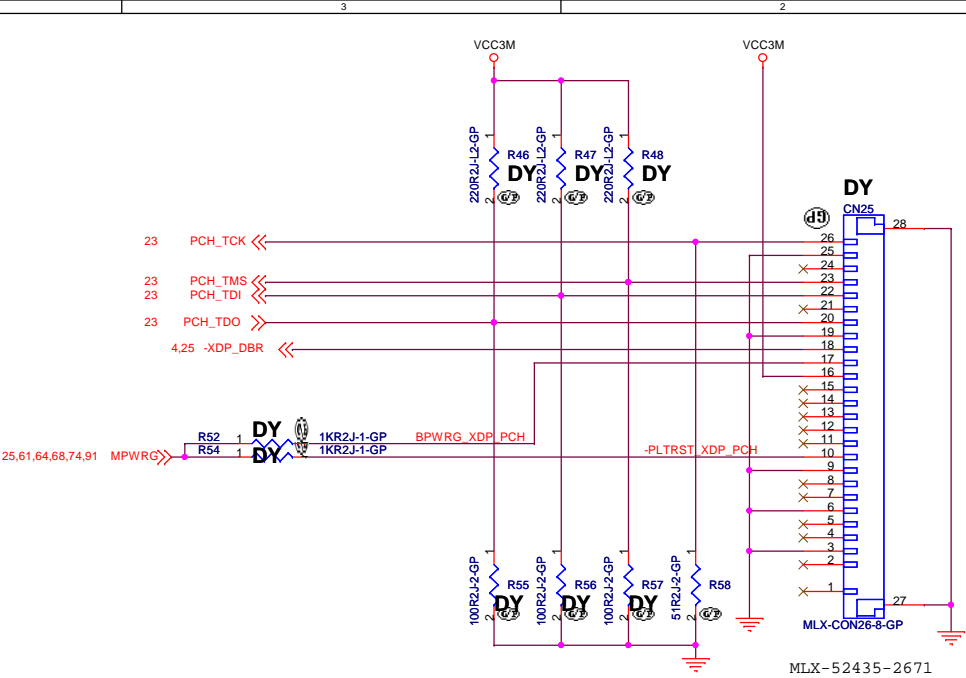


DEBUG Interface for Processor.

XDP1

		ENABLE	DISABLE
TDO	R45	ASM	DY
TRST#	R59	ASM	ASM
DBRST#	R44	ASM	ASM
RESET#	R49	ASM	DY
CFG0	R51	ASM	DY
PWRGD	R53	ASM	DY
BPWRG	R50	ASM	DY
	CN24	ASM	DY

↑
SIT Logic



DEBUG Interface for PCH.

XDP2

		ENABLE	DISABLE
TDO	R46	220	DY
	R55	100	DY
TMS	R48	220	DY
	R57	100	DY
TDI	R47	220	DY
	R56	100	DY
TCK	R58	51	51
MPWRG	R52	ASM	DY
	R54	ASM	DY
	CN25	ASM	DY

↑
SIT Logic

A		B		C		D		E	

BLANK

<Core Design>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BLANK			
Size	Document Number		Rev
A3	NL-note		SC
Date:	Monday, March 07, 2011	Sheet 14 of	101

BLANK

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size
A3

Document Number

Rev
SC

Date: Monday, March 07, 2011

Sheet 15 of 101

NL-note

BLANK

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size
A3

Document Number

Rev
SC

Date: Monday, March 07, 2011

Sheet 16 of 101

BLANK

<Core Design>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BLANK			
Size	Document Number		Rev
A3	NL-note		SC
Date:	Monday, March 07, 2011	Sheet 17 of	101

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size

A3

Document Number

NL-note

Rev

SC

Date: Monday, March 07, 2011

Sheet 18 of 101

BLANK

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size	Document Number	Rev
A3	NL-note	SC
Date:	Monday, March 07, 2011	Sheet 19 of 101

BLANK

<Core Design>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BLANK			
Size	Document Number		Rev
A3	NL-note		SC
Date:	Monday, March 07, 2011	Sheet	20 of 101

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size

A3

Document Number

NL-note

Rev

SC

Date:

Monday, March 07, 2011

Sheet

21

of

101

BLANK

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size

A3

Document Number

NL-note

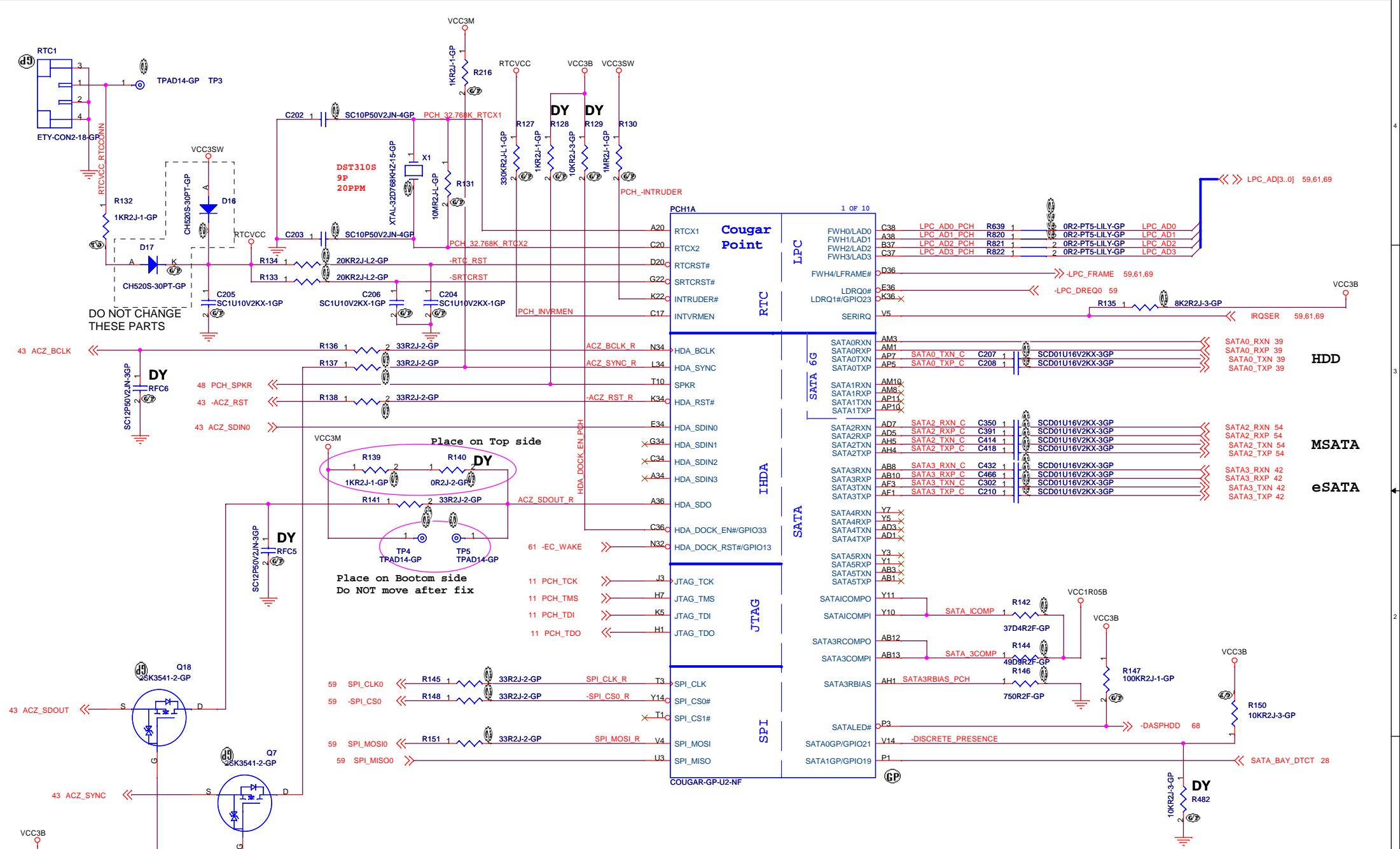
Rev

SC

Date: Monday, March 07, 2011

Sheet 22 of 101

1



54 PCIE_WLAN_RXN >>
54 PCIE_WLAN_RXP >>
54 PCIE_WLAN_TXN >>
54 PCIE_WLAN_TXP >>

55 PCIE_MCC_RXN >>
55 PCIE_MCC_TXN >>
55 PCIE_MCC_TXP >>
50 PCIE_GBE_RXN >>
50 PCIE_GBE_RXP >>
50 PCIE_GBE_TXN >>
50 PCIE_GBE_TXP >>

100 PCIE_USB30_RXN >>
100 PCIE_USB30_RXP >>
100 PCIE_USB30_TXN >>
100 PCIE_USB30_TXP >>

54 -PCIE_CLK_WLAN <<
54 PCIE_CLK_WLAN <<
54 -CLKREQ_WLAN <<

55 -PCIE_CLK_MCC <<
55 PCIE_CLK_MCC <<
55 -CLKREQ_MCC <<

50 -PCIE_CLK_GBE <<
50 PCIE_CLK_GBE <<
50 -CLKREQ_GBE <<

100 -PCIE_CLK_USB30 <<
100 PCIE_CLK_USB30 <<
100 -CLKREQ_USB30_TR <<

11 -XDP_CLK_100M <<
11 XDP_CLK_100M <<

PCH1B
BG34
BH34
AV32
AU32

PERN1
PERP1
PETN1
PETP1

PERN2
PERP2
PETN2
PETP2

PERN3
PERP3
PETN3
PETP3

PERN4
PERP4
PETN4
PETP4

PERN5
PERP5
PETN5
PETP5

PERN6
PERP6
PETN6
PETP6

PERN7
PERP7
PETN7
PETP7

PERN8
PERP8
PETN8
PETP8

CLKOUT_PCIE0N
CLKOUT_PCIE0P
PCIECLKRQ0#/GPIO73

CLKOUT_PCIE1N
CLKOUT_PCIE1P
PCIECLKRQ1#/GPIO18

CLKOUT_PCIE2N
CLKOUT_PCIE2P
PCIECLKRQ2#/GPIO20

CLKOUT_PCIE3N
CLKOUT_PCIE3P
PCIECLKRQ3#/GPIO25

CLKOUT_PCIE4N
CLKOUT_PCIE4P
PCIECLKRQ4#/GPIO26

CLKOUT_PCIE5N
CLKOUT_PCIE5P
PCIECLKRQ5#/GPIO44

CLKOUT_PEG_B_N
CLKOUT_PEG_B_P
PEG_B_CLKRQ#/GPIO56

CLKOUT_PCIE6N
CLKOUT_PCIE6P
PCIECLKRQ6#/GPIO45

CLKOUT_PCIE7N
CLKOUT_PCIE7P
PCIECLKRQ7#/GPIO46

CLKOUT_ITPXD_P_N
CLKOUT_ITPXD_P_P

Cougar
Point

PCI-E*

CLOCKS

COUGAR-GP-U2-NF

SMBUS

Controller
Link

2 OF 10

PEG_A_CLKRQ#/GPIO47

CLKOUT_PEG_A_N
CLKOUT_PEG_A_P

CLKOUT_DMI_N
CLKOUT_DMI_P

CLKOUT_DP_N
CLKOUT_DP_P

CLKIN_DMI_N
CLKIN_DMI_P

CLKIN_GND1_N
CLKIN_GND1_P

CLKIN_DOT_96N
CLKIN_DOT_96P

CLKIN_SATA_N
CLKIN_SATA_P

REFCLK14IN

CLKIN_PCIELOOPBACK

XTAL25_IN
XTAL25_OUT

XCLK_RCOMP

CLKOUTFLEX0/GPIO64

CLKOUTFLEX1/GPIO65

CLKOUTFLEX2/GPIO66

CLKOUTFLEX3/GPIO67

CLKOUTFLEX4/GPIO68

CLKOUTFLEX5/GPIO69

CLKOUTFLEX6/GPIO70

CLKOUTFLEX7/GPIO71

CLKOUTFLEX8/GPIO72

CLKOUTFLEX9/GPIO73

CLKOUTFLEX10/GPIO74

CLKOUTFLEX11/GPIO75

CLKOUTFLEX12/GPIO76

CLKOUTFLEX13/GPIO77

CLKOUTFLEX14/GPIO78

CLKOUTFLEX15/GPIO79

CLKOUTFLEX16/GPIO80

CLKOUTFLEX17/GPIO81

CLKOUTFLEX18/GPIO82

CLKOUTFLEX19/GPIO83

CLKOUTFLEX20/GPIO84

CLKOUTFLEX21/GPIO85

CLKOUTFLEX22/GPIO86

CLKOUTFLEX23/GPIO87

CLKOUTFLEX24/GPIO88

CLKOUTFLEX25/GPIO89

CLKOUTFLEX26/GPIO90

CLKOUTFLEX27/GPIO91

CLKOUTFLEX28/GPIO92

CLKOUTFLEX29/GPIO93

CLKOUTFLEX30/GPIO94

CLKOUTFLEX31/GPIO95

CLKOUTFLEX32/GPIO96

CLKOUTFLEX33/GPIO97

CLKOUTFLEX34/GPIO98

CLKOUTFLEX35/GPIO99

CLKOUTFLEX36/GPIO100

CLKOUTFLEX37/GPIO101

CLKOUTFLEX38/GPIO102

CLKOUTFLEX39/GPIO103

CLKOUTFLEX40/GPIO104

CLKOUTFLEX41/GPIO105

CLKOUTFLEX42/GPIO106

CLKOUTFLEX43/GPIO107

CLKOUTFLEX44/GPIO108

CLKOUTFLEX45/GPIO109

CLKOUTFLEX46/GPIO110

CLKOUTFLEX47/GPIO111

CLKOUTFLEX48/GPIO112

CLKOUTFLEX49/GPIO113

CLKOUTFLEX50/GPIO114

CLKOUTFLEX51/GPIO115

CLKOUTFLEX52/GPIO116

CLKOUTFLEX53/GPIO117

CLKOUTFLEX54/GPIO118

CLKOUTFLEX55/GPIO119

CLKOUTFLEX56/GPIO120

CLKOUTFLEX57/GPIO121

CLKOUTFLEX58/GPIO122

CLKOUTFLEX59/GPIO123

CLKOUTFLEX60/GPIO124

CLKOUTFLEX61/GPIO125

CLKOUTFLEX62/GPIO126

CLKOUTFLEX63/GPIO127

CLKOUTFLEX64/GPIO128

CLKOUTFLEX65/GPIO129

CLKOUTFLEX66/GPIO130

CLKOUTFLEX67/GPIO131

CLKOUTFLEX68/GPIO132

CLKOUTFLEX69/GPIO133

CLKOUTFLEX70/GPIO134

CLKOUTFLEX71/GPIO135

CLKOUTFLEX72/GPIO136

CLKOUTFLEX73/GPIO137

CLKOUTFLEX74/GPIO138

CLKOUTFLEX75/GPIO139

CLKOUTFLEX76/GPIO140

CLKOUTFLEX77/GPIO141

CLKOUTFLEX78/GPIO142

CLKOUTFLEX79/GPIO143

CLKOUTFLEX80/GPIO144

CLKOUTFLEX81/GPIO145

CLKOUTFLEX82/GPIO146

CLKOUTFLEX83/GPIO147

CLKOUTFLEX84/GPIO148

CLKOUTFLEX85/GPIO149

CLKOUTFLEX86/GPIO150

CLKOUTFLEX87/GPIO151

CLKOUTFLEX88/GPIO152

CLKOUTFLEX89/GPIO153

CLKOUTFLEX90/GPIO154

CLKOUTFLEX91/GPIO155

CLKOUTFLEX92/GPIO156

CLKOUTFLEX93/GPIO157

CLKOUTFLEX94/GPIO158

CLKOUTFLEX95/GPIO159

CLKOUTFLEX96/GPIO160

CLKOUTFLEX97/GPIO161

CLKOUTFLEX98/GPIO162

CLKOUTFLEX99/GPIO163

CLKOUTFLEX100/GPIO164

CLKOUTFLEX101/GPIO165

CLKOUTFLEX102/GPIO166

CLKOUTFLEX103/GPIO167

CLKOUTFLEX104/GPIO168

CLKOUTFLEX105/GPIO169

CLKOUTFLEX106/GPIO170

CLKOUTFLEX107/GPIO171

CLKOUTFLEX108/GPIO172

CLKOUTFLEX109/GPIO173

CLKOUTFLEX110/GPIO174

CLKOUTFLEX111/GPIO175

CLKOUTFLEX112/GPIO176

CLKOUTFLEX113/GPIO177

CLKOUTFLEX114/GPIO178

CLKOUTFLEX115/GPIO179

CLKOUTFLEX116/GPIO180

CLKOUTFLEX117/GPIO181

CLKOUTFLEX118/GPIO182

CLKOUTFLEX119/GPIO183

CLKOUTFLEX120/GPIO184

CLKOUTFLEX121/GPIO185

CLKOUTFLEX122/GPIO186

CLKOUTFLEX123/GPIO187

CLKOUTFLEX124/GPIO188

CLKOUTFLEX125/GPIO189

CLKOUTFLEX126/GPIO190

CLKOUTFLEX127/GPIO191

CLKOUTFLEX128/GPIO192

CLKOUTFLEX129/GPIO193

CLKOUTFLEX130/GPIO194

CLKOUTFLEX131/GPIO195

CLKOUTFLEX132/GPIO196

CLKOUTFLEX133/GPIO197

CLKOUTFLEX134/GPIO198

CLKOUTFLEX135/GPIO199

CLKOUTFLEX136/GPIO200

CLKOUTFLEX137/GPIO201

CLKOUTFLEX138/GPIO202

CLKOUTFLEX139/GPIO203

CLKOUTFLEX140/GPIO204

CLKOUTFLEX141/GPIO205

CLKOUTFLEX142/GPIO206

CLKOUTFLEX143/GPIO207

CLKOUTFLEX144/GPIO208

CLKOUTFLEX145/GPIO209

CLKOUTFLEX146/GPIO210

CLKOUTFLEX147/GPIO211

CLKOUTFLEX148/GPIO212

CLKOUTFLEX149/GPIO213

CLKOUTFLEX150/GPIO214

CLKOUTFLEX151/GPIO215

CLKOUTFLEX152/GPIO216

CLKOUTFLEX153/GPIO217

CLKOUTFLEX154/GPIO218

CLKOUTFLEX155/GPIO219

CLKOUTFLEX156/GPIO220

CLKOUTFLEX157/GPIO221

CLKOUTFLEX158/GPIO222

CLKOUTFLEX159/GPIO223

CLKOUTFLEX160/GPIO224

CLKOUTFLEX161/GPIO225

CLKOUTFLEX162/GPIO226

CLKOUTFLEX163/GPIO227

CLKOUTFLEX164/GPIO228

CLKOUTFLEX165/GPIO229

CLKOUTFLEX166/GPIO230

CLKOUTFLEX167/GPIO231

CLKOUTFLEX168/GPIO232

CLKOUTFLEX169/GPIO233

CLKOUTFLEX170/GPIO234

CLKOUTFLEX171/GPIO235

CLKOUTFLEX172/GPIO236

CLKOUTFLEX173/GPIO237

CLKOUTFLEX174/GPIO238

CLKOUTFLEX175/GPIO239

CLKOUTFLEX176/GPIO240

CLKOUTFLEX177/GPIO241

CLKOUTFLEX178/GPIO242

CLKOUTFLEX179/GPIO243

CLKOUTFLEX180/GPIO244

CLKOUTFLEX181/GPIO245

CLKOUTFLEX182/GPIO246

CLKOUTFLEX183/GPIO247

CLKOUTFLEX184/GPIO248

CLKOUTFLEX185/GPIO249

CLKOUTFLEX186/GPIO250

CLKOUTFLEX187/GPIO251

CLKOUTFLEX188/GPIO252

CLKOUTFLEX189/GPIO253

CLKOUTFLEX190/GPIO254

CLKOUTFLEX191/GPIO255

CLKOUTFLEX192/GPIO256

CLKOUTFLEX193/GPIO257

CLKOUTFLEX194/GPIO258

CLKOUTFLEX195/GPIO259

CLKOUTFLEX196/GPIO260

CLKOUTFLEX197/GPIO261

CLKOUTFLEX198/GPIO262

CLKOUTFLEX199/GPIO263

CLKOUTFLEX200/GPIO264

CLKOUTFLEX201/GPIO265

CLKOUTFLEX202/GPIO266

CLKOUTFLEX203/GPIO267

CLKOUTFLEX204/GPIO268

CLKOUTFLEX205/GPIO269

CLKOUTFLEX206/GPIO270

CLKOUTFLEX207/GPIO271

CLKOUTFLEX208/GPIO272

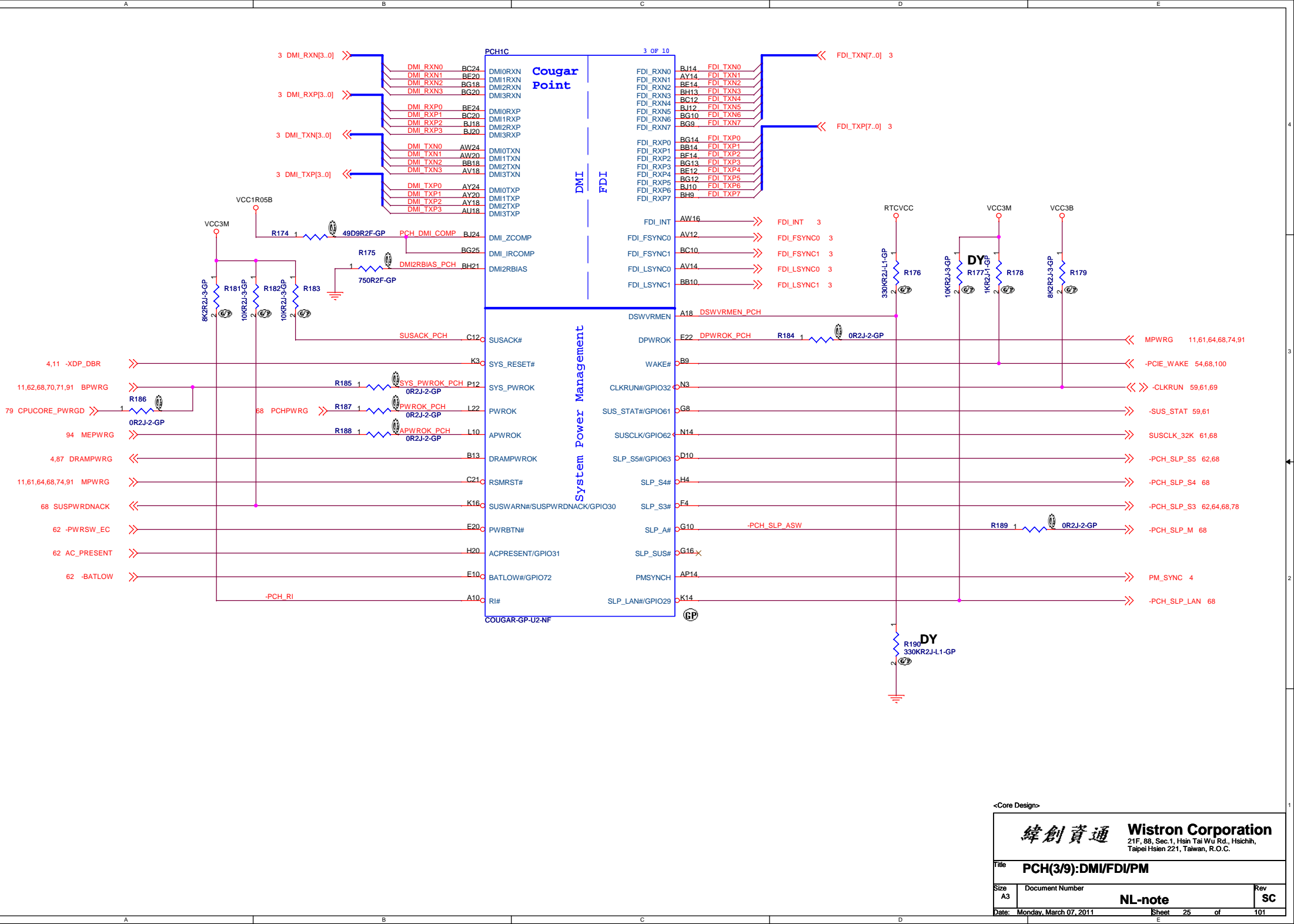
CLKOUTFLEX209/GPIO273

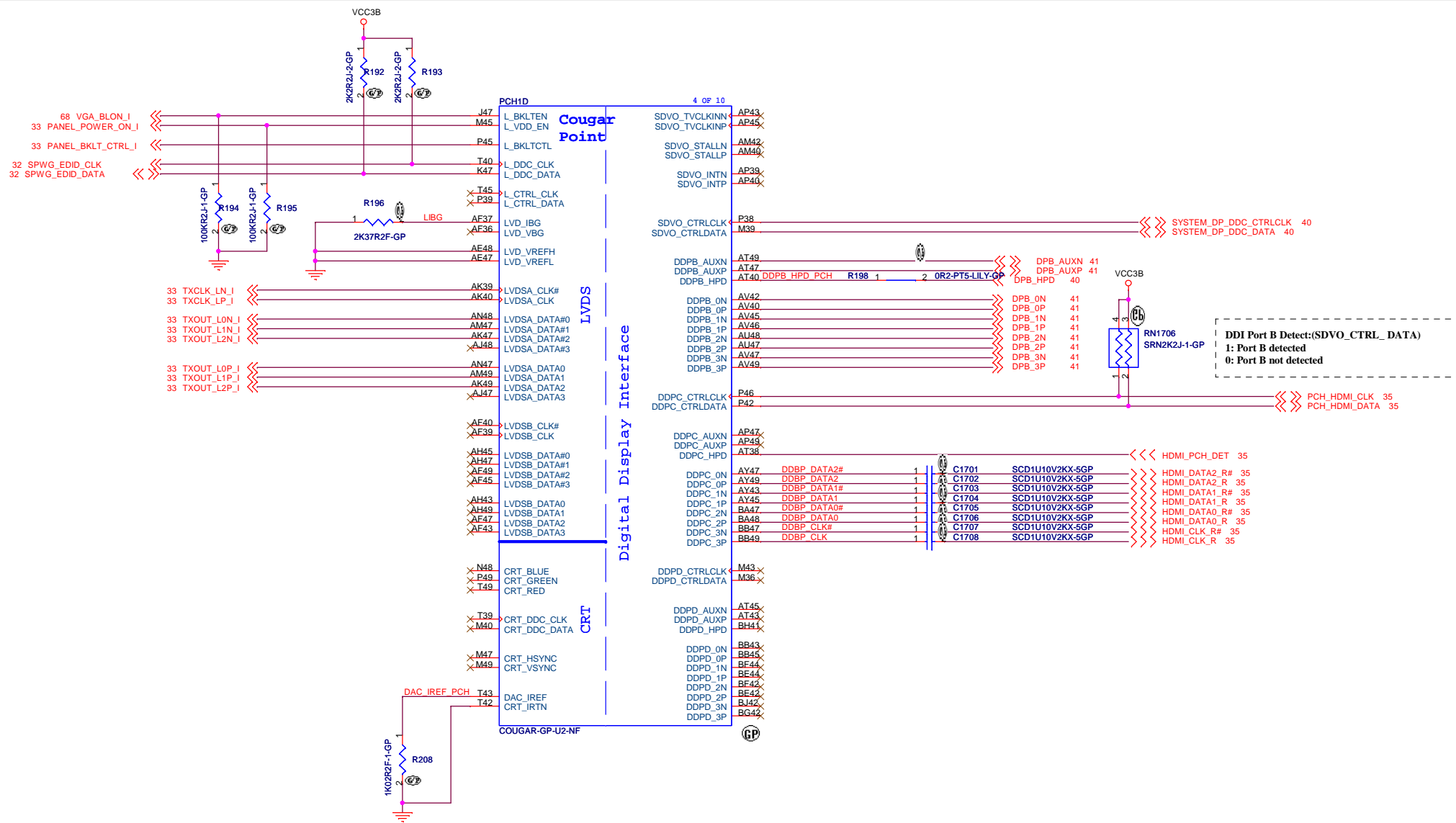
CLKOUTFLEX210/GPIO274

CLKOUTFLEX211/GPIO275

CLKOUTFLEX212/GPIO276

CLKOUTFLEX213/GPIO277

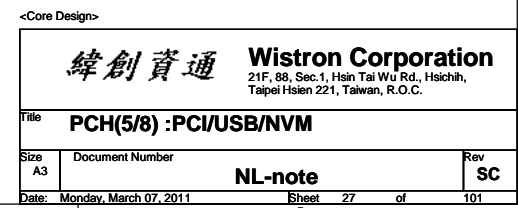


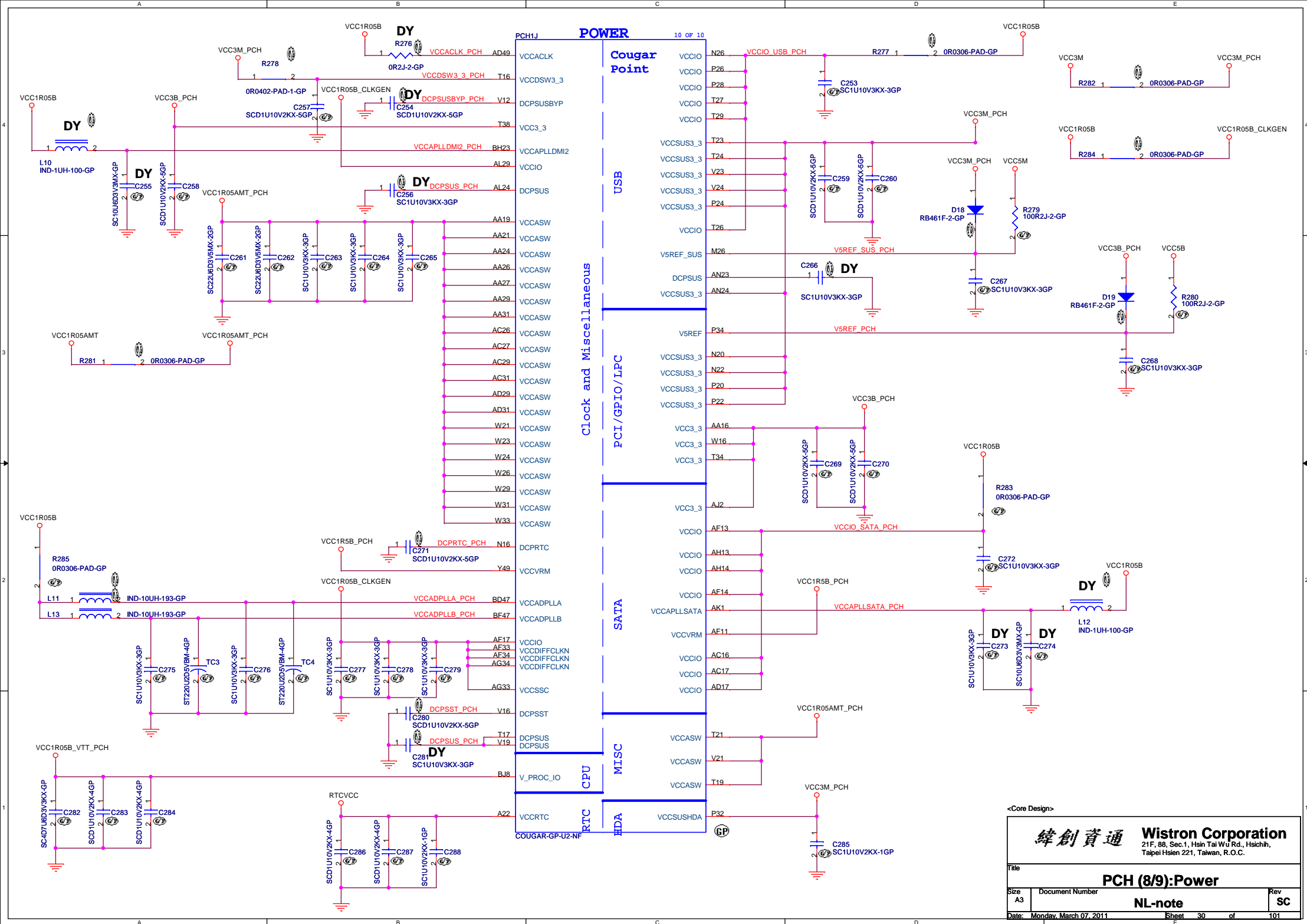


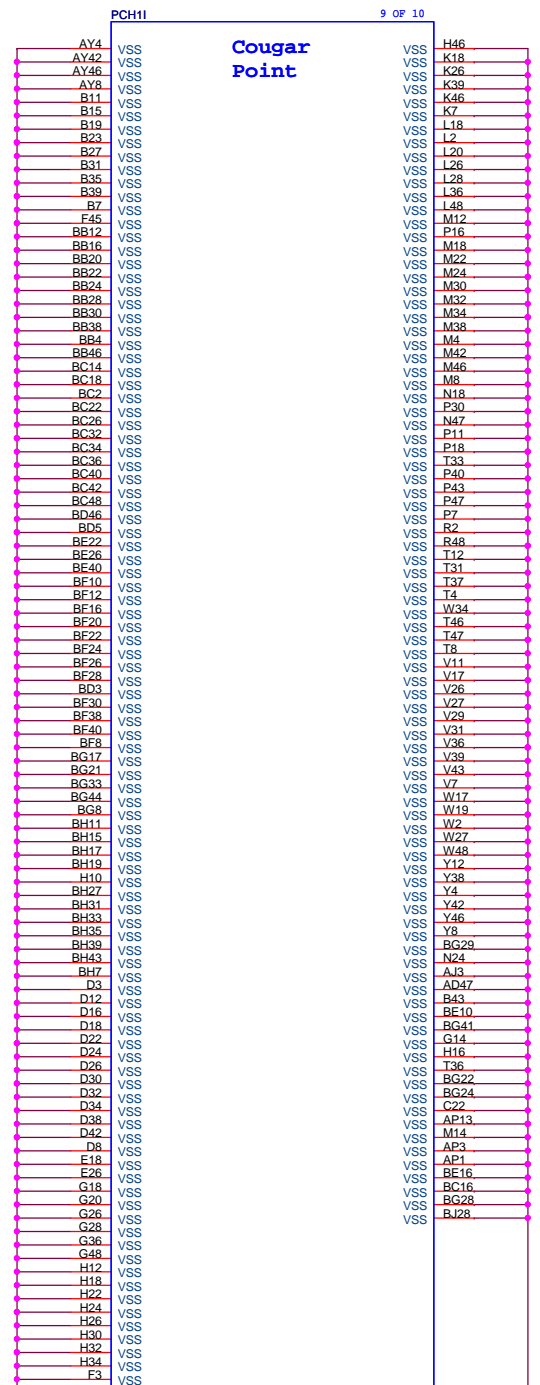
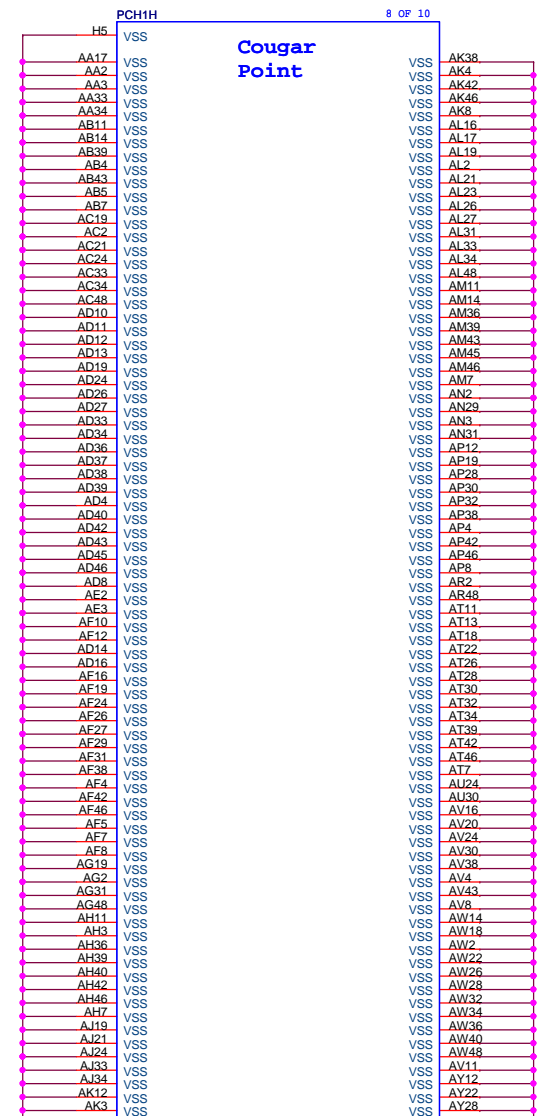
<Core Design>

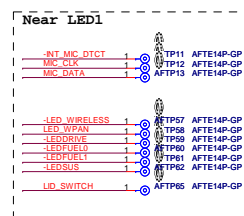
緯創資通 Wistron Corporation
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
 Taipei Hsien 221, Taiwan, R.O.C.

Title			PCH(4/9):LVDS/CRT/DDI	
Size	Document Number	NL-note		Rev
A3				SC
Date:	Monday, March 07, 2011	Sheet	26	of 101

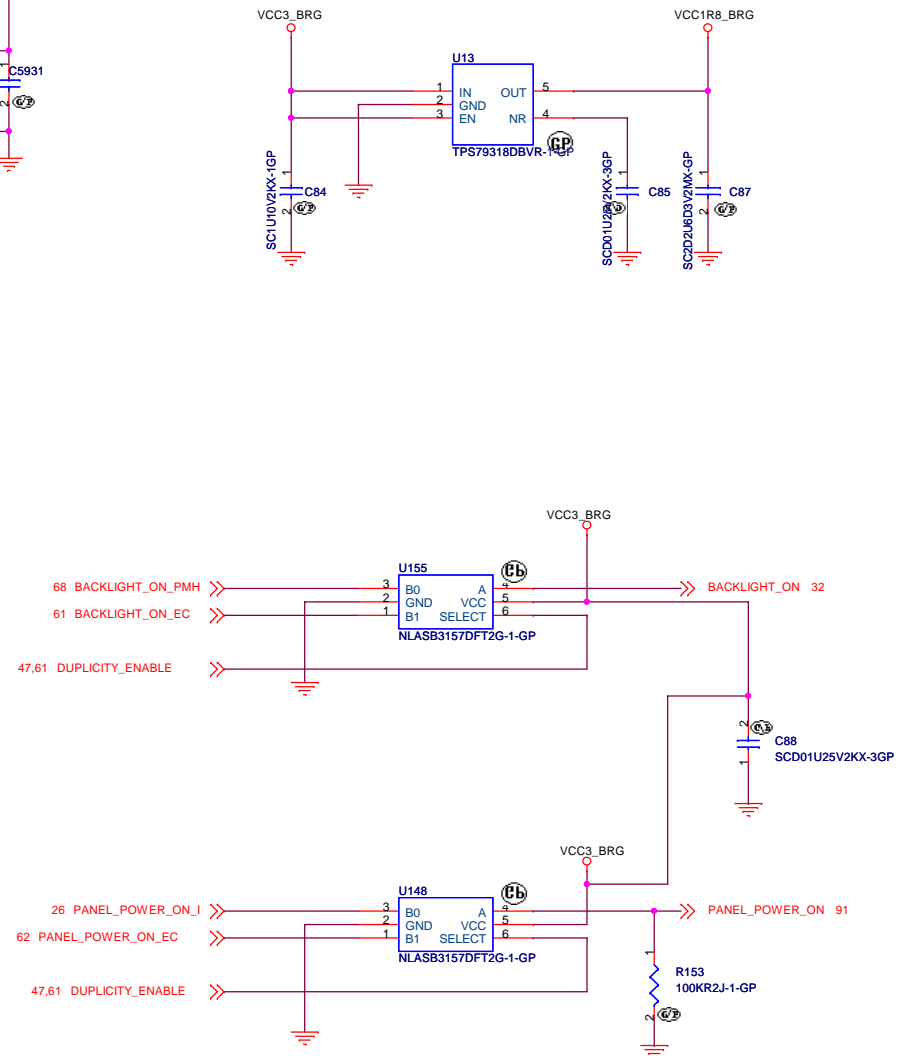
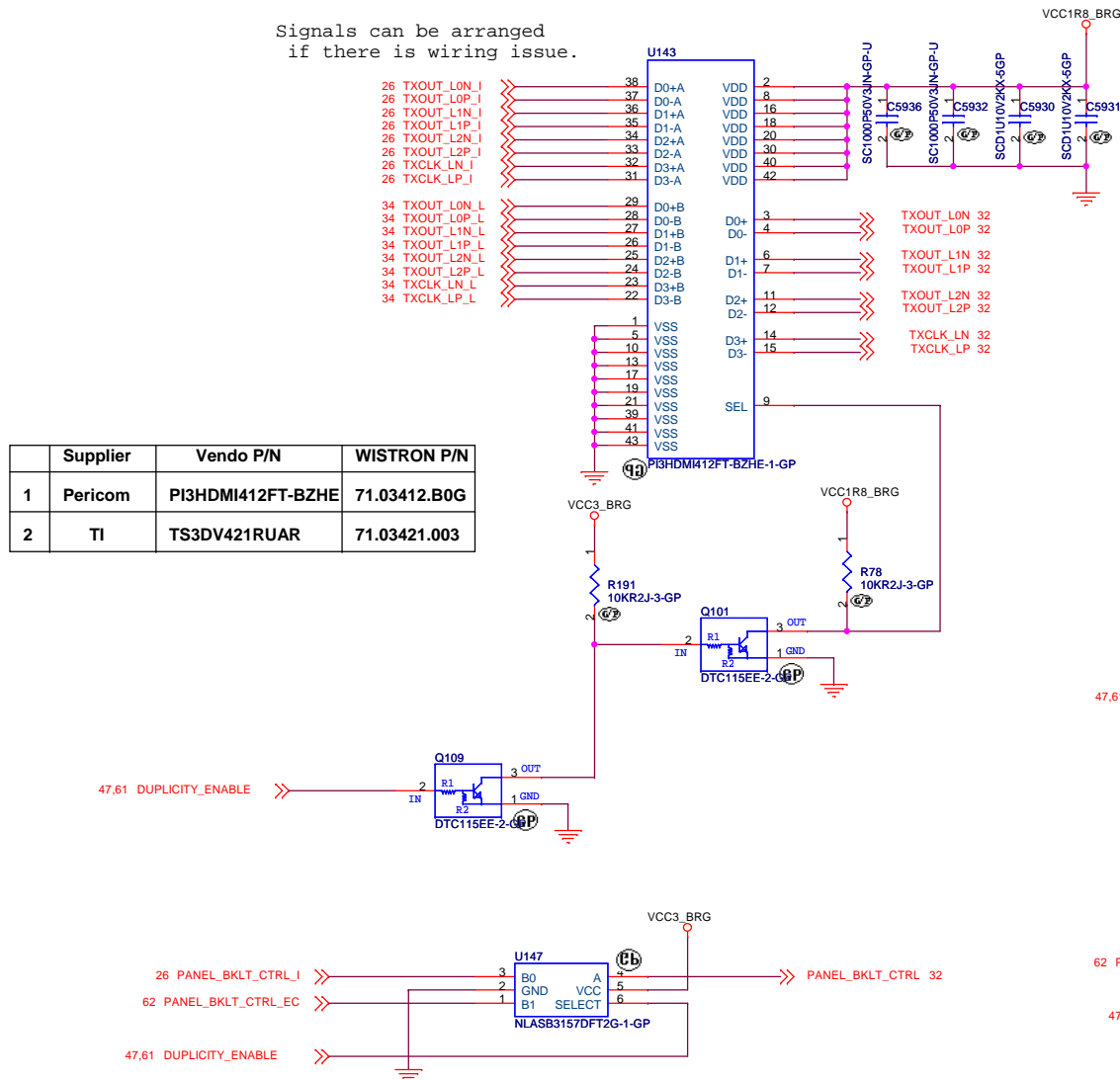








Signals can be arranged
if there is wiring issue.



<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

LPM LVDS SW

Size
A3

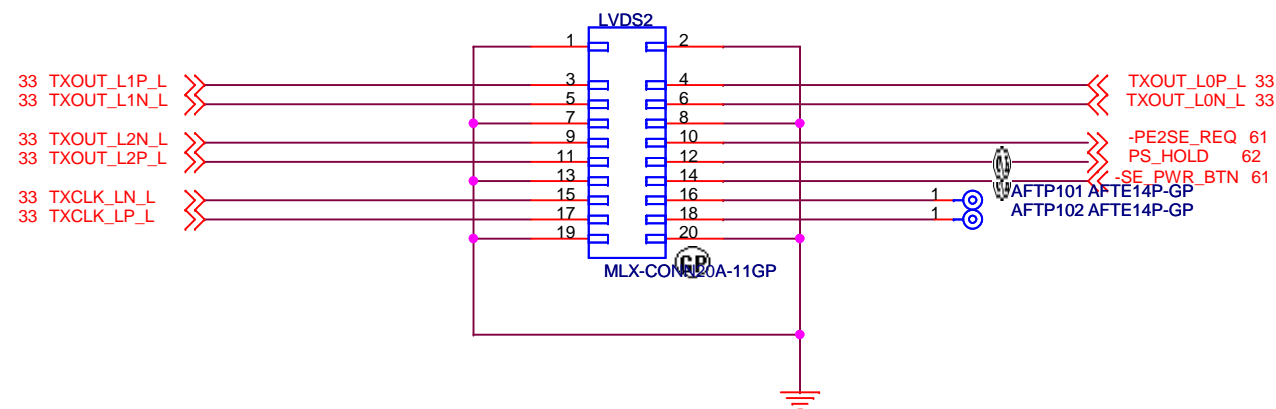
Document Number

NL-note

Rev
SC

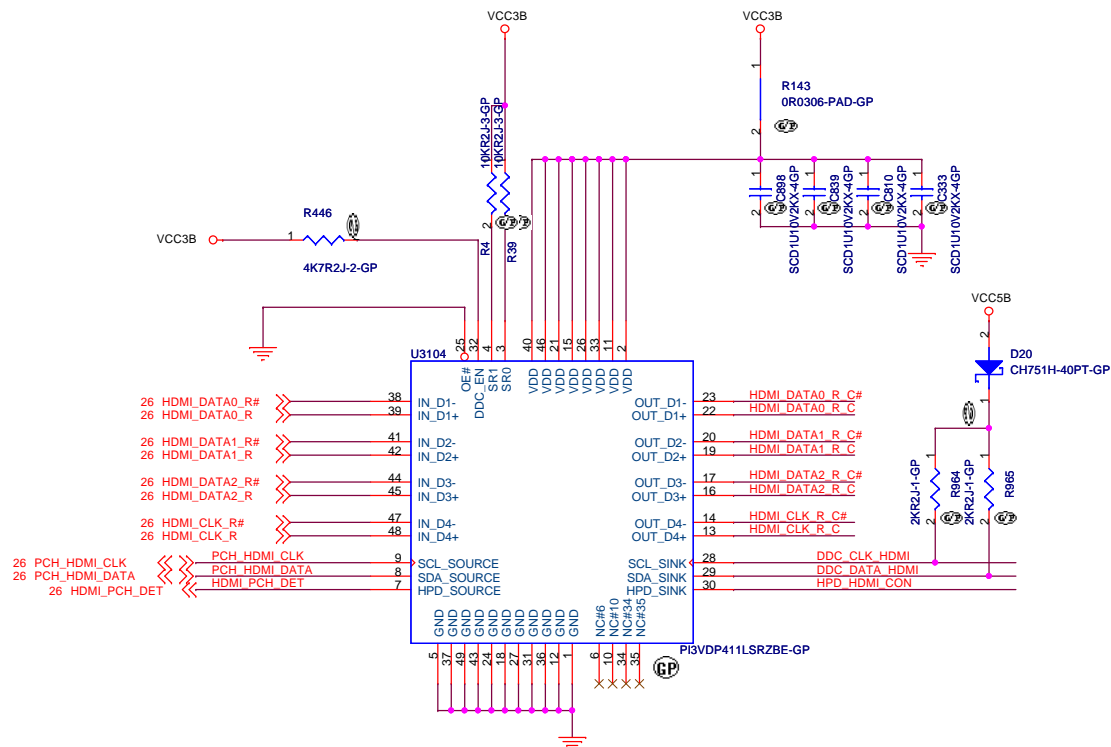
Date: Monday, March 07, 2011

Sheet 33 of 101

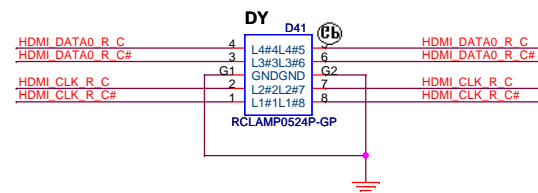
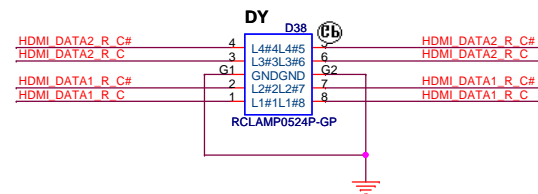
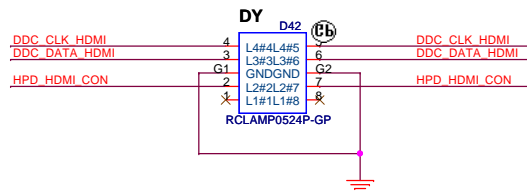
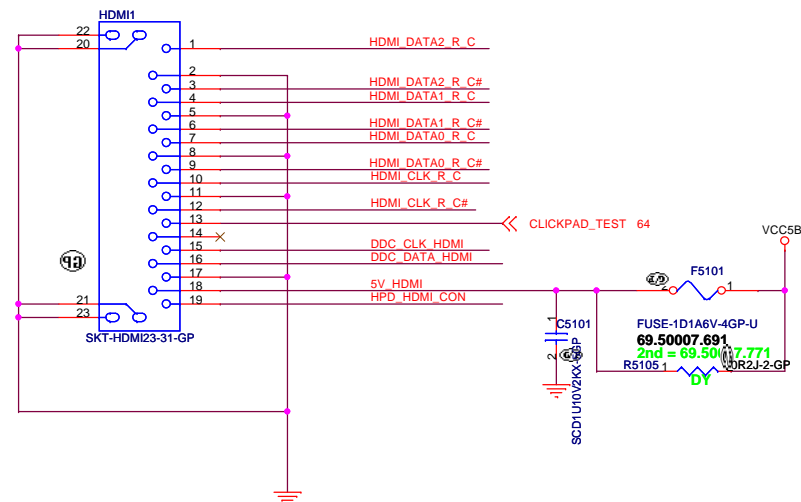


<Core Design>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title			
LPM LVDS Connector			
Size A4	Document Number NL-note		Rev SC
Date:	Monday, March 07, 2011	Sheet 34 of	101



HDMI CONN



<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title		
HDMI Port Connector		
Size	Document Number	Rev
A3	NL-note	SC
Date:	Monday, March 07, 2011	Sheet 35 of 101

5

4

3

2

1

D

C

B

A

<Core Design>

<div><div>緯創資通</div><div>Wistron Corporation</div><div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div></div>		
TitleBLANK		
SizeA4	Document NumberNL-note	RevSC
Date: Monday, March 07, 2011	Sheet 36 of	101

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size
A4

Document Number

Rev	SC
-----	----

NL-note

Date: Monday, March 07, 2011	Sheet 37 of 101
------------------------------	-----------------

BLANK

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

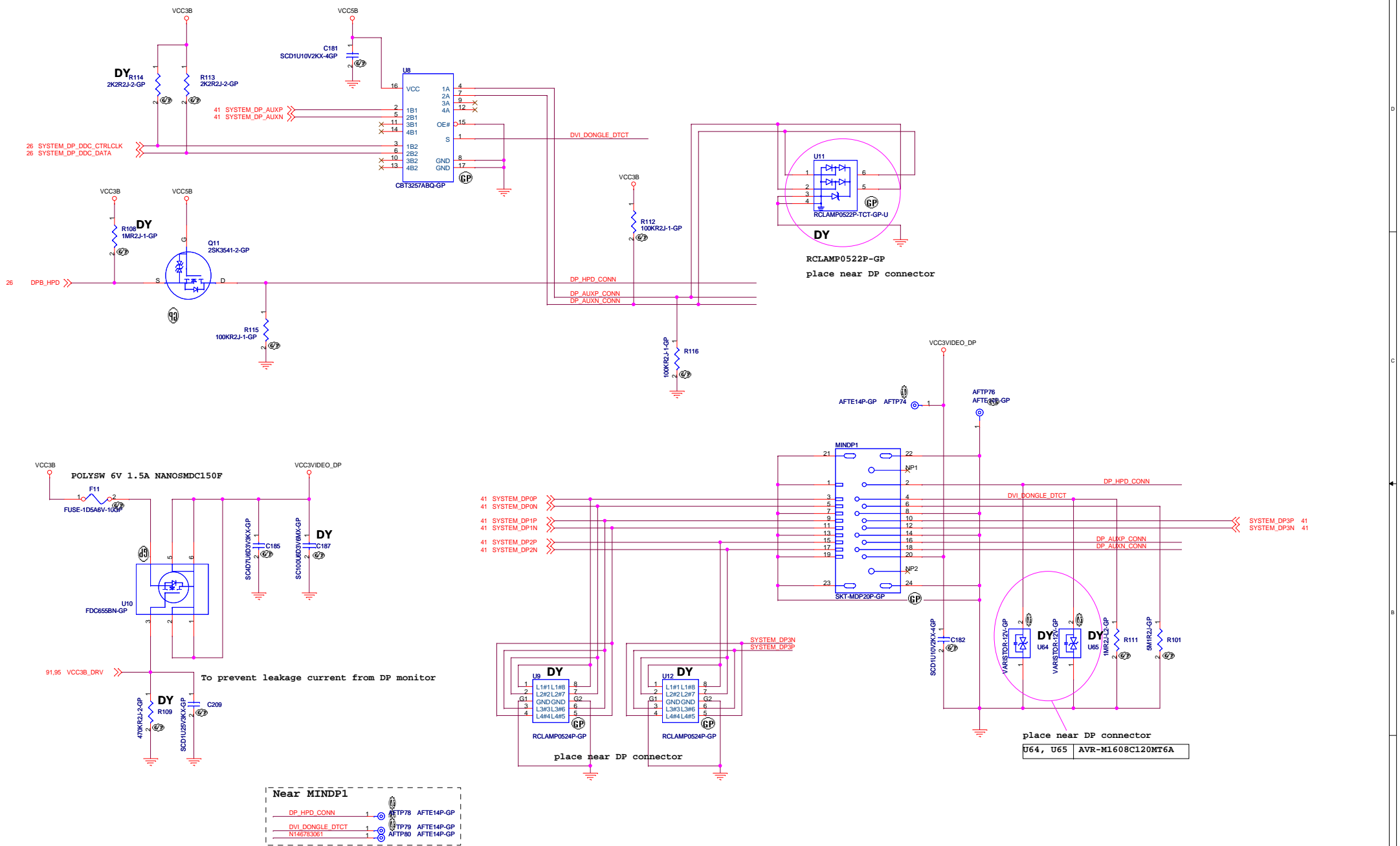
Size
A3

Document Number

Rev
SC

Date: Monday, March 07, 2011

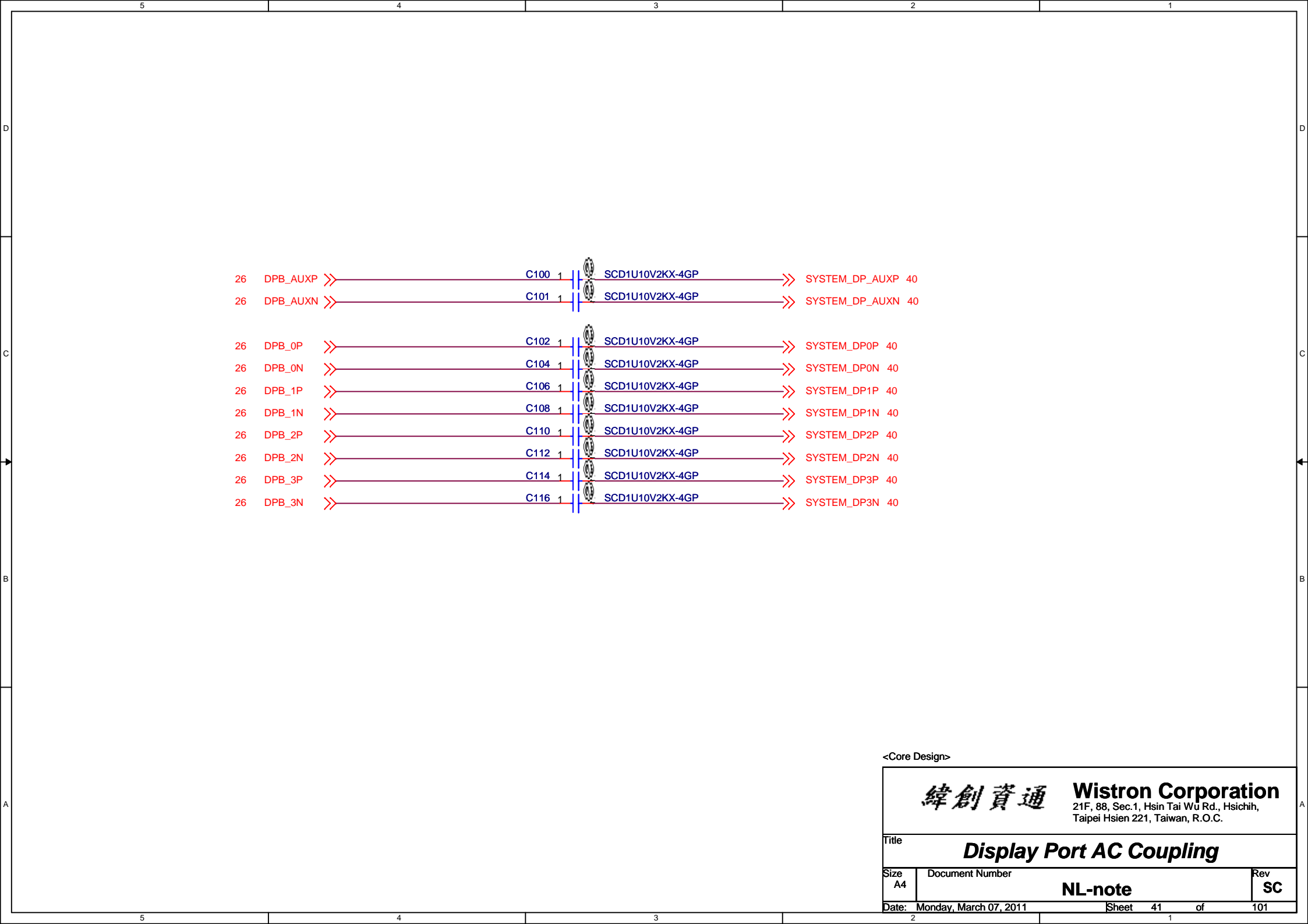
Sheet 38 of 101



<Core Design>

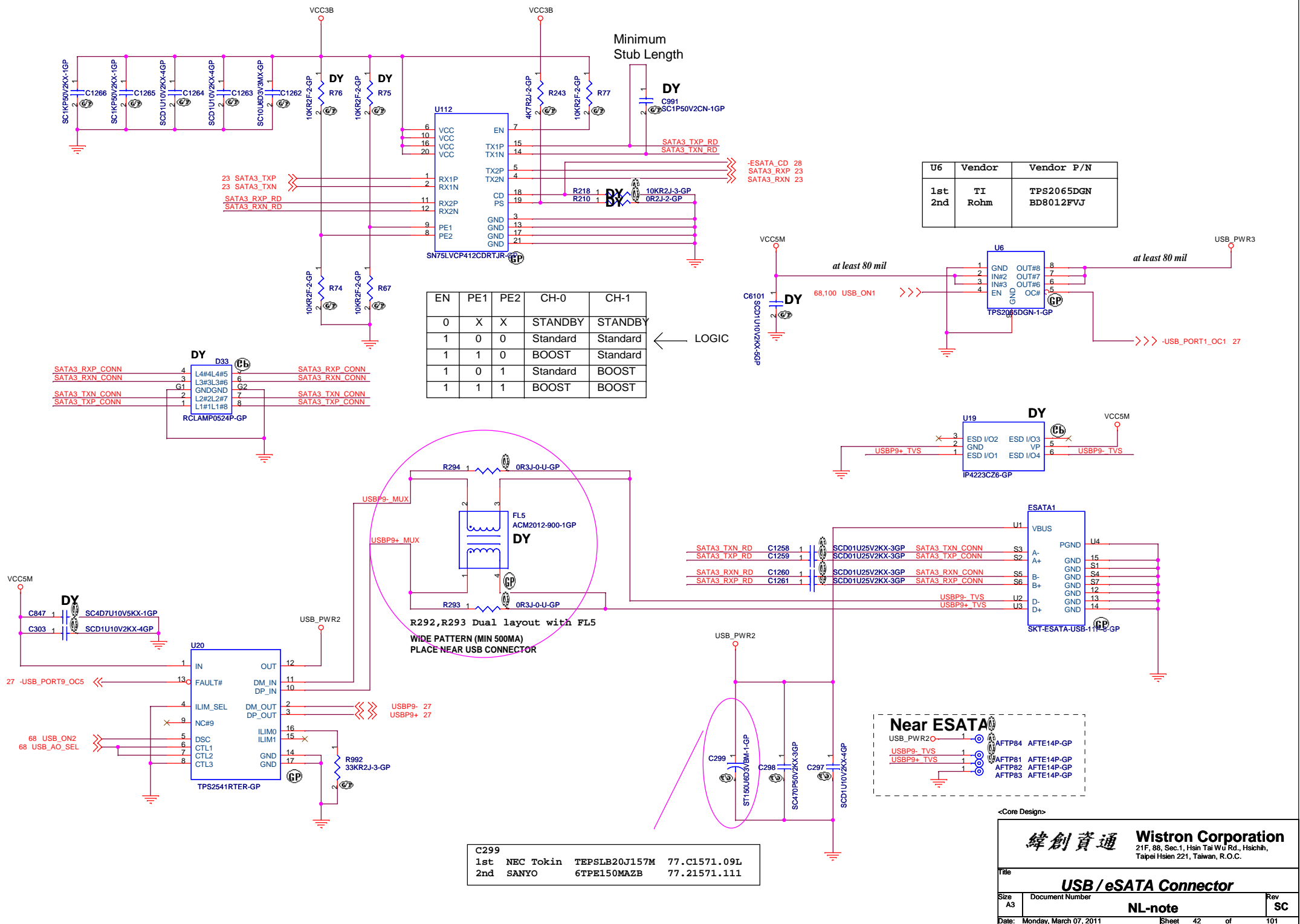
緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
Display Port Connector			
Size	Document Number		Rev
Custom	NL-note		SC
Date: Monday, March 07, 2011		Sheet 40 of	101

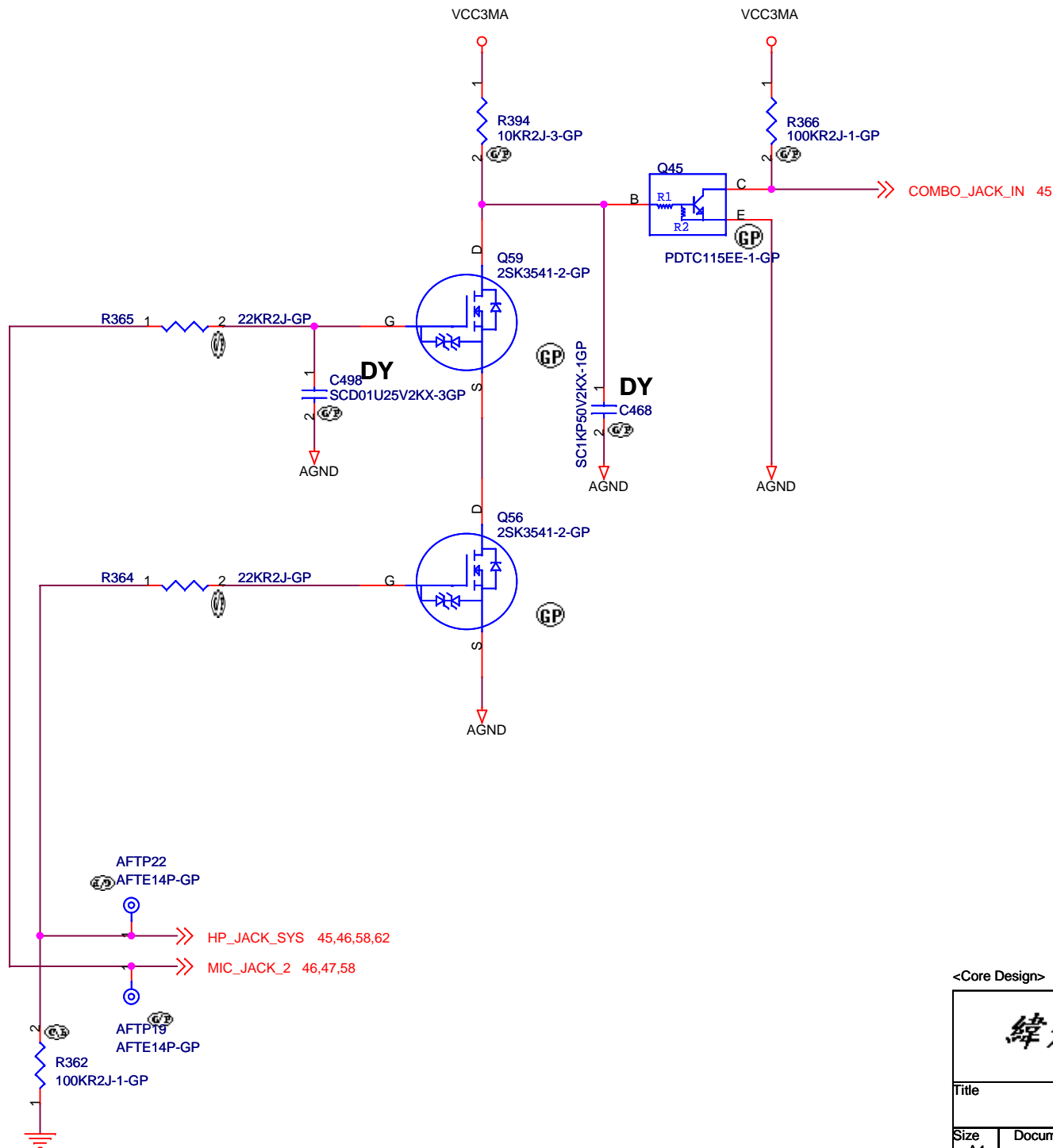


<Core Design>

<div><div>緯創資通</div><div>Wistron Corporation</div><div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div></div>	
Title	
Display Port AC Coupling	
Size	Document Number
A4	NL-note
Date	Rev
Monday, March 07, 2011	SC
Sheet 41 of 101	



<Core Design>



<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Audio Connector

Size
A4

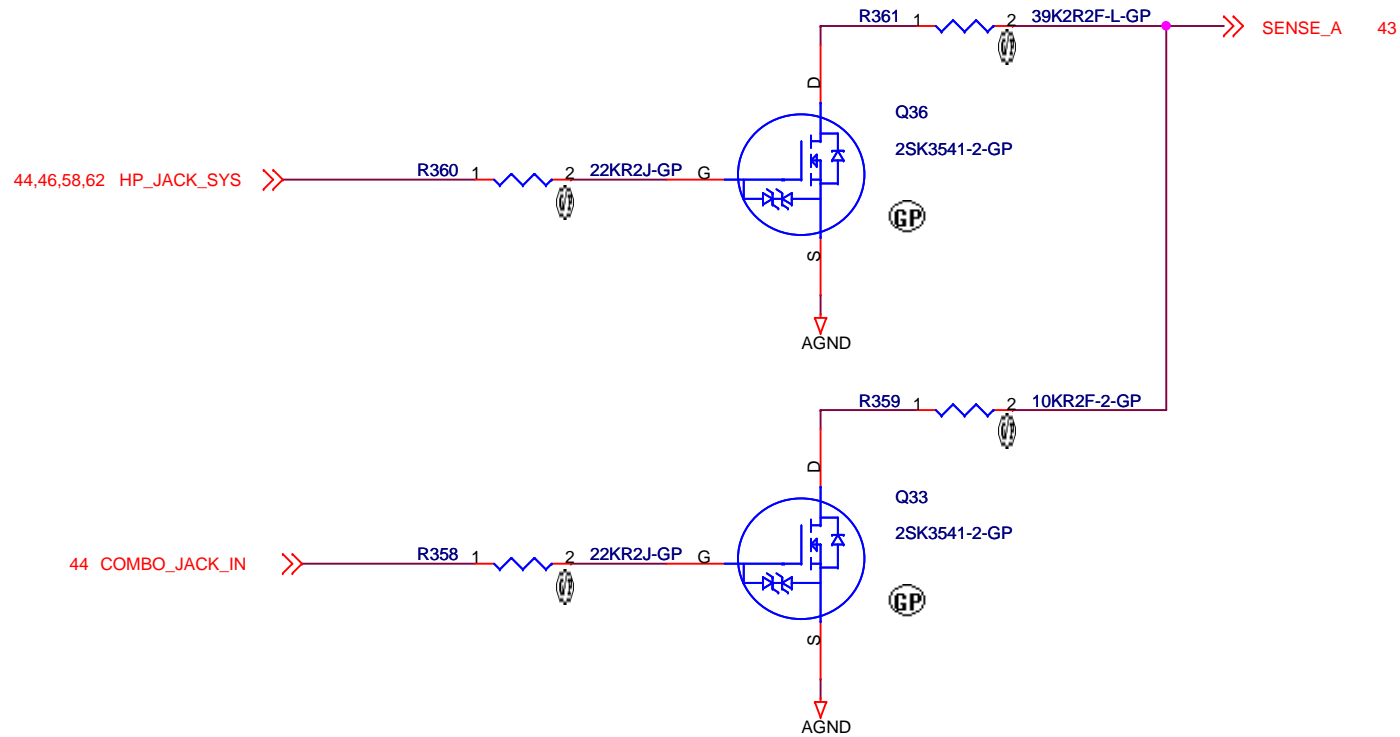
Document Number

NL-note

Rev
SC

Date: Monday, March 07, 2011

Sheet 44 of 101



<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Audio Jack Sense

Size
A4

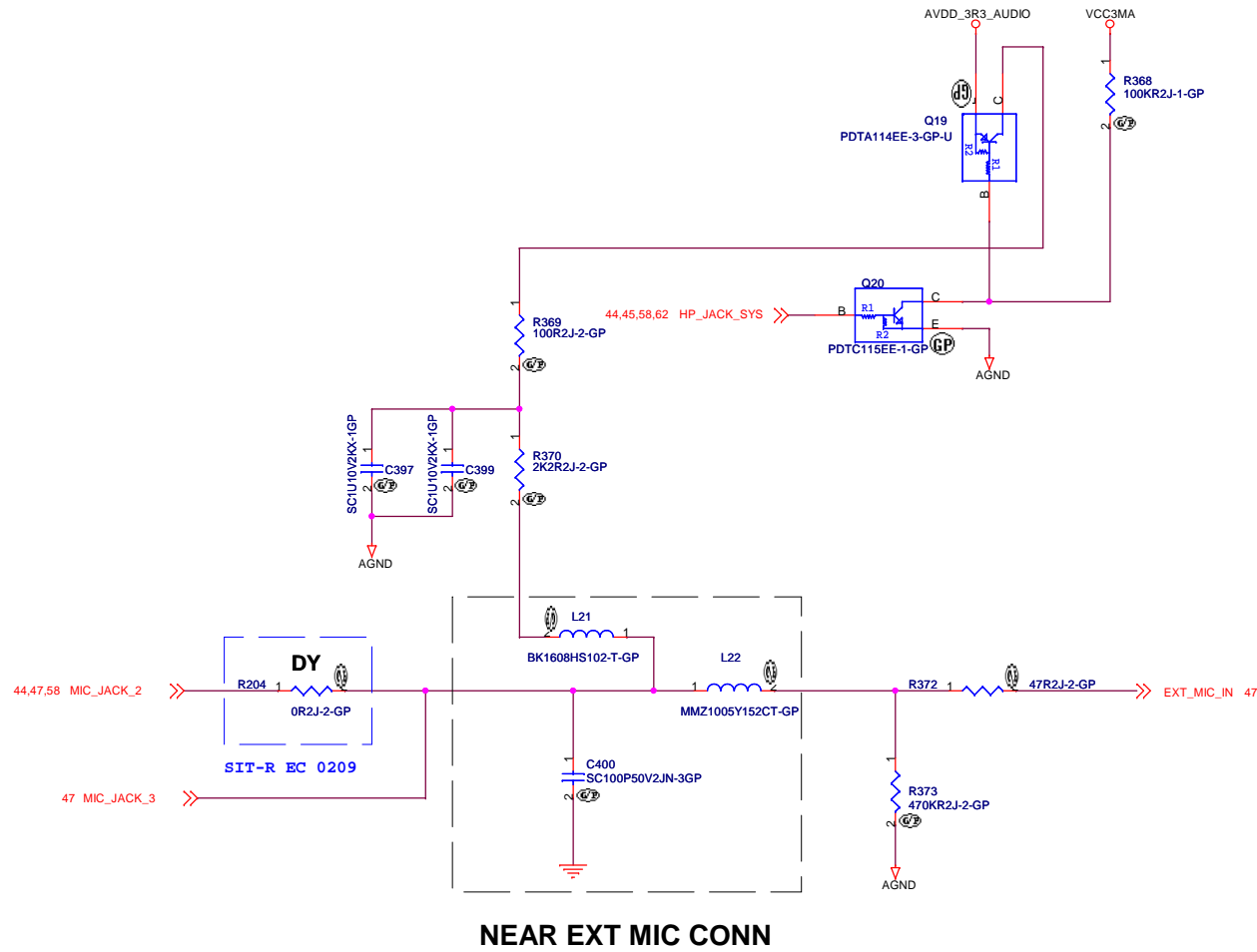
Document Number

NL-note

Rev
SC

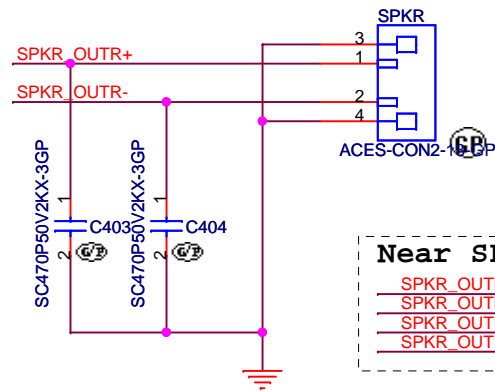
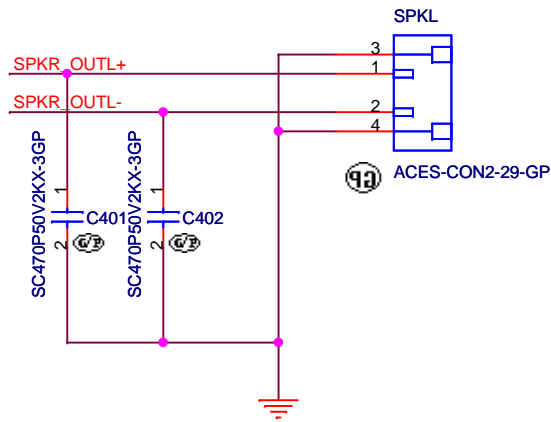
Date: Monday, March 07, 2011

Sheet 45 of 101



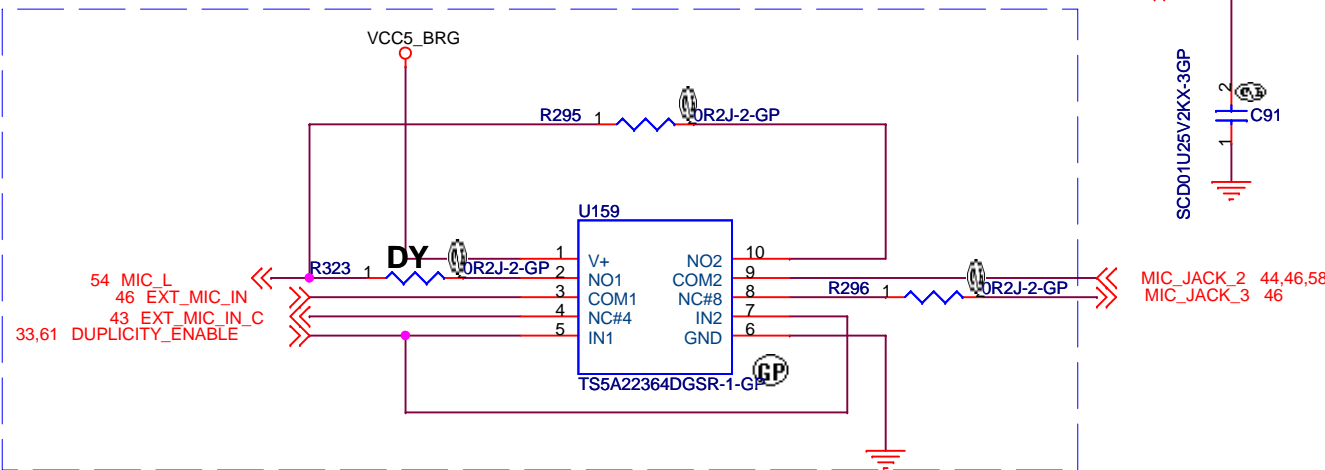
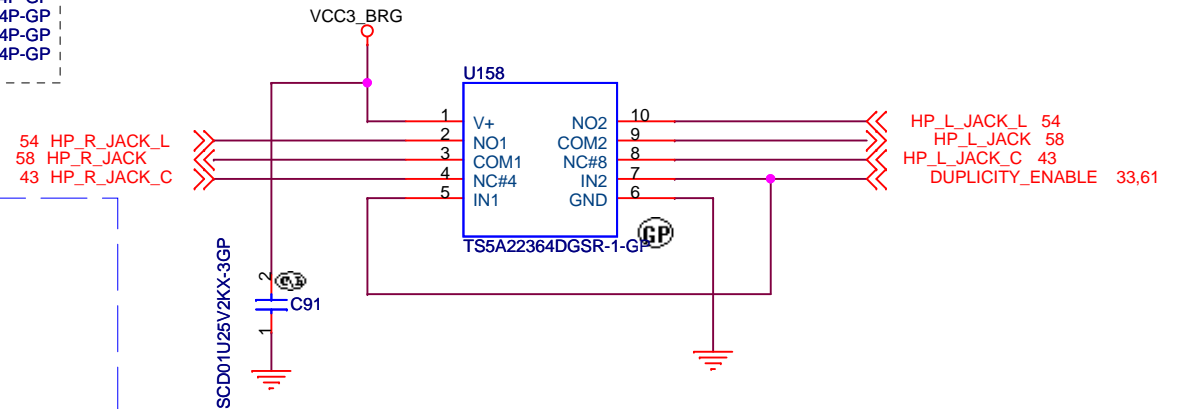
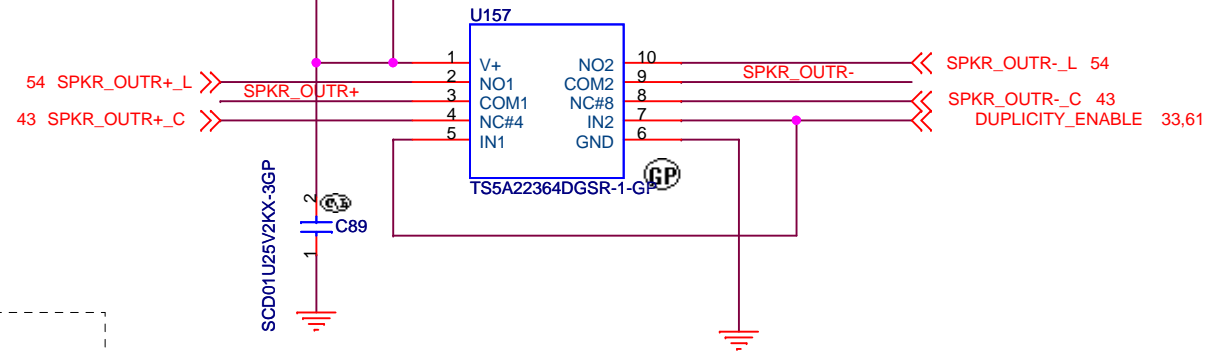
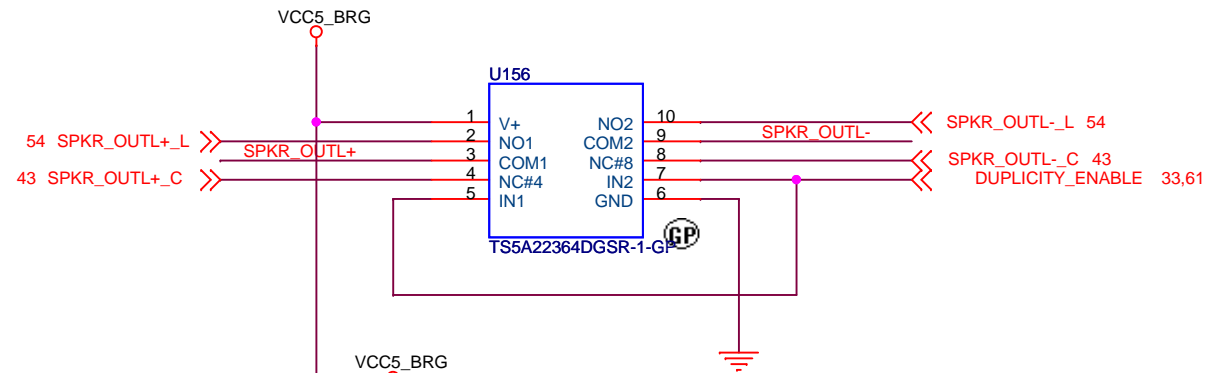
<Core Design>

緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title Audio Ext MIC I/F	
Size A3	Document Number NL-note Rev SC
Date: Monday, March 07, 2011	Sheet 46 of 101



Near SPKL and SPKR

SPKR_OUTL+	1	AFTP92	AFTE14P-GP
SPKR_OUTL-	1	AFTP93	AFTE14P-GP
SPKR_OUTR-	1	AFTP94	AFTE14P-GP
SPKR_OUTR+	1	AFTP95	AFTE14P-GP



SIT-R EC 0209

<Core Design>

緯創資通

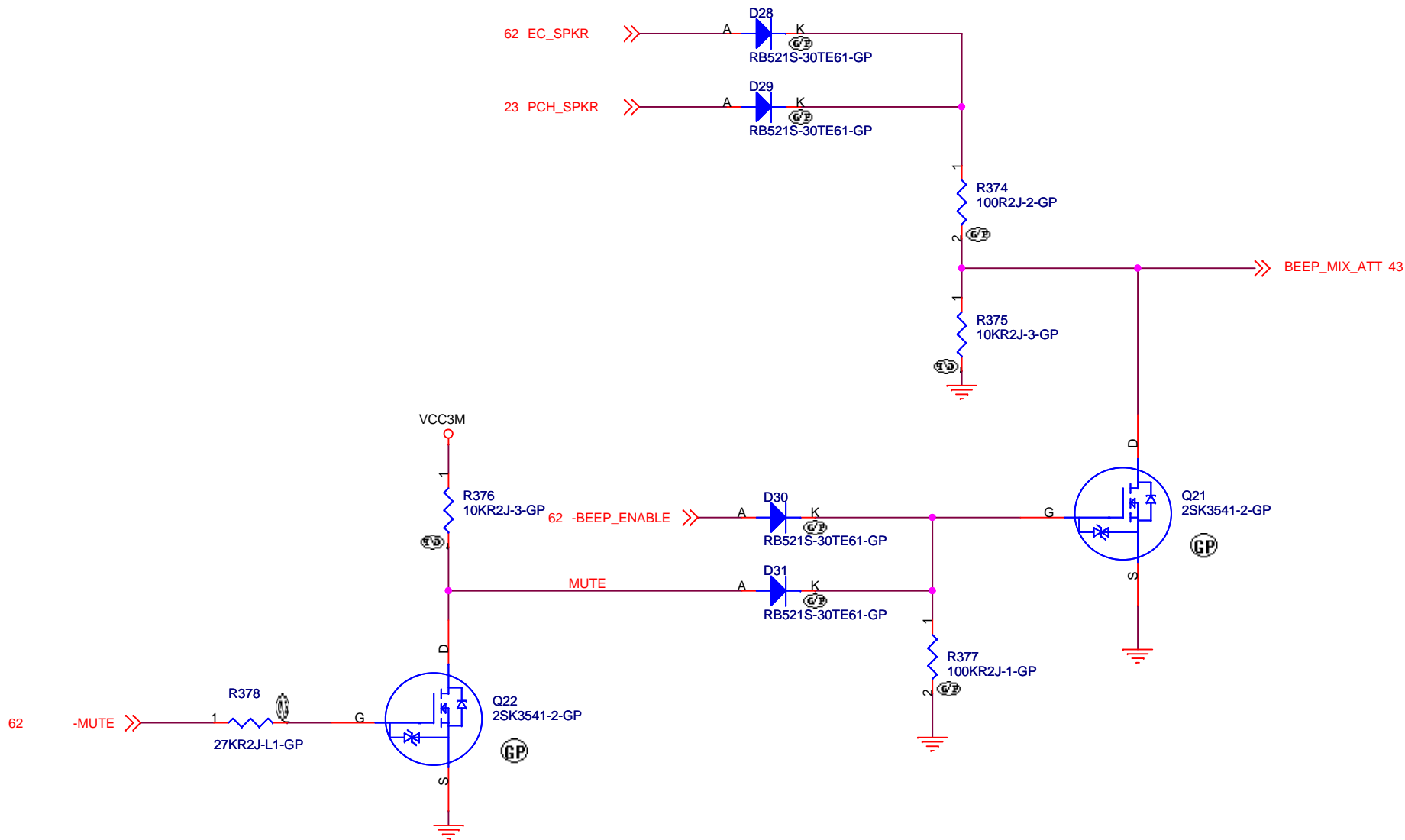
Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title **AUDIO SPEAKER**

Size A4	Document Number NL-note	Rev SC
------------	-----------------------------------	------------------

Date: Monday, March 07, 2011 Sheet 47 of 101



<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Audio BEEP

Size
A4

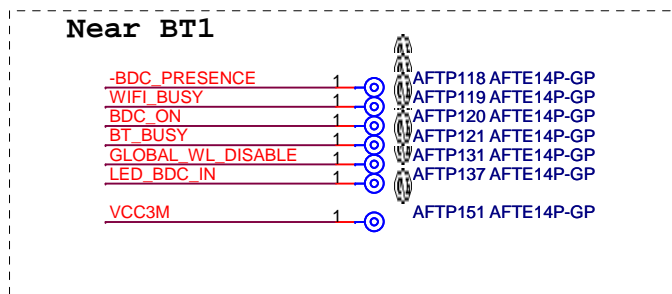
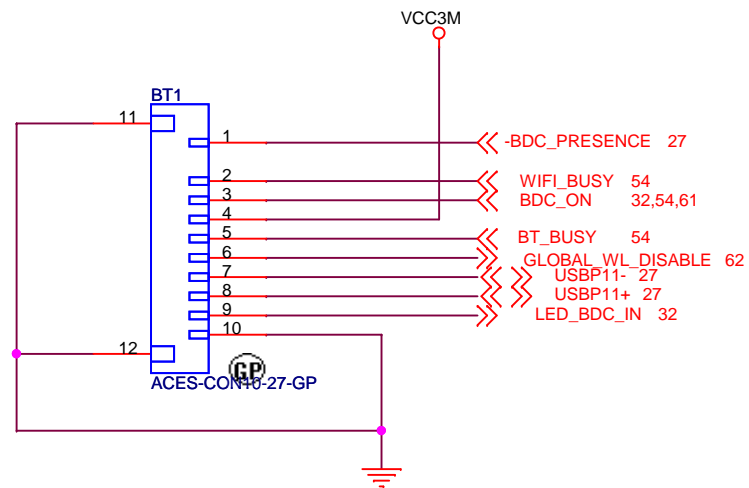
Document Number

NL-note


Rev
SC

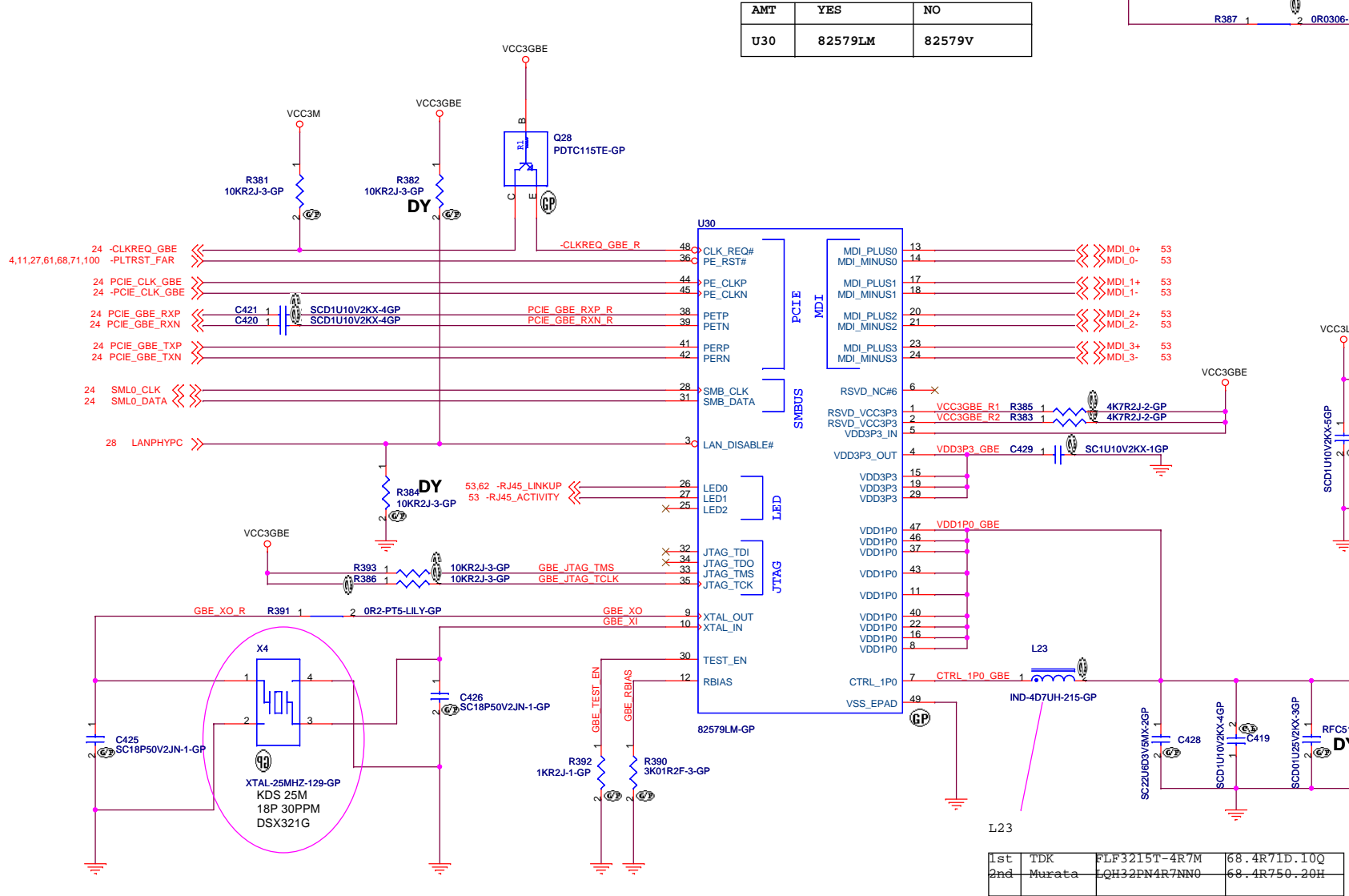
Date: Monday, March 07, 2011

Sheet 48 of 101



<Core Design>

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title BT & WLAN SW			
Size A4	Document Number NL-note		Rev SC
Date: Monday, March 07, 2011		Sheet 49 of 101	



AMT	YES	NO
U30	82579LM	82579V

1st	TDK	FLF3215T-4R7M	68.4R71D.10Q
2nd	Murata	LQH32PN4R7NN0	68.4R750.20H

X4			
1st	KDS	DSX321G 25M 18P 30PPM	82.30020.B11
2nd	H.ELE	HSX321S 25M 18P 30PPM	82.30020.B21
3rd	TXC	7V25020001 25M 18P 30PPM	41U6141AA

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

GBE Lewisville

Size

A3

Document Number

NL-note

Rev

SC

Date

Monday, March 07, 2011

Sheet

50

of

101

	A	B	C	D	E
4					
3					
2					
1					
	A	B	C	D	E

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size A4	Document Number	Rev SC
------------	-----------------	-----------

D

D

C

C

B

B

A

A

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	
-------	--

BLANK

Size

Size	Document Number
------	-----------------

Rev

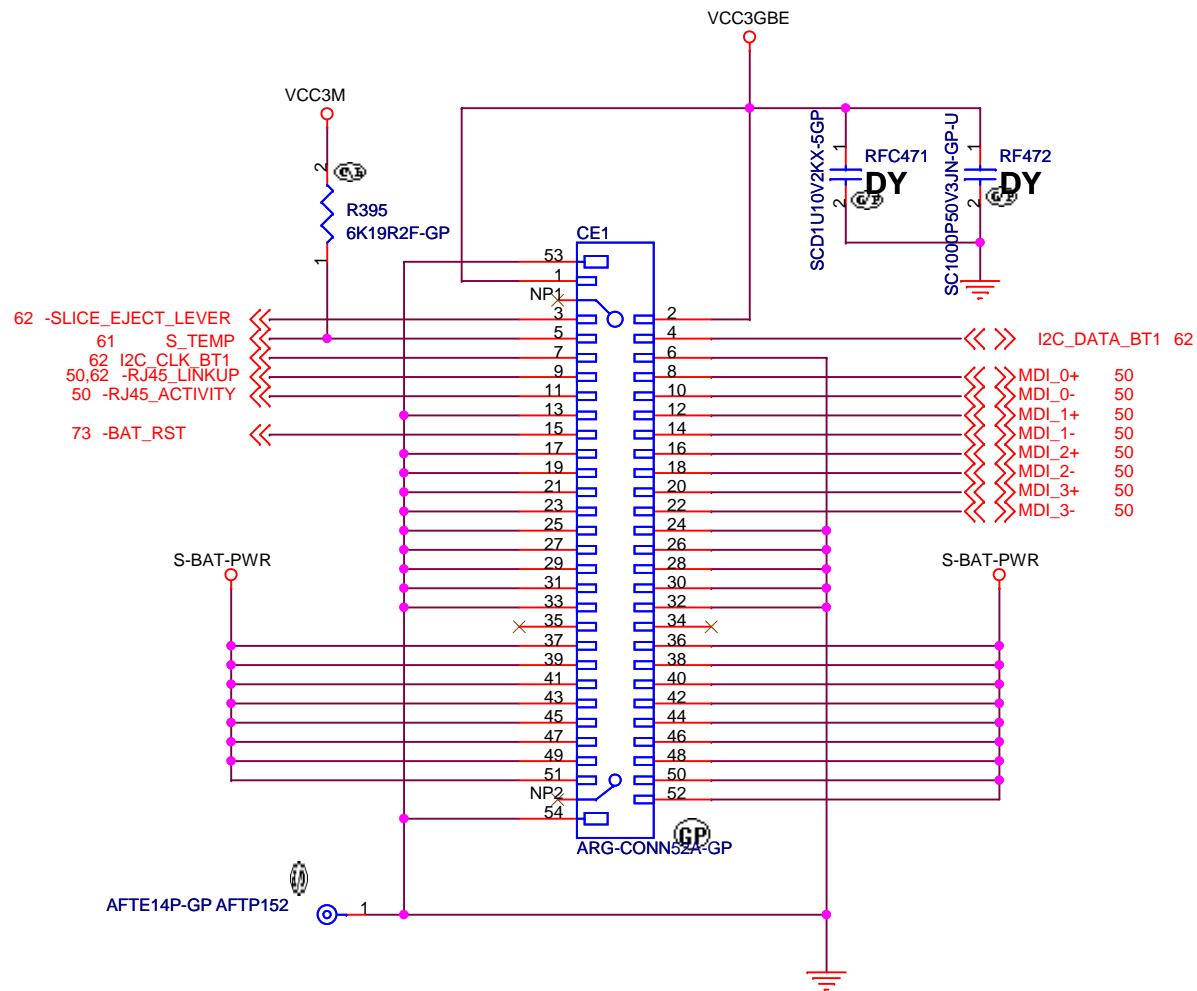
Size	L
Custom	

NL-note

SC

Date: Monday, March 07, 2011

Sheet	52	of	101
-------	----	----	-----



Near CE1

-SLICE_EJECT_LEVER	1	AFTE145 AFTE14P-GP
-RJ45_LINKUP	1	AFTE146 AFTE14P-GP
-RJ45_ACTIVITY	1	AFTE147 AFTE14P-GP
-BAT_RST	1	AFTE148 AFTE14P-GP
VCC3GBE	1	AFTE149 AFTE14P-GP
S-BAT-PWR	1	AFTE150 AFTE14P-GP

<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

RJ45 CONNECTOR

Size
A4

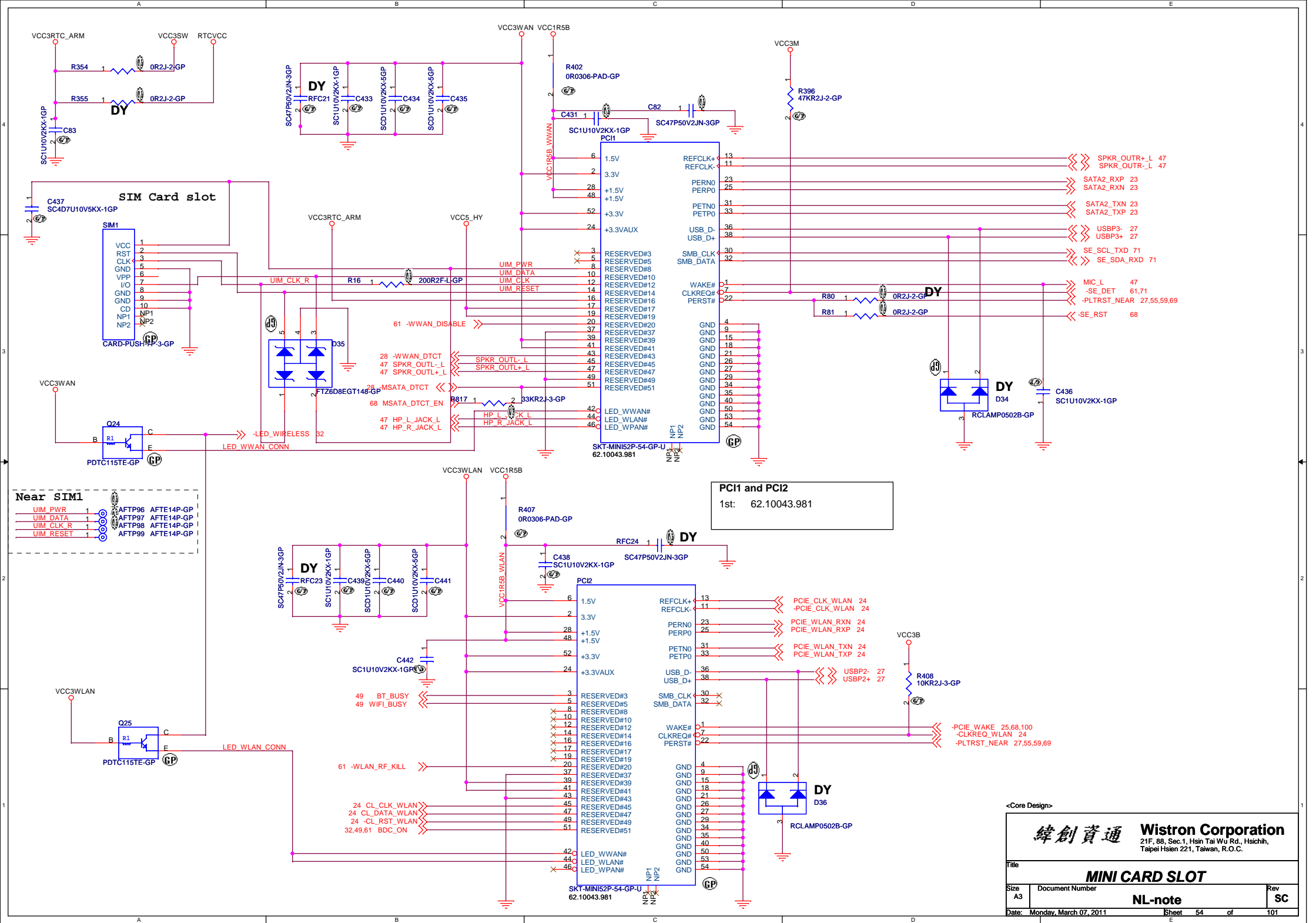
Document Number

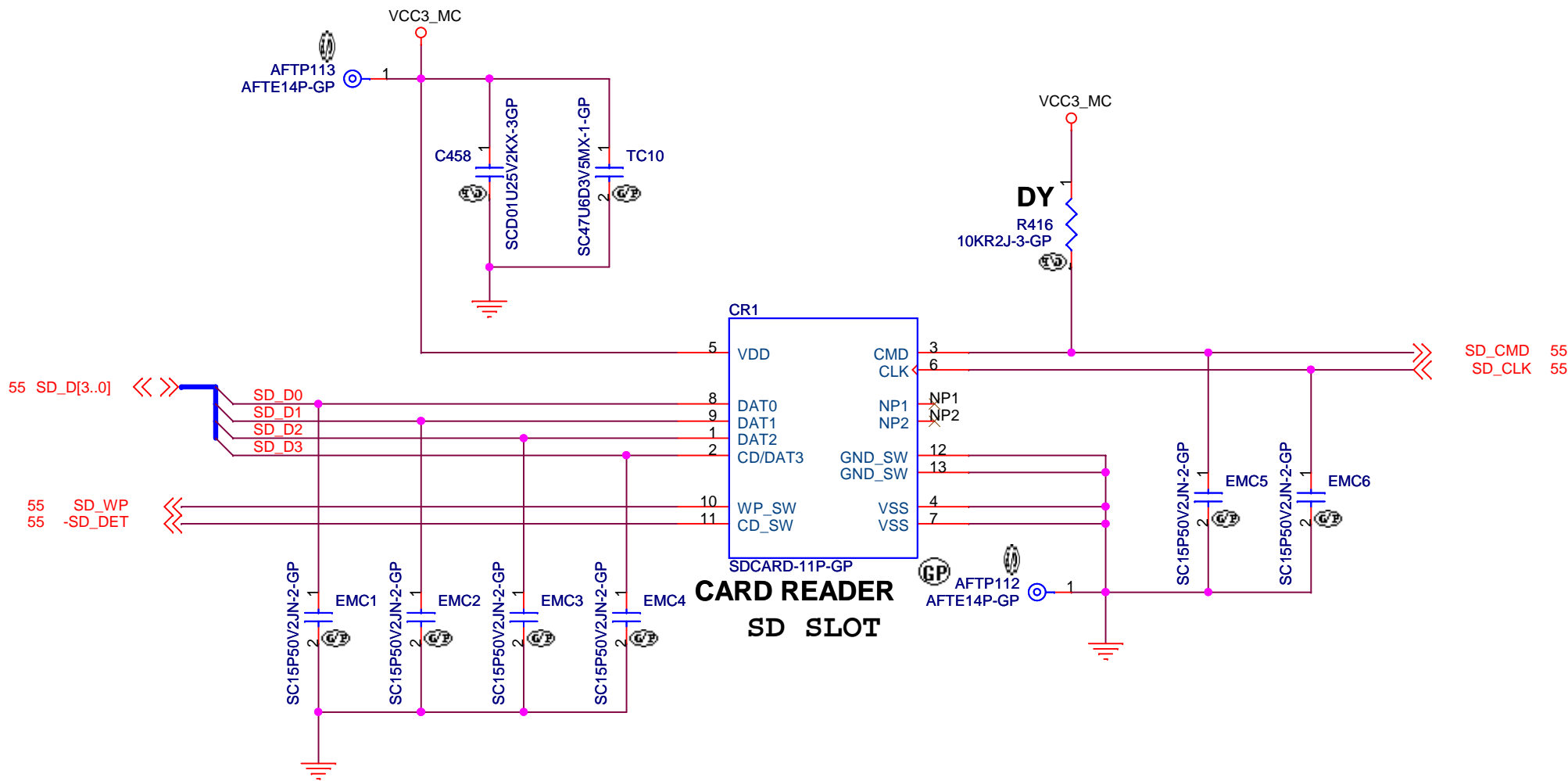
NL-note

Rev
SC

Date: Monday, March 07, 2011

Sheet 53 of 101



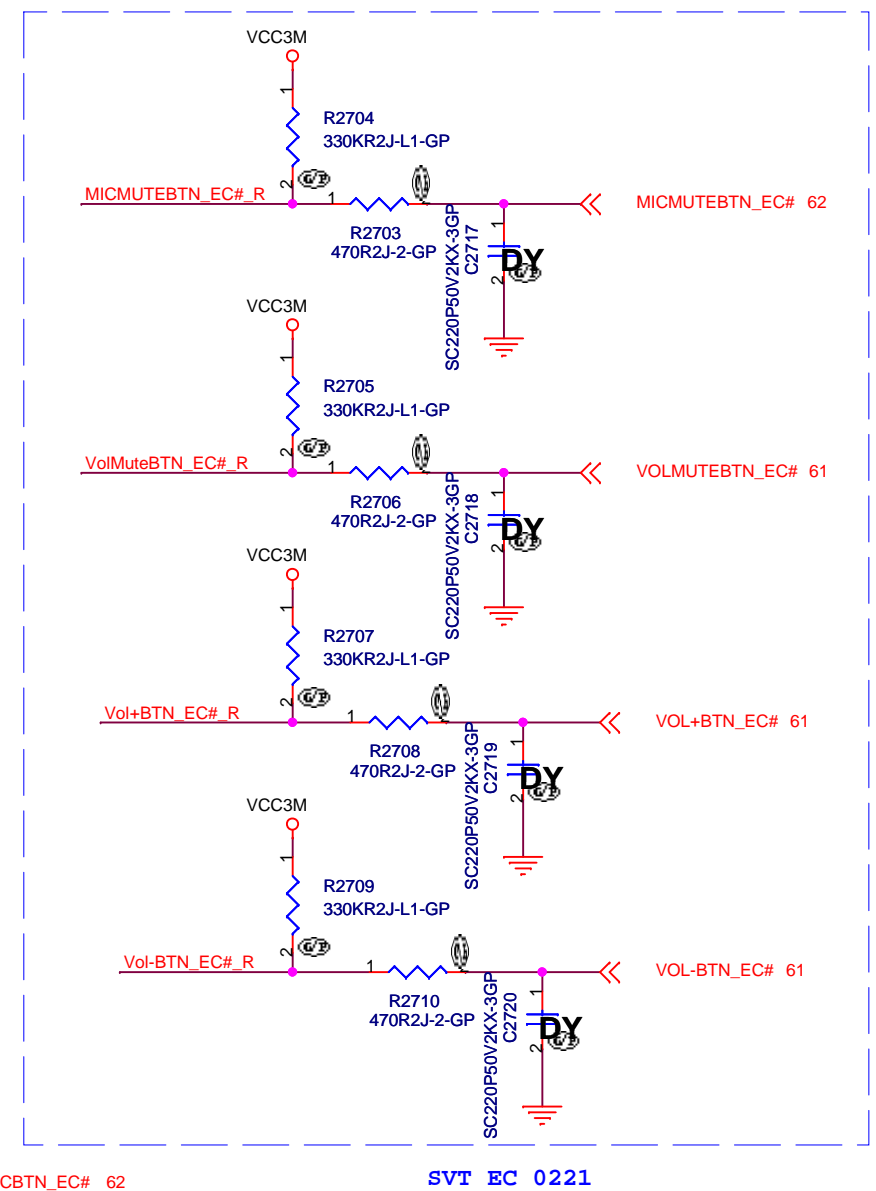
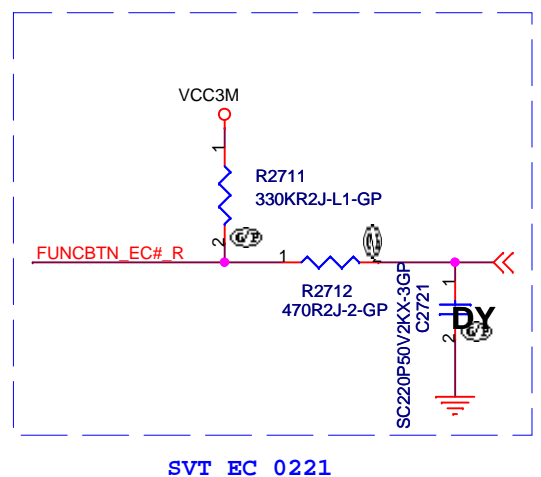
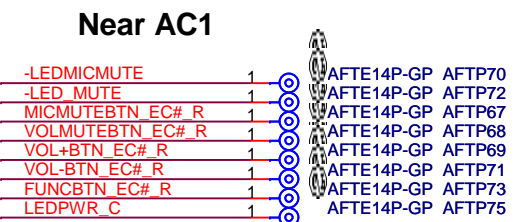
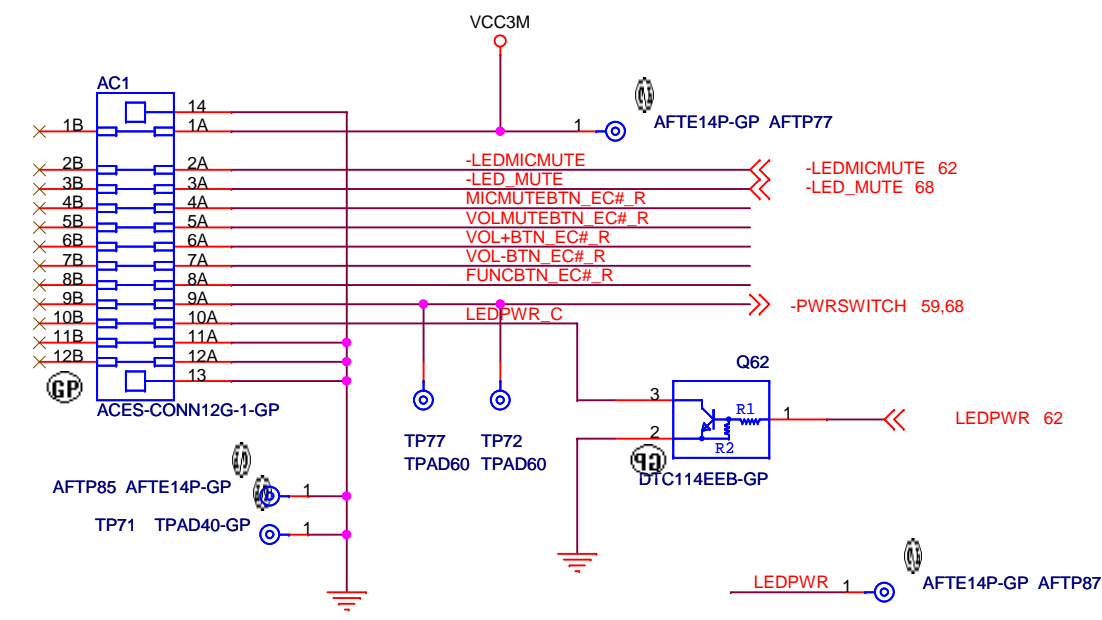


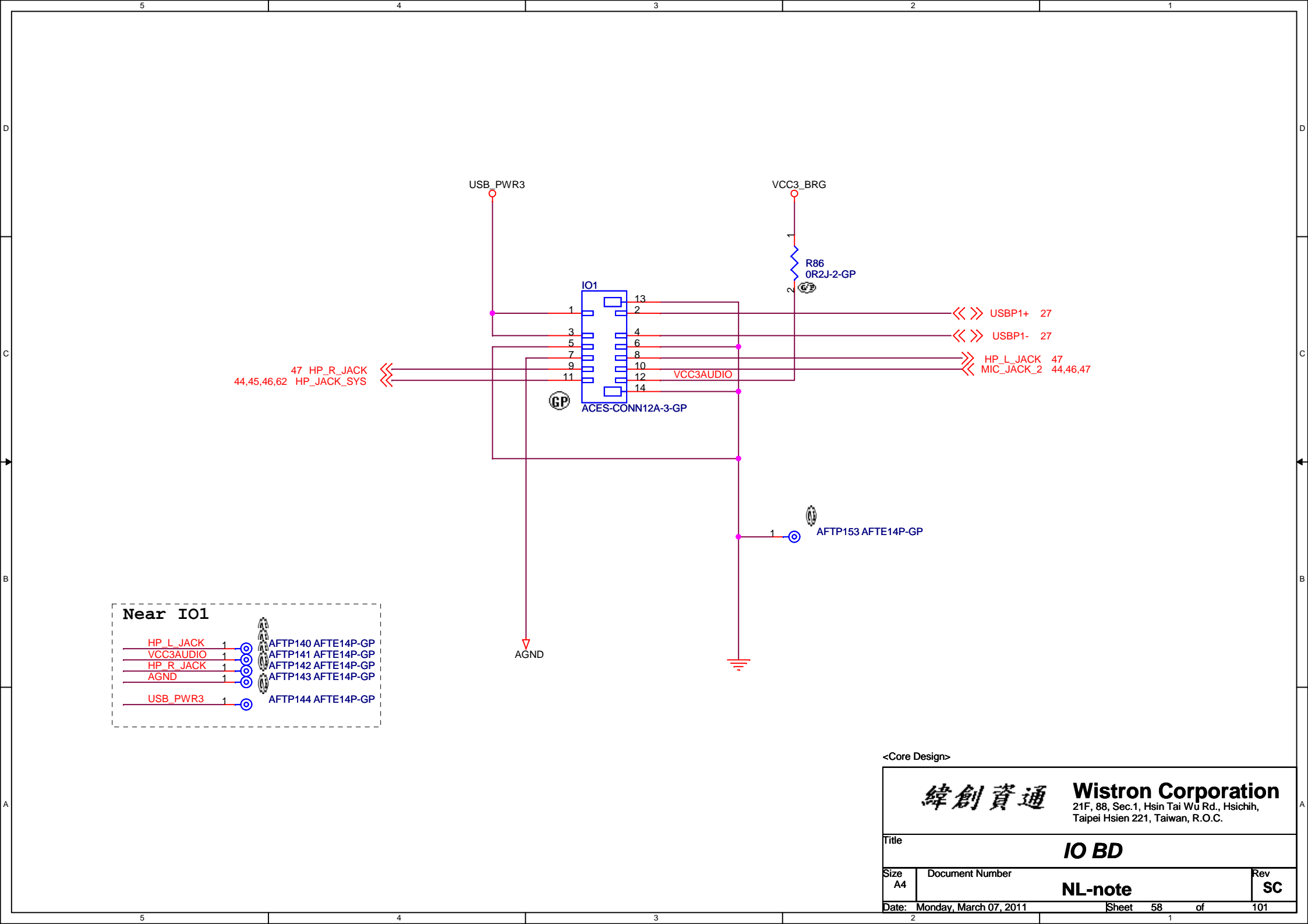
Near CR1

SD_D0	1	AFTP104	AFTE14P-GP
SD_D1	1	AFTP105	AFTE14P-GP
SD_D2	1	AFTP106	AFTE14P-GP
SD_D3	1	AFTP107	AFTE14P-GP
SD_CMD	1	AFTP108	AFTE14P-GP
SD_CLK	1	AFTP109	AFTE14P-GP
-SD_DET	1	AFTP110	AFTE14P-GP
SD_WP	1	AFTP111	AFTE14P-GP

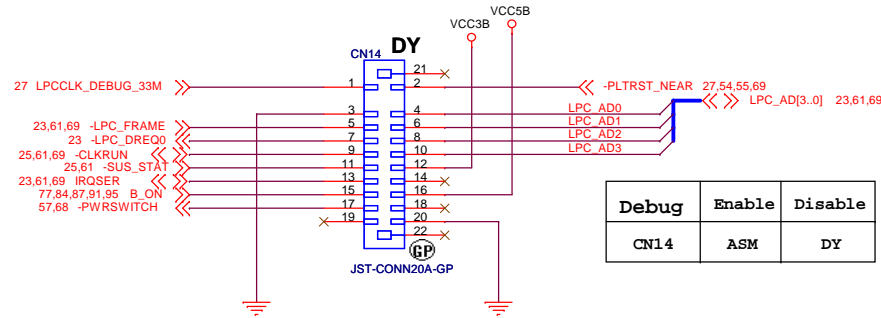
<Core Design>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>			
Title			
Media Card Slot			
Size A	Document Number NL-note		Rev SC
Date:	Monday, March 07, 2011	Sheet 56 of	101



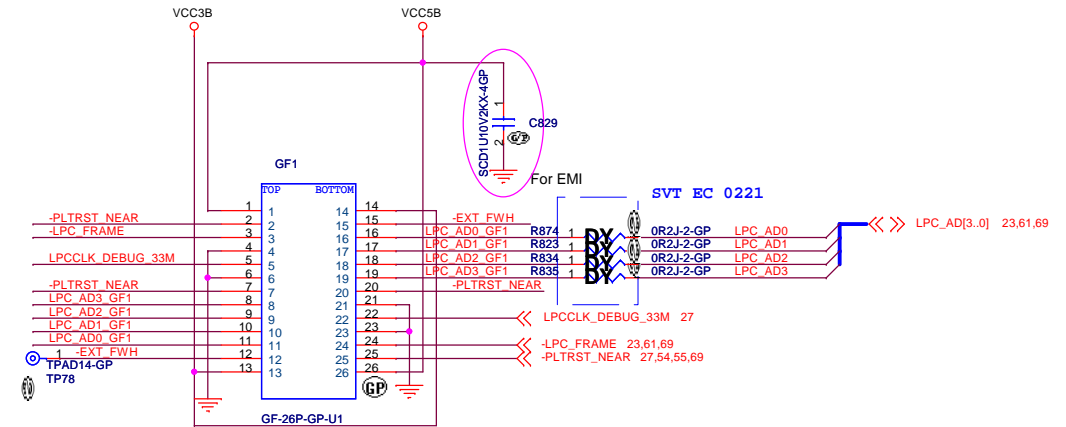


Debug card connector



Put "easy-to -access" place

Golden Finger for Debug Board



(BOTTOM VIEW)
TOP VIEW

(26) (25)....(15) (14)

13 12 2 1

GF1

YES

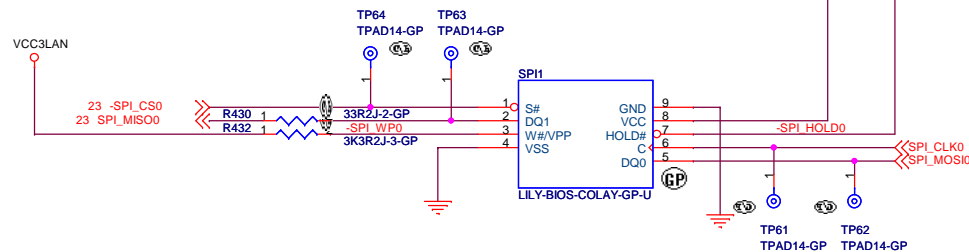
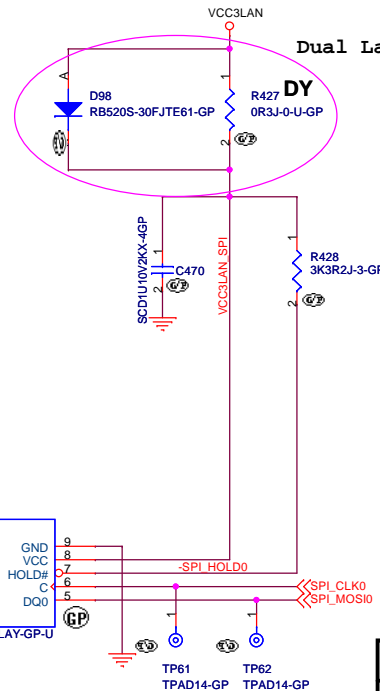
NO

	YES	NO
R874	ASM	DY
R823	ASM	DY
R834	ASM	DY
R835	ASM	DY
R235	ASM	DY

64Mbit SPI FLASH (for vPRO model)

SO8		
Marconix	MX25L6436EM2I-10G	72.25643.001
Marconix	MX25L6406EM2I-10G	
Winbond	W25Q64CVSSIG	72.25Q64.B01

Dual Layout



SF 100 PIN HEADER INTERFACE (Top View)

1	VCC	R427.2	GND	GND	2
3	CS#	R148.1	R145.1	CLK	4
5	MISO	R430.2	R151.1	MOSI	6
7	(KEY)	N/A	N/A	(RESET)	8

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

SPI&LPC debug card

Size

Document Number

NL-note

Rev

SC

Date: Monday, March 07, 2011

Sheet 59 of 101

BLANK

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size
A4

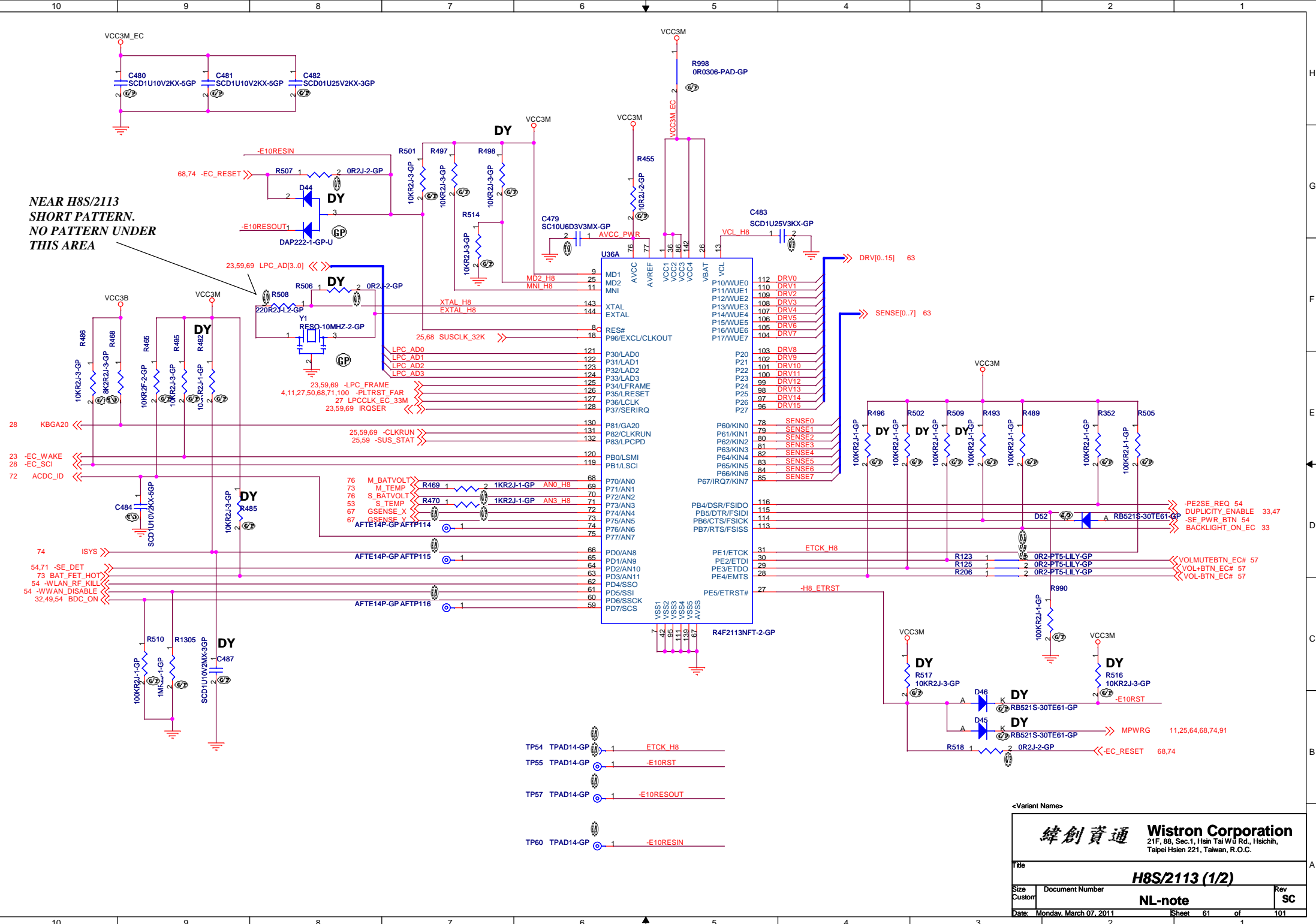
Document Number

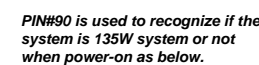
NL-note

Rev
SC

Date: Monday, March 07, 2011

Sheet 60 of 101

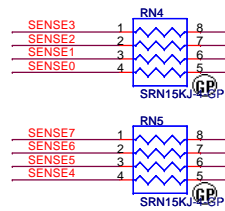
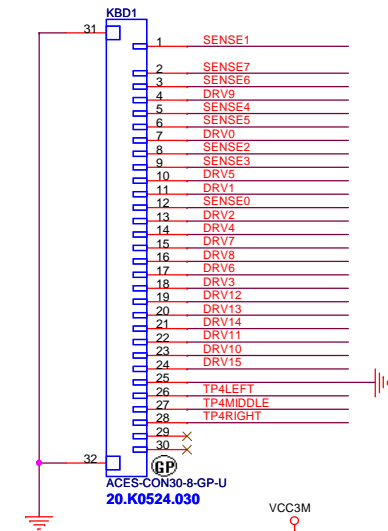




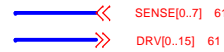
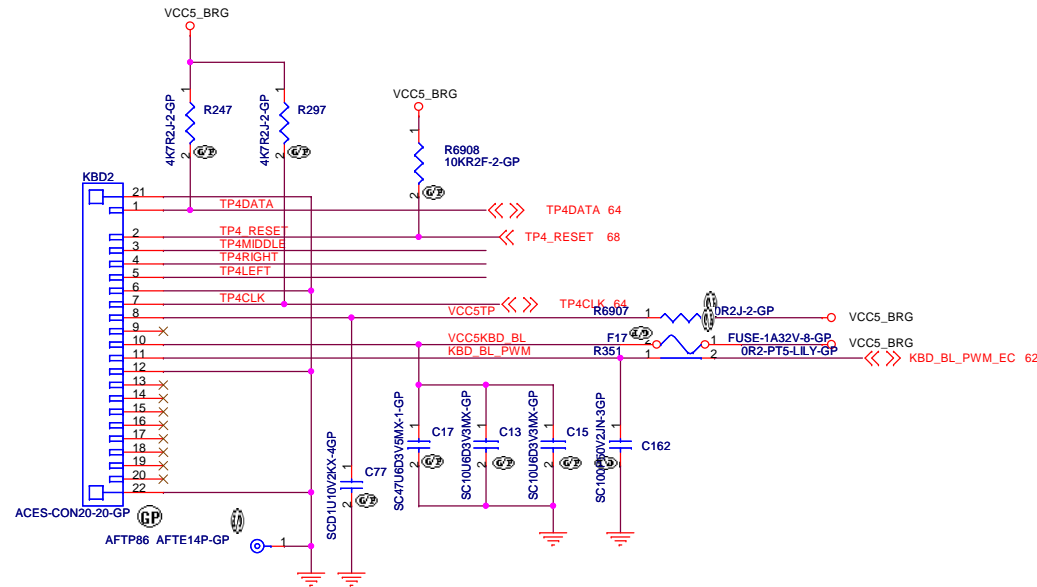
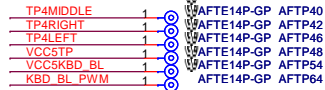
E10A DEBUG I/F	ENABLE	DISABLE
R506 R515	NO ASM NO_ASM	ASM ASM
D47 D48 D49 D49B R513 R514	ASM ASM ASM ASM ASM ASM	NO_ASM NO_ASM NO_ASM ASM NO_ASM NO_ASM
R511	ASM NO_ASM	NO_ASM ASM
R660 R509	ASM NO_ASM	NO_ASM ASM
R967 R968 R969	NO_ASM NO_ASM NO_ASM	ASM ASM ASM

Keyboard Connector

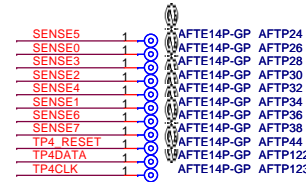
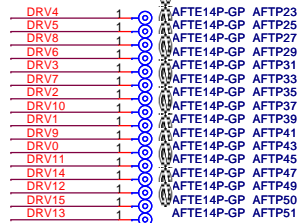
Track Point And Keyboard Backlight Connector



Near KBD2



Near KBD1

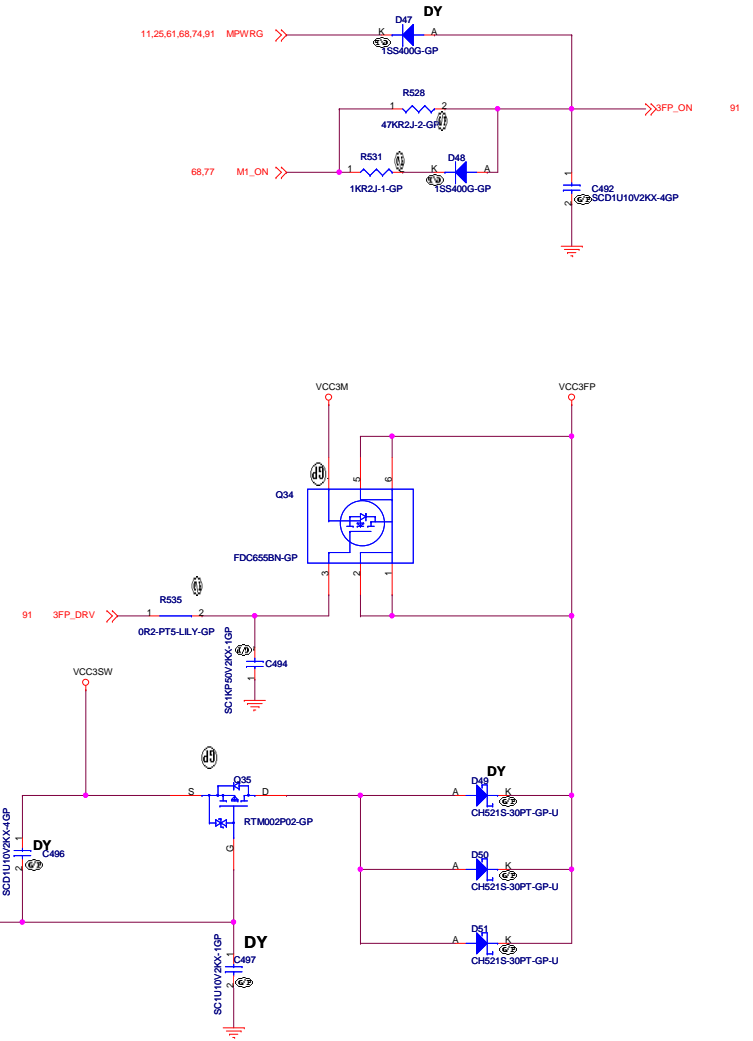
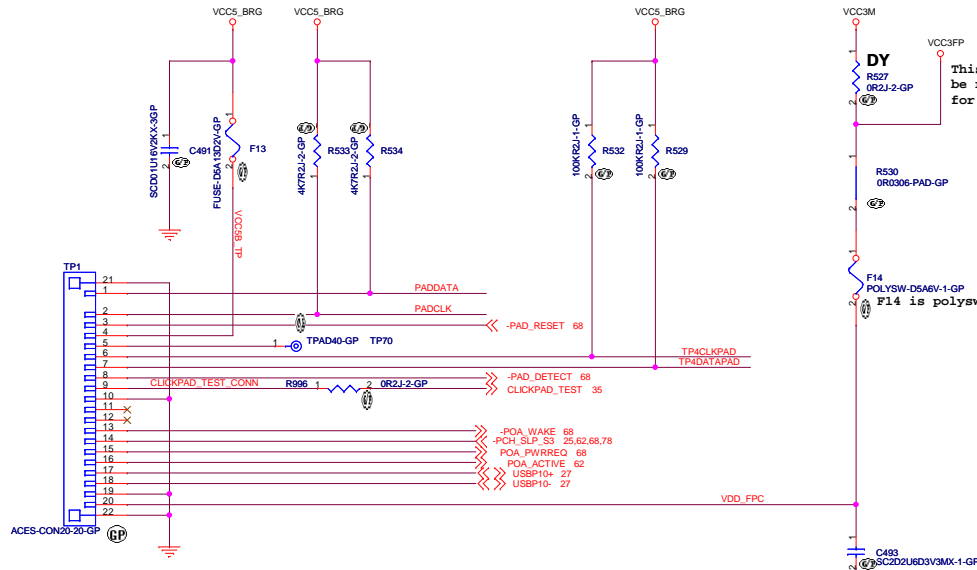


<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
KEYBOARD CONNECTOR			
Size A3	Document Number	Rev SC	
Date: Monday, March 07, 2011	Sheet 63	of	101

Fingerprint Reader / Touch PAD



Near TP1

PADATA	1	AFTE14P-GP	AFTP124
PADCLK	1	AFTE14P-GP	AFTP125
-PAD_RESET	1	AFTE14P-GP	AFTP126
VCC5B_TP	1	AFTE14P-GP	AFTP127
TP4CLKPAD	1	AFTE14P-GP	AFTP128
TP4DATA	1	AFTE14P-GP	AFTP129
-PAD_DETECT	1	AFTE14P-GP	AFTP130
-PAD_WAKE	1	AFTE14P-GP	AFTP131
-POH_SLP_S3	1	AFTE14P-GP	AFTP132
POA_PWRREQ	1	AFTE14P-GP	AFTP133
POA_ACTIVE	1	AFTE14P-GP	AFTP134
VDD_FPC	1	AFTE14P-GP	AFTP135
CLICKPAD_TEST_CONN	1	AFTE14P-GP	AFTP136

<Core Design>

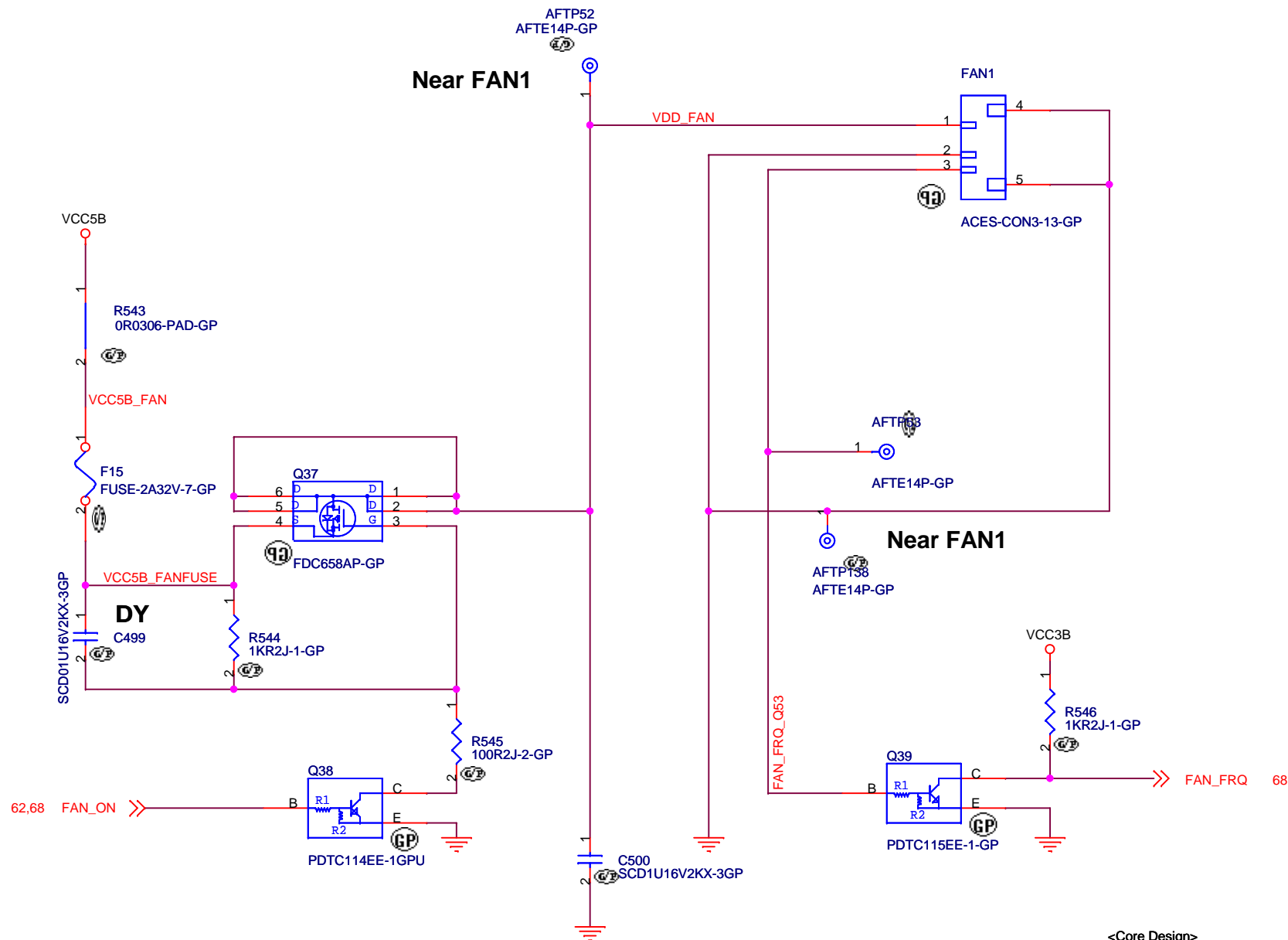
緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	TOUCH PAD CONNECTOR		
Size	Document Number	NL-note	Rev SC
Custom			
Date:	Monday, March 07, 2011	Sheet 64	of 101

BLANK

<Core Design>

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title BLANK			
Size A	Document Number NL-note		Rev SC
Date: Monday, March 07, 2011		Sheet 65 of	101



<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

FAN Control

Size
A4

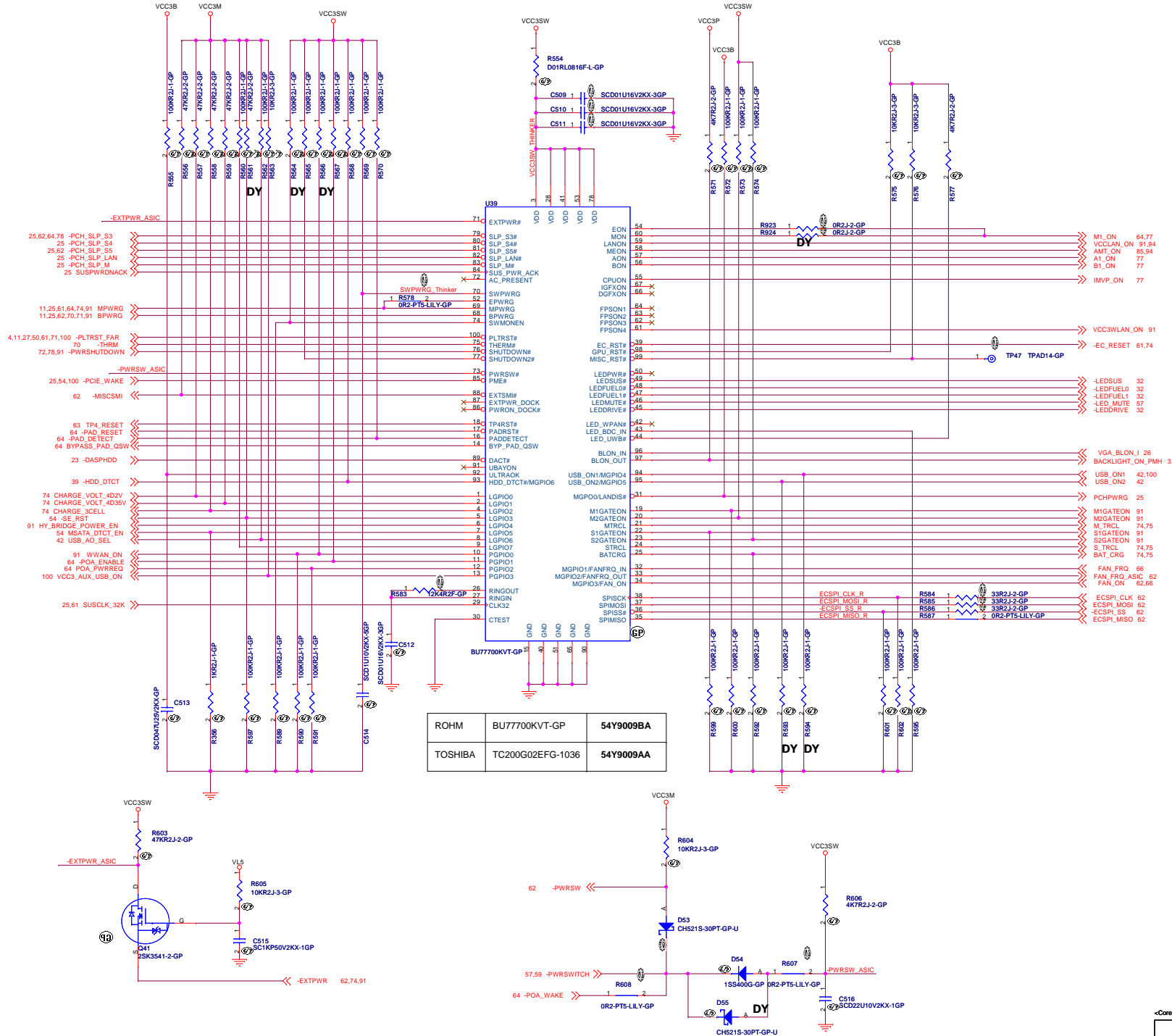
Document Number

NL-note

Rev
SC

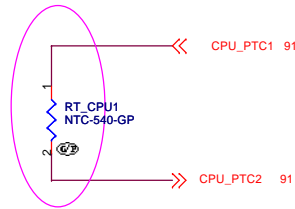
Date: Monday, March 07, 2011

Sheet 66 of 101



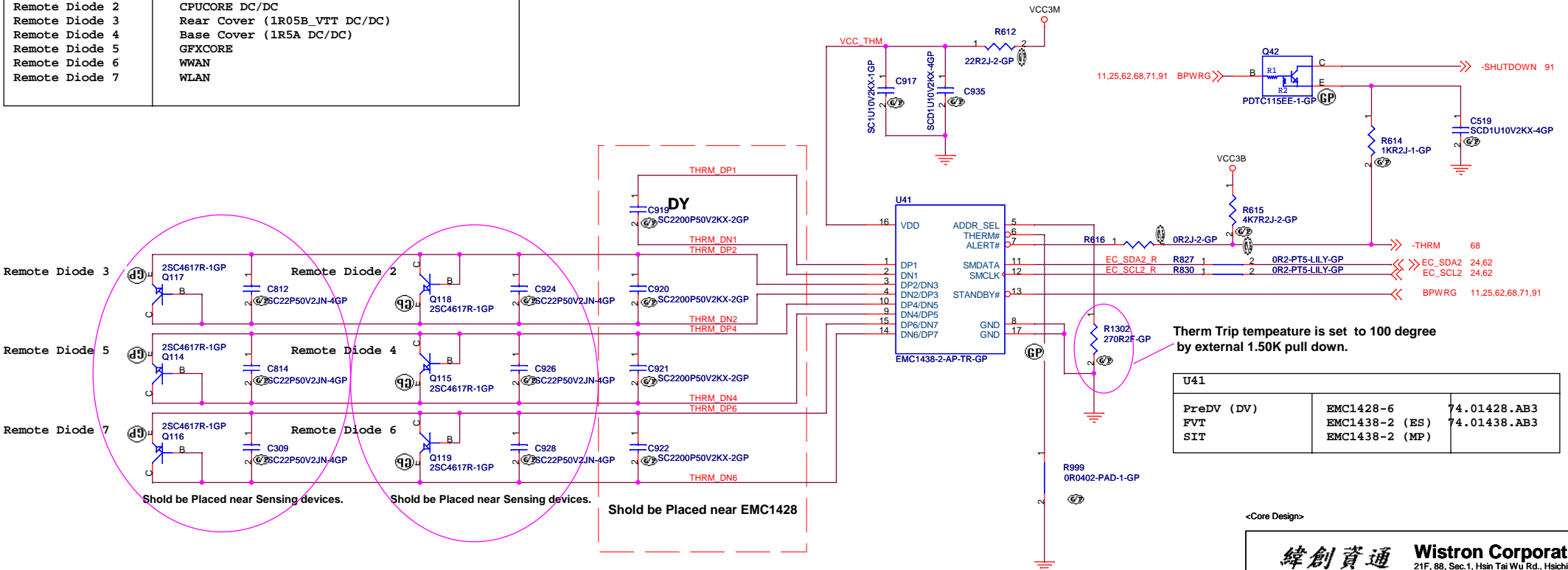
ROHM	BU77700KT-GP	54Y9009BA
TOSHIBA	TC200G02EFG-1036	54Y9009AA





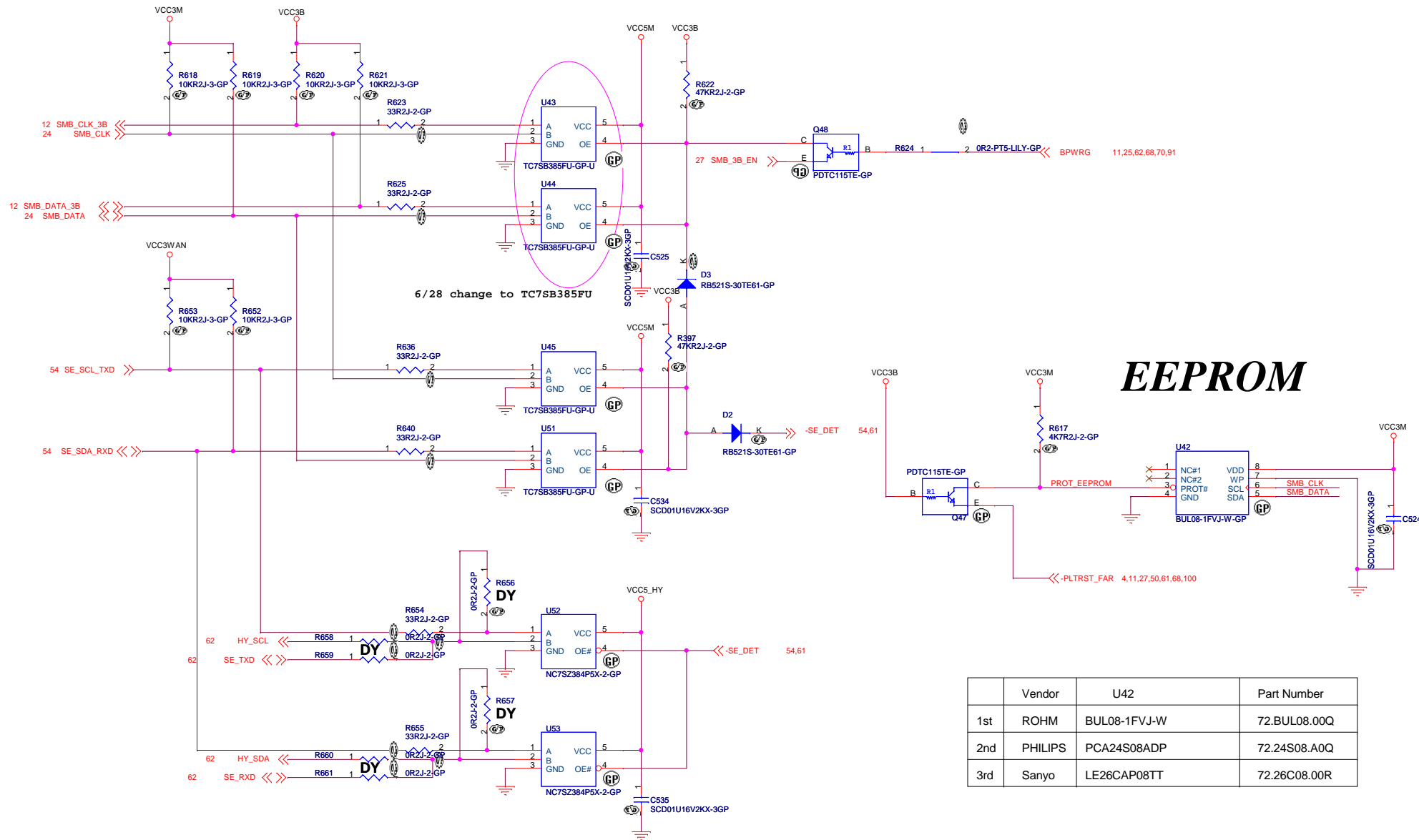
It should be arranged near CPU instead of CPU thermal sensor

Channel	Location
Local	MEMORY
Remote Diode 2	CPUCORE DC/DC
Remote Diode 3	Rear Cover (1R05B_VTT DC/DC)
Remote Diode 4	Base Cover (1R5A DC/DC)
Remote Diode 5	GFXCORE
Remote Diode 6	WWAN
Remote Diode 7	WLAN



Therm Trip temperature is set to 100 degree by external 1.50K pull down.

U41		
PreDV (DV)	EMC1428-6	74.01428.AB3
FVT	EMC1438-2 (ES)	74.01438.AB3
SIT	EMC1438-2 (MP)	

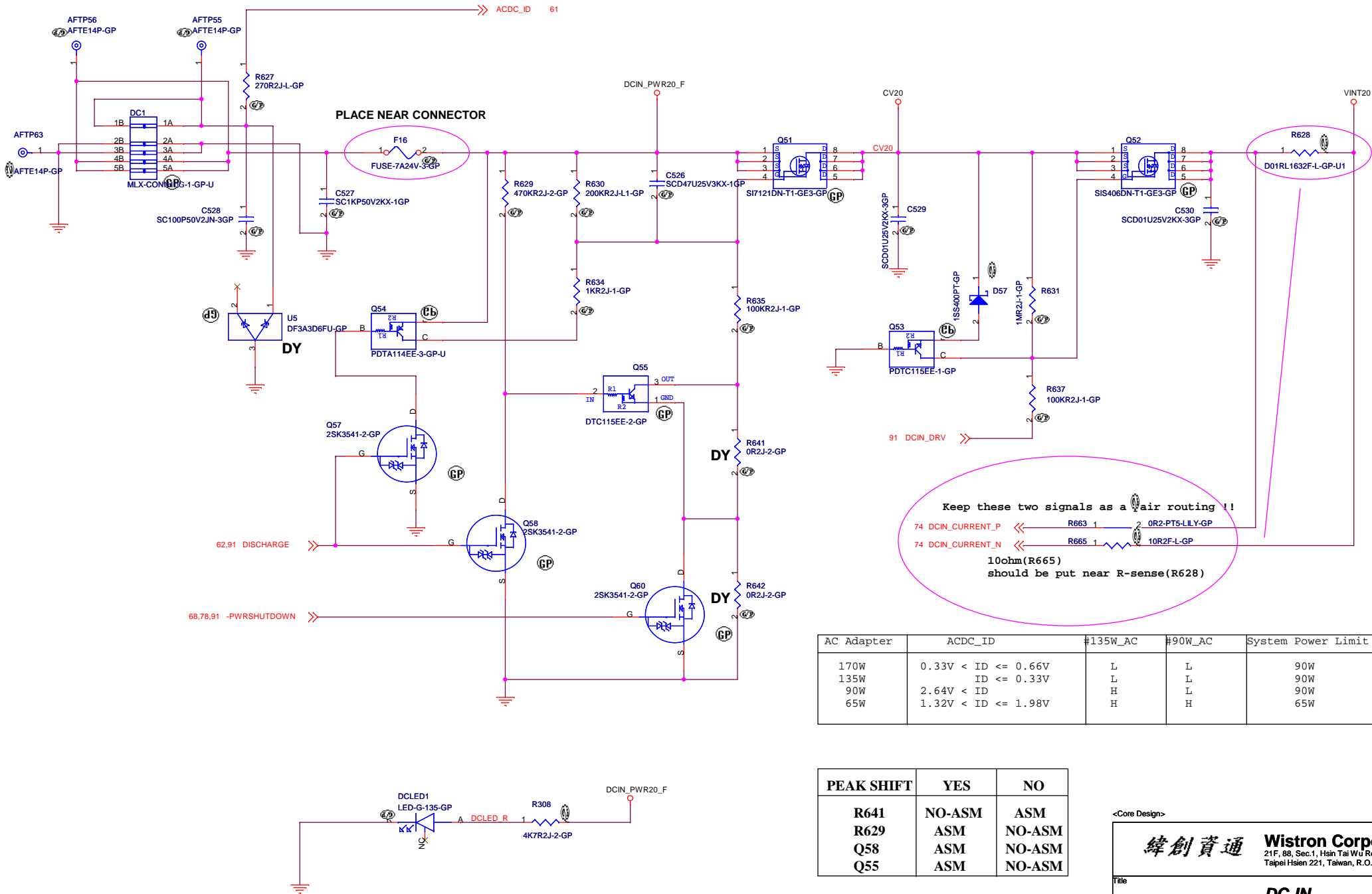


EEPROM

<Core Design>

緯創資通 Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			Rev
EEPROM/SMBUS SW			SC
Size	Document Number	NL-note	
Custom			
Date:	Monday, March 07, 2011	Sheet	71 of 101

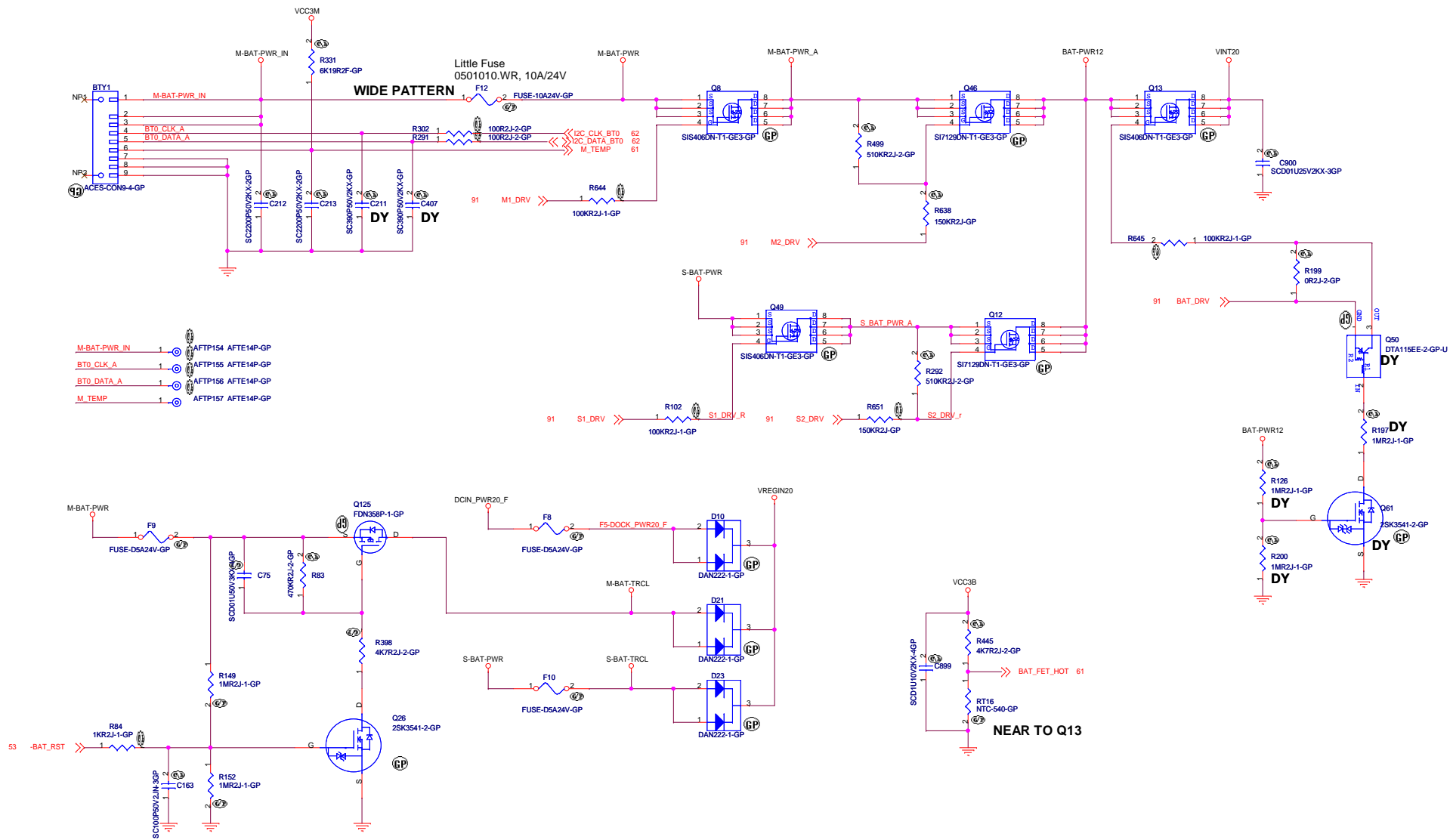


AC Adapter	ACDC_ID	#135W_AC	#90W_AC	System Power Limit
170W	0.33V < ID <= 0.66V	L	L	90W
135W	ID <= 0.33V	L	L	90W
90W	2.64V < ID	H	L	90W
65W	1.32V < ID <= 1.98V	H	H	65W

PEAK SHIFT	YES	NO
R641	NO-ASM	ASM
R629	ASM	NO-ASM
Q58	ASM	NO-ASM
Q55	ASM	NO-ASM

<Core Design>

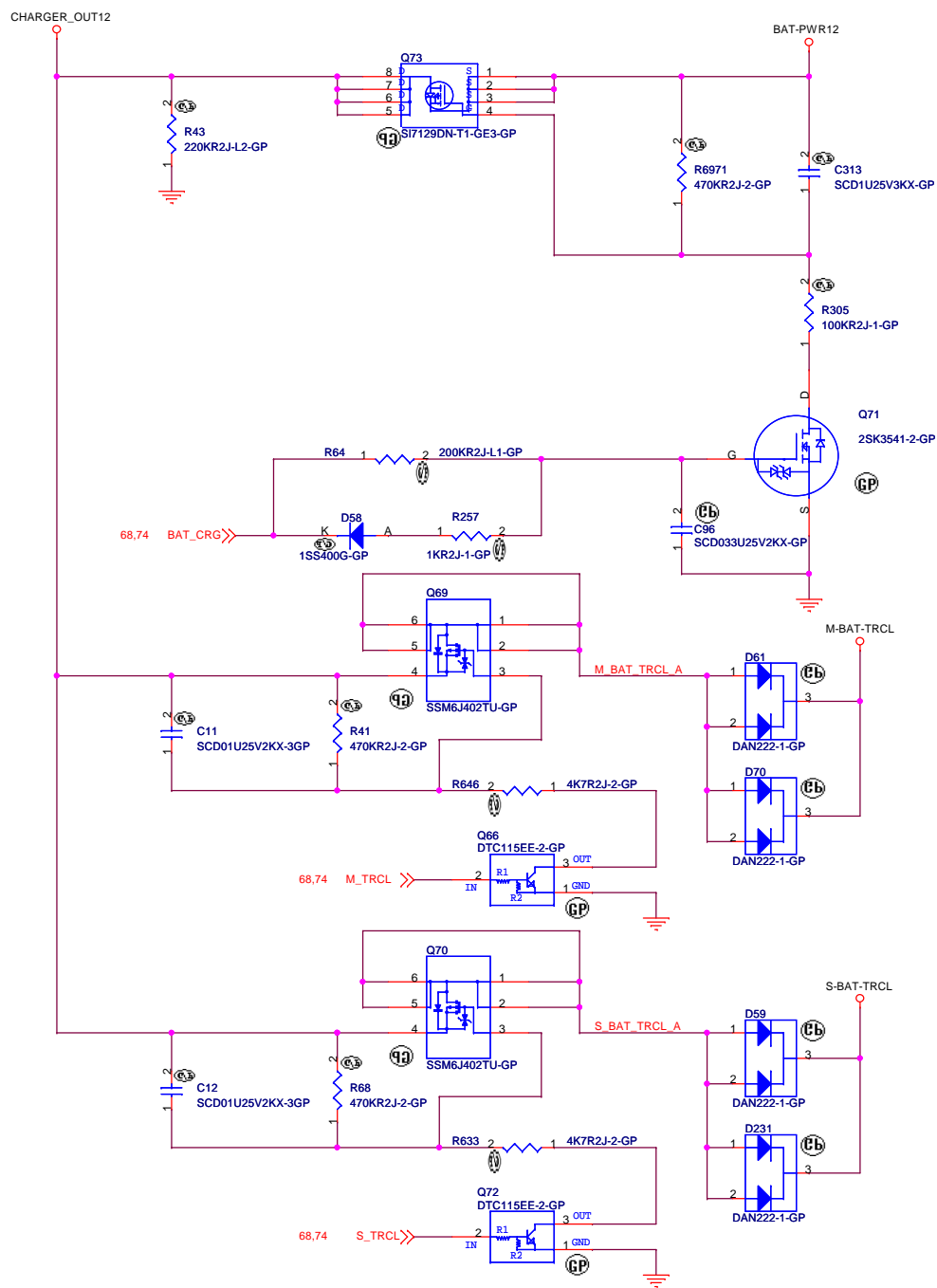
緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
DC-IN			
Size A3	Document Number		Rev SC
NL-note			
Date:	Monday, March 07, 2011	Sheet 72 of 101	

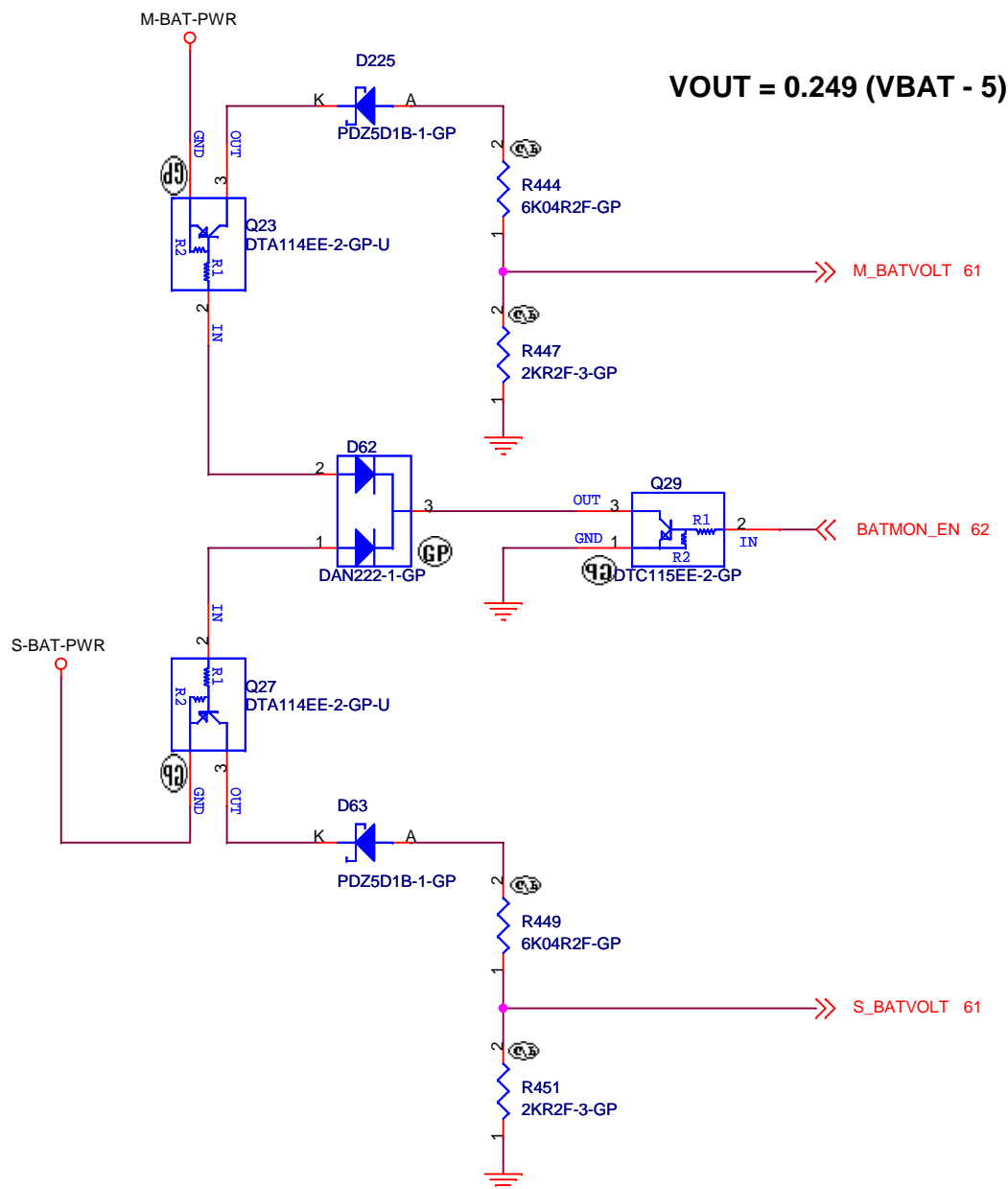


<Core Design>

緯創資通 **Wistron Corporation**
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			
BATTERY INPUT			
Size	Document Number		Rev
Custom	NL-note		SC
Date: Monday, March 07, 2011		Sheet 73 of	101





<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BATTERY MONITOR

Size
A4

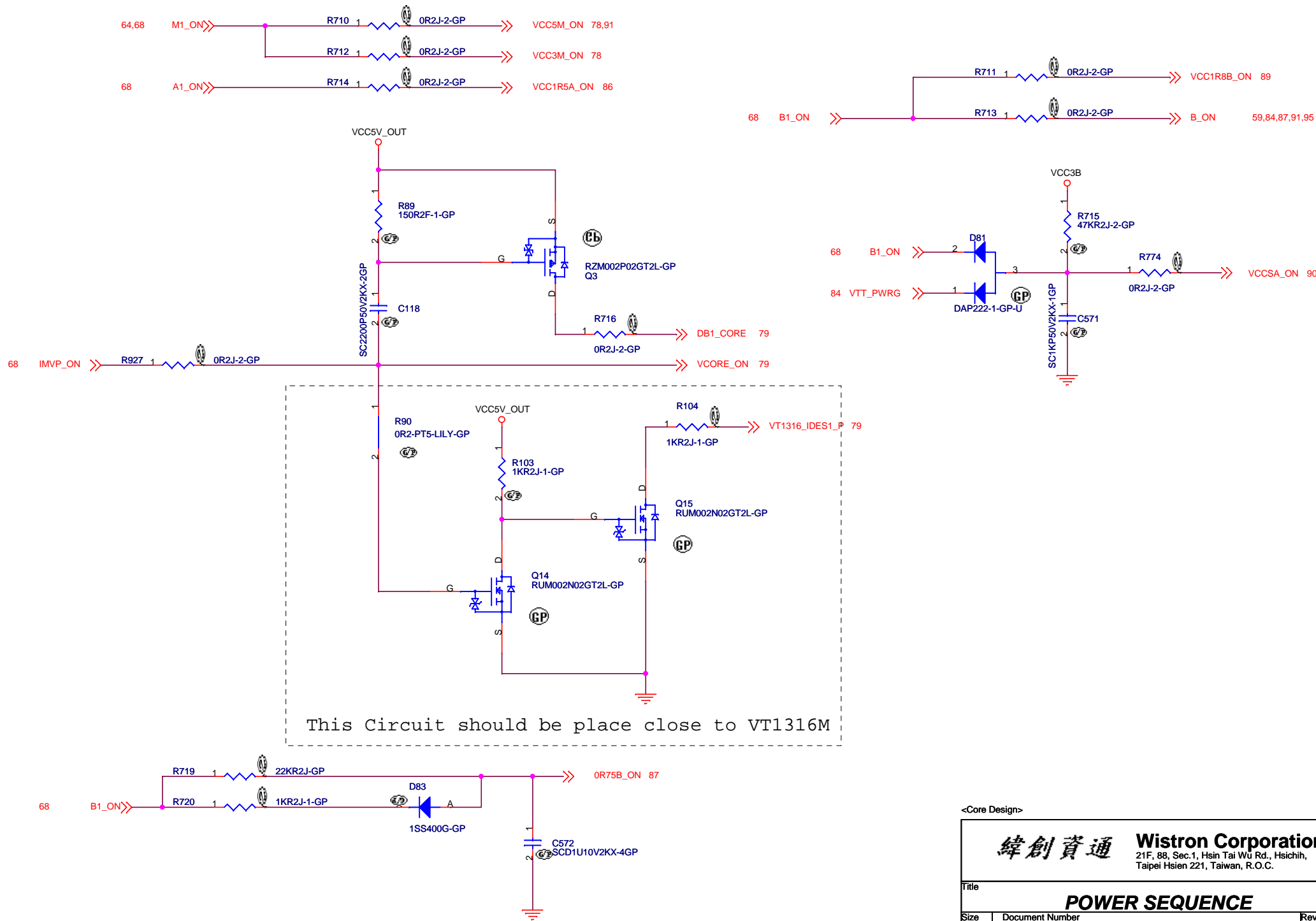
Document Number

NL-note

Rev
SC

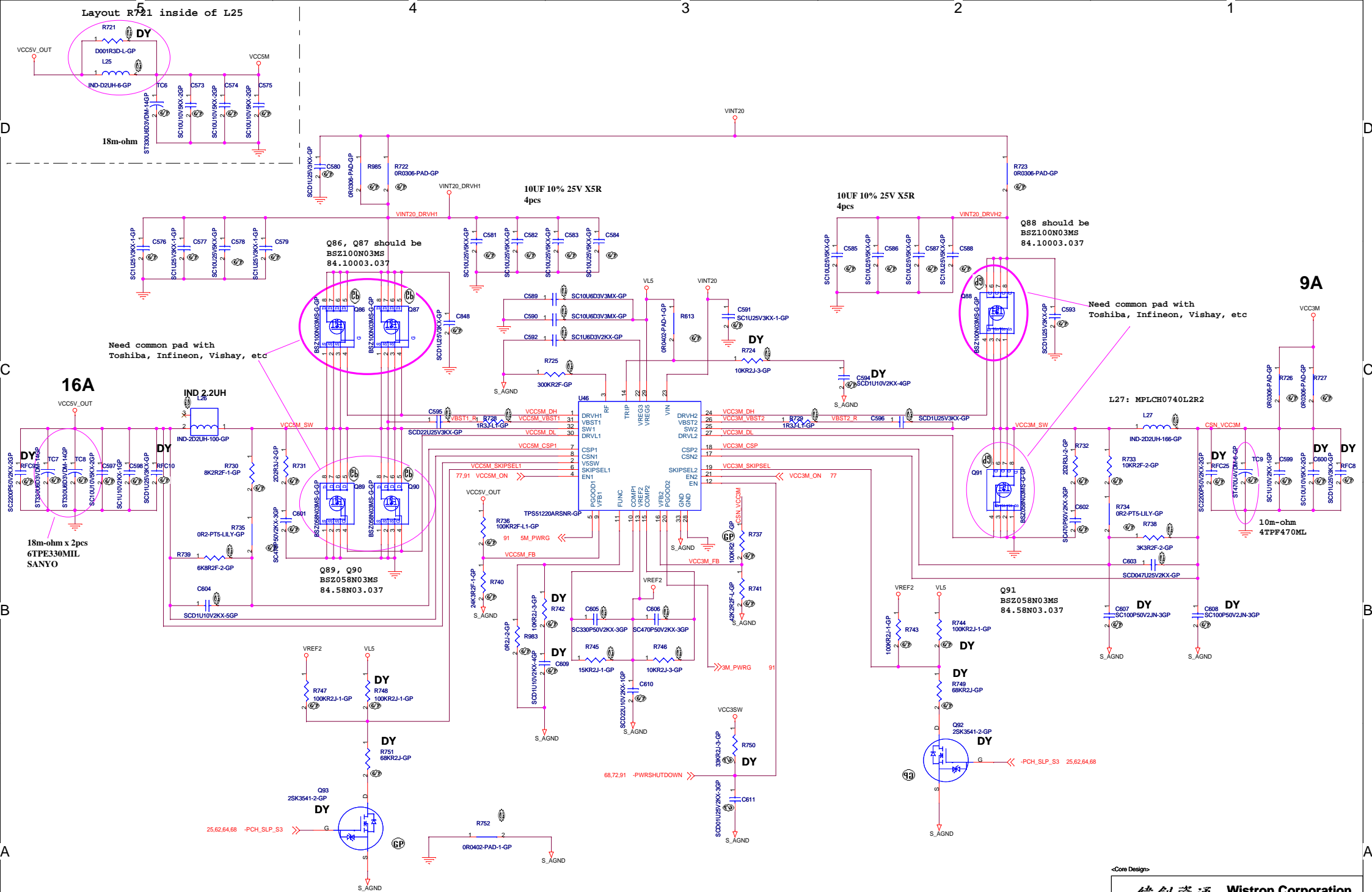
Date: Monday, March 07, 2011

Sheet 76 of 101



<Core Design>

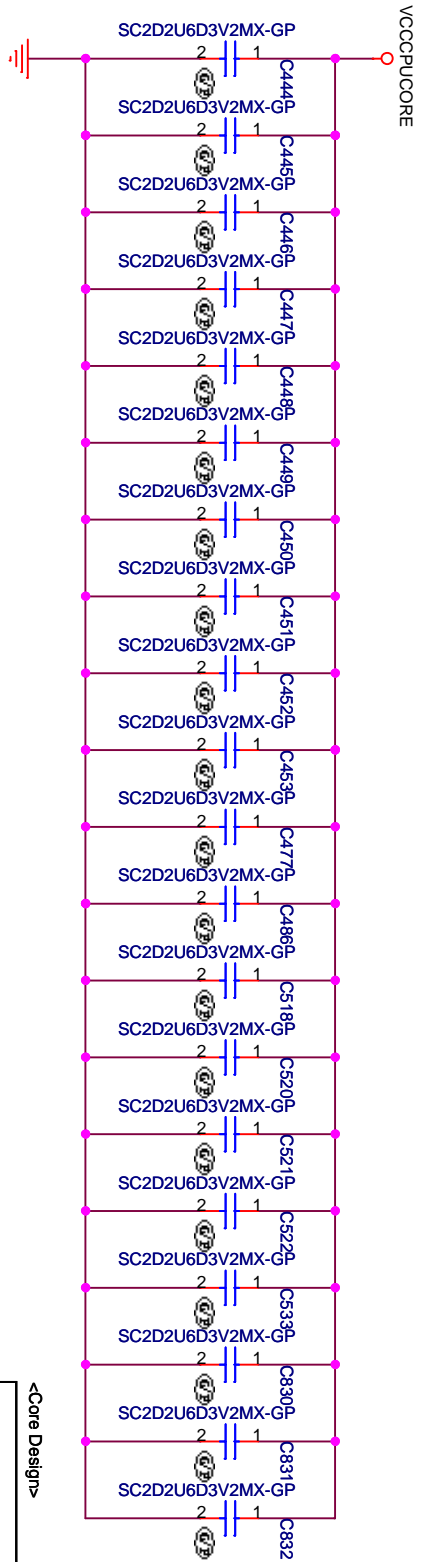
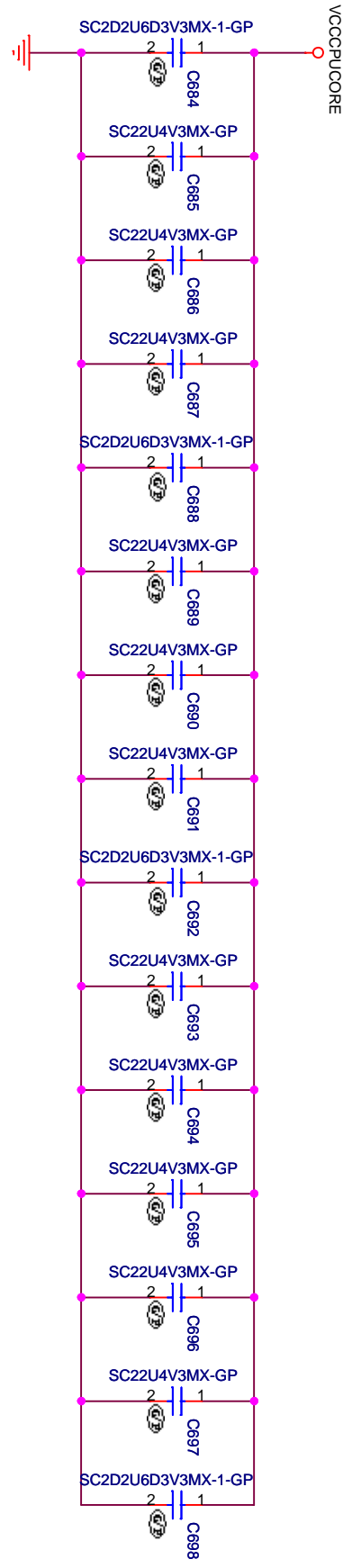
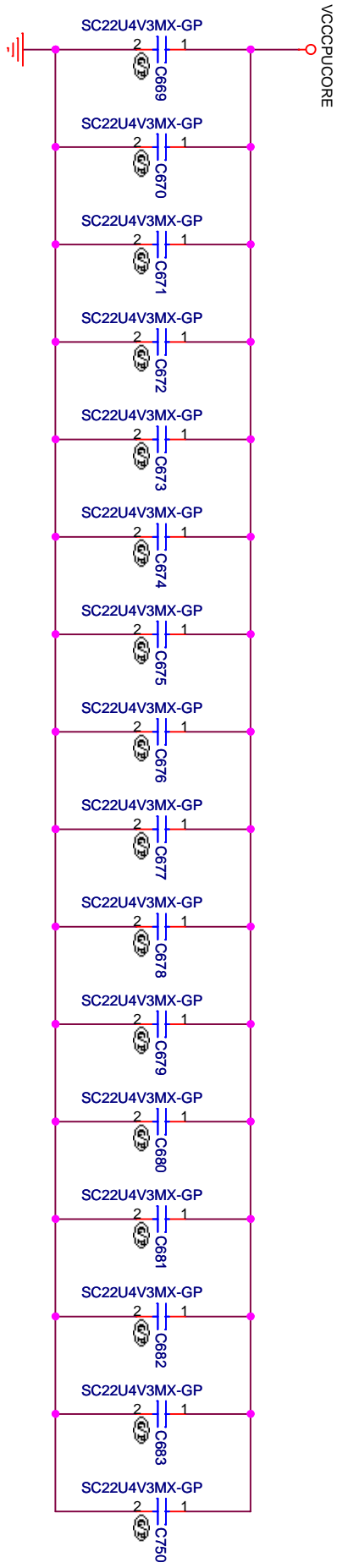
緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
POWER SEQUENCE			
Size Custom	Document Number NL-note		Rev SC
Date: Monday, March 07, 2011	Sheet 77	of	101



BLANK

<Core Design>

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title BLANK			
Size A4	Document Number NL-note		Rev SC
Date: Monday, March 07, 2011		Sheet 80 of	101



<Core Design>

緯創資通

Wistron Corporation

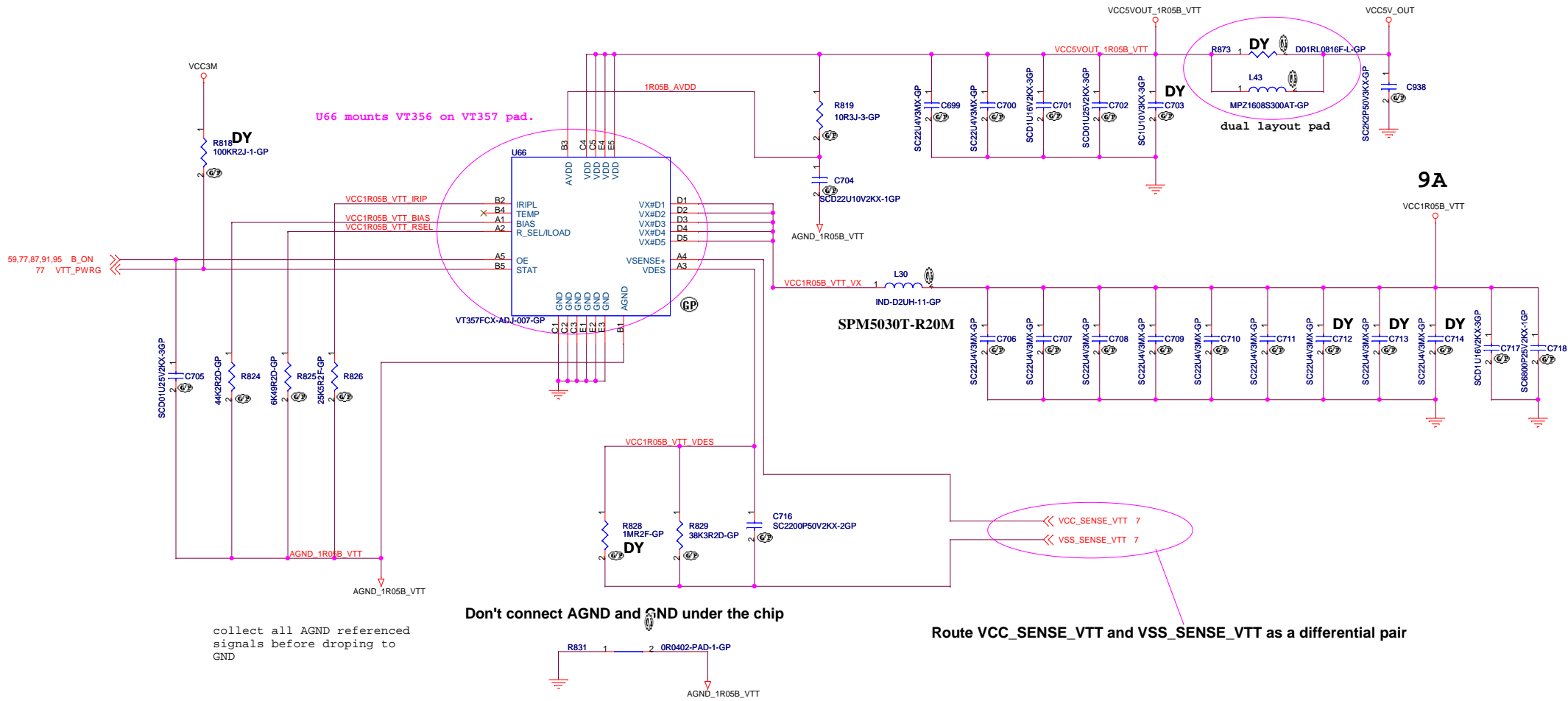
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title			DC-DC VCCPUCORE		
Size	Document Number		Rev		
A4			SC		
Date:	Monday, March 07, 2011	Sheet	82	of	101
NL-note					

BLANK

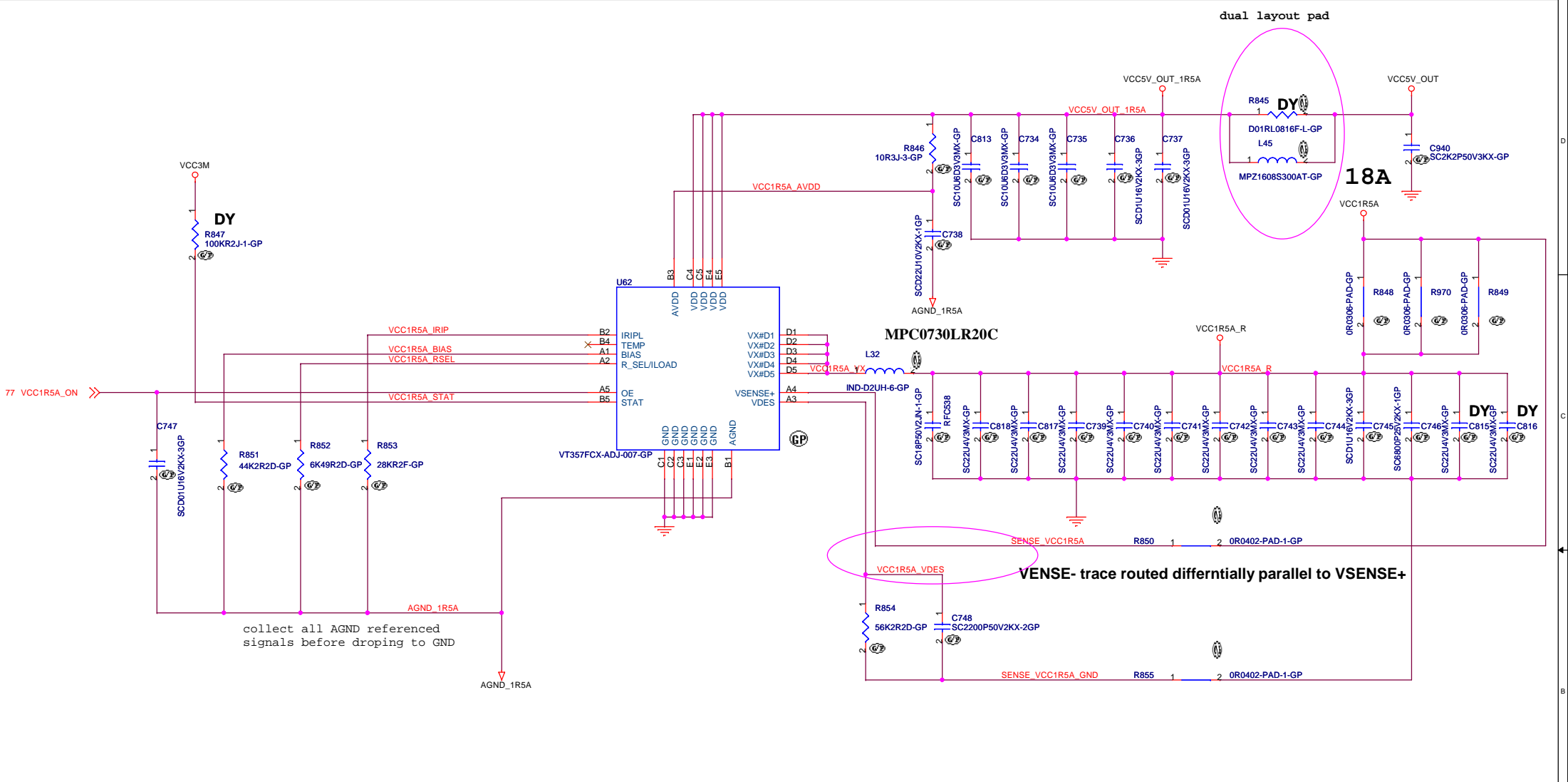
<Core Design>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title <div>BLANK</div>	
Size <div>A4</div>	Document Number <div>NL-note</div>
Date <div>Monday, March 07, 2011</div>	Rev <div>SC</div>
Date: Monday, March 07, 2011	
Sheet 83 of 101	



<Core Design>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
DC-DC VCC1R05B VTT			
Size	Document Number	Rev	
Custom	Dasher-1	SC	
Date:	Monday, March 07, 2011	Sheet	84 of 101

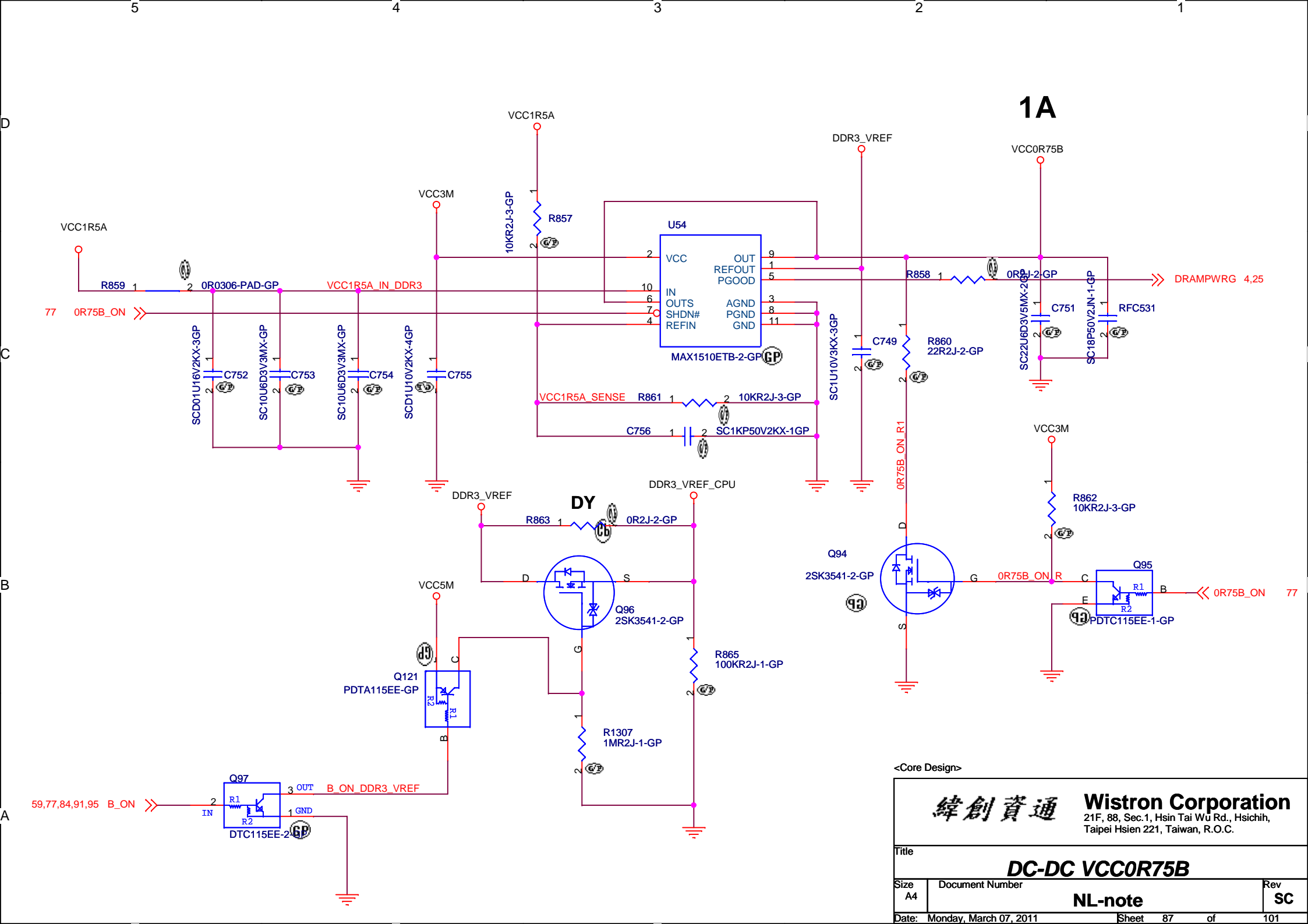


77 VCC1R5A_ON


collect all AGND referenced signals before dropping to GND

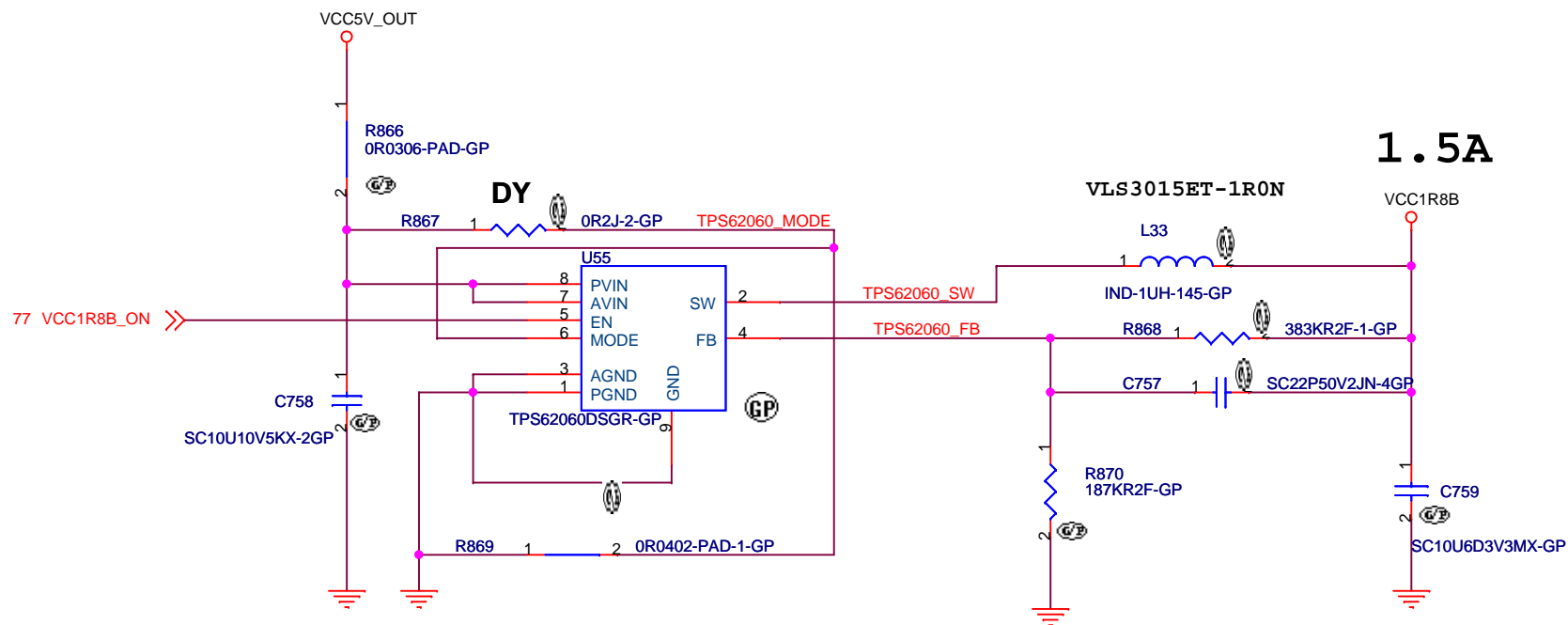
Don't connect AGND and GND under the chip





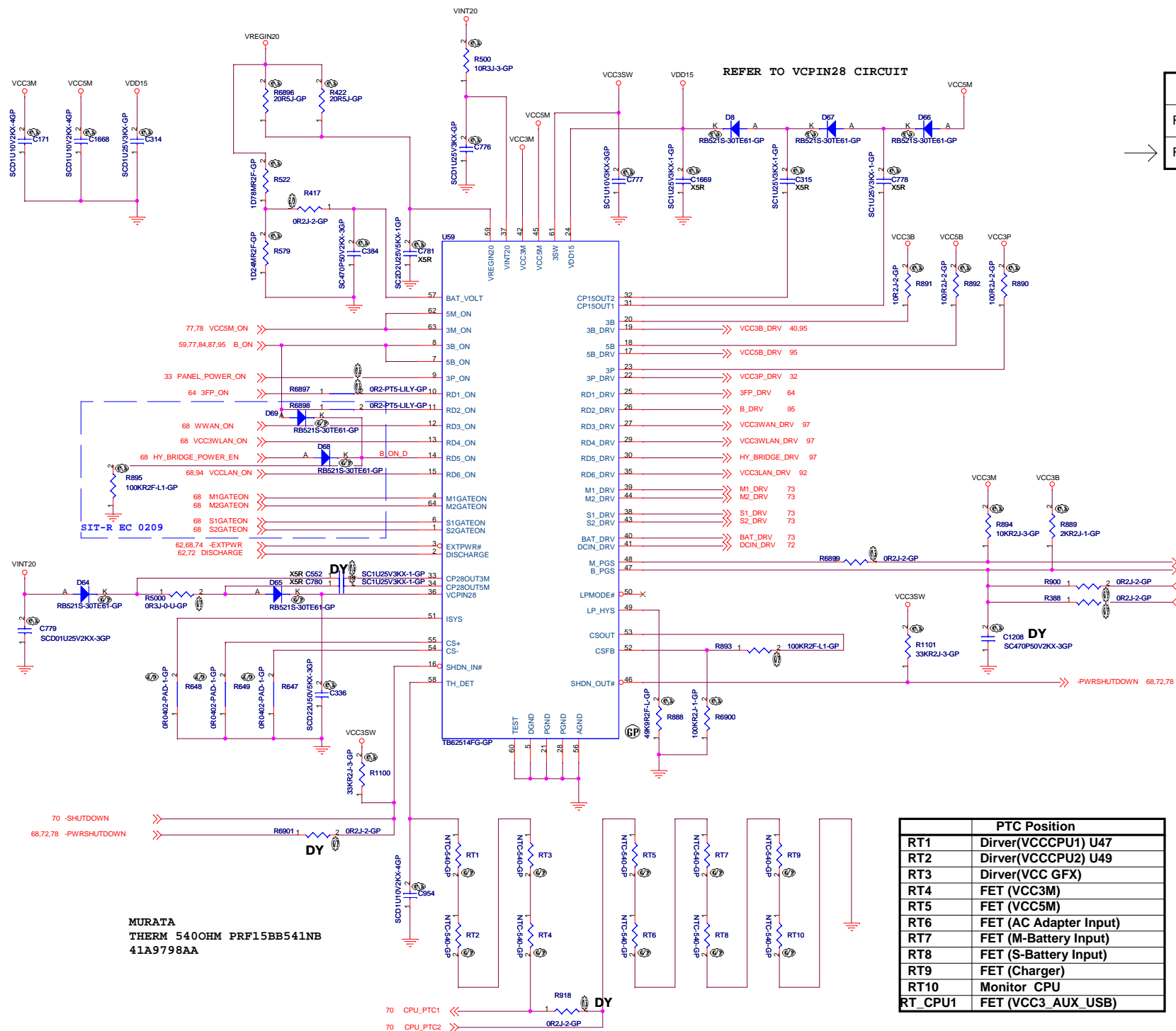
<Core Design>

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
BLANK			
Size A4	Document Number NL-note		Rev SC
Date:	Monday, March 07, 2011	Sheet 88 of	101



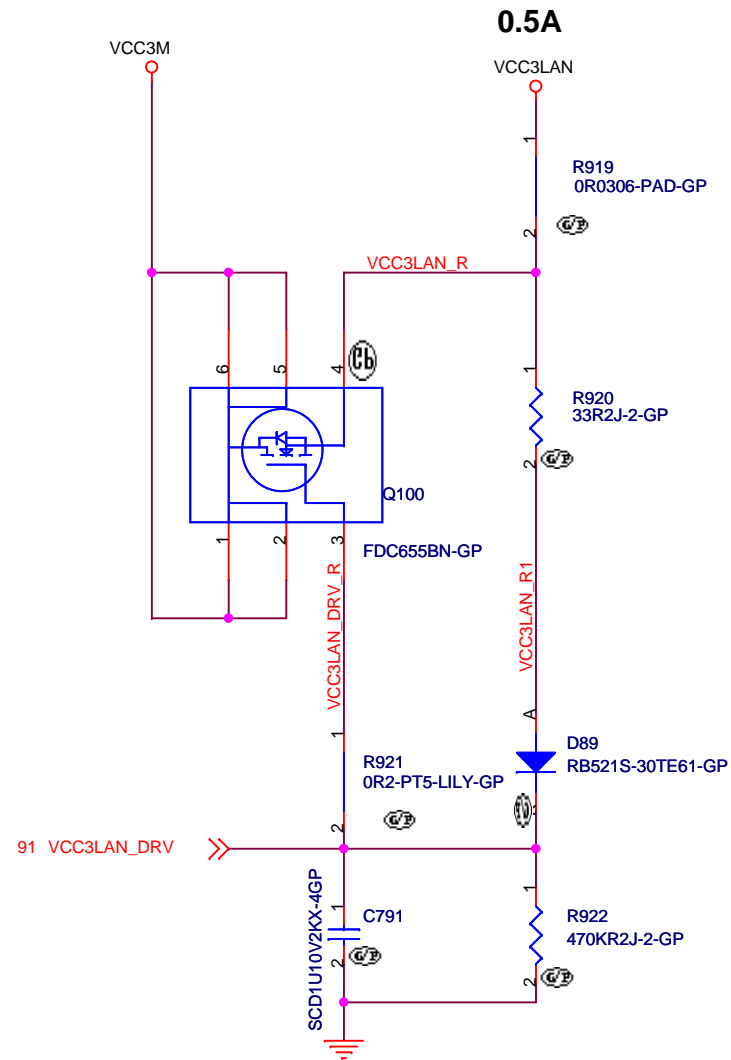
<Core Design>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title			
DC-DC VCC1R8B			
Size A4	Document Number NL-note		Rev SC
Date: Monday, March 07, 2011	Sheet 89 of		101



	ROHM	TOSHIBA
RINKAN-1	BD4176KVT	
RINKAN-2	BD41760KVT	TB62514FG

	PTC Position
RT1	Dirver(VCCCPU1) U47
RT2	Dirver(VCCCPU2) U49
RT3	Dirver(VCC GFX)
RT4	FET (VCC3M)
RT5	FET (VCC5M)
RT6	FET (AC Adapter Input)
RT7	FET (M-Battery Input)
RT8	FET (S-Battery Input)
RT9	FET (Charger)
RT10	Monitor CPU
RT_CPU1	FET (VCC3_AUX_USB)



<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

LOAD SW 1

Size
A4

Document Number

NL-note

Rev
SC

Date: Monday, March 07, 2011

Sheet 92 of 101

BLANK

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size
A4

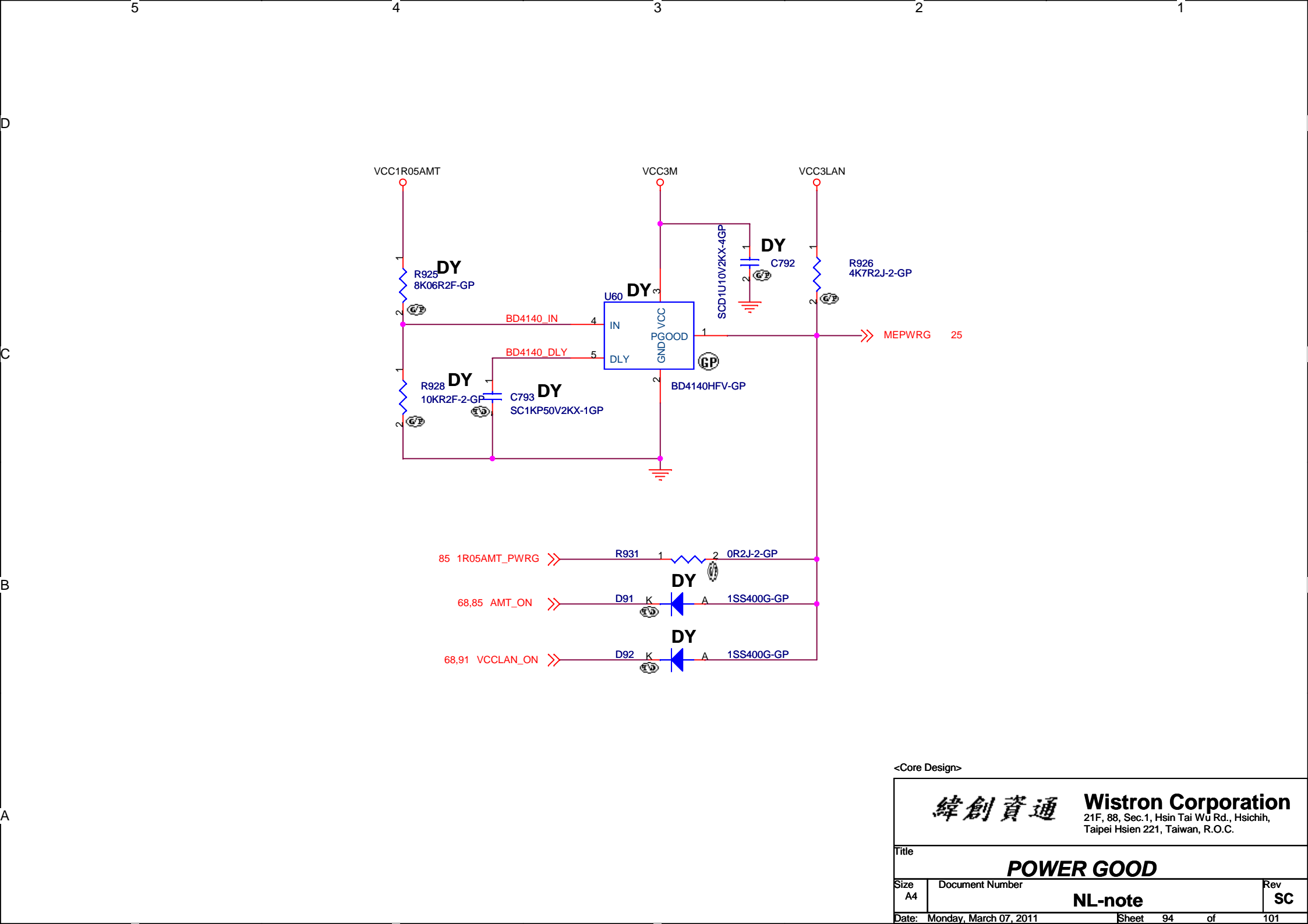
Document Number

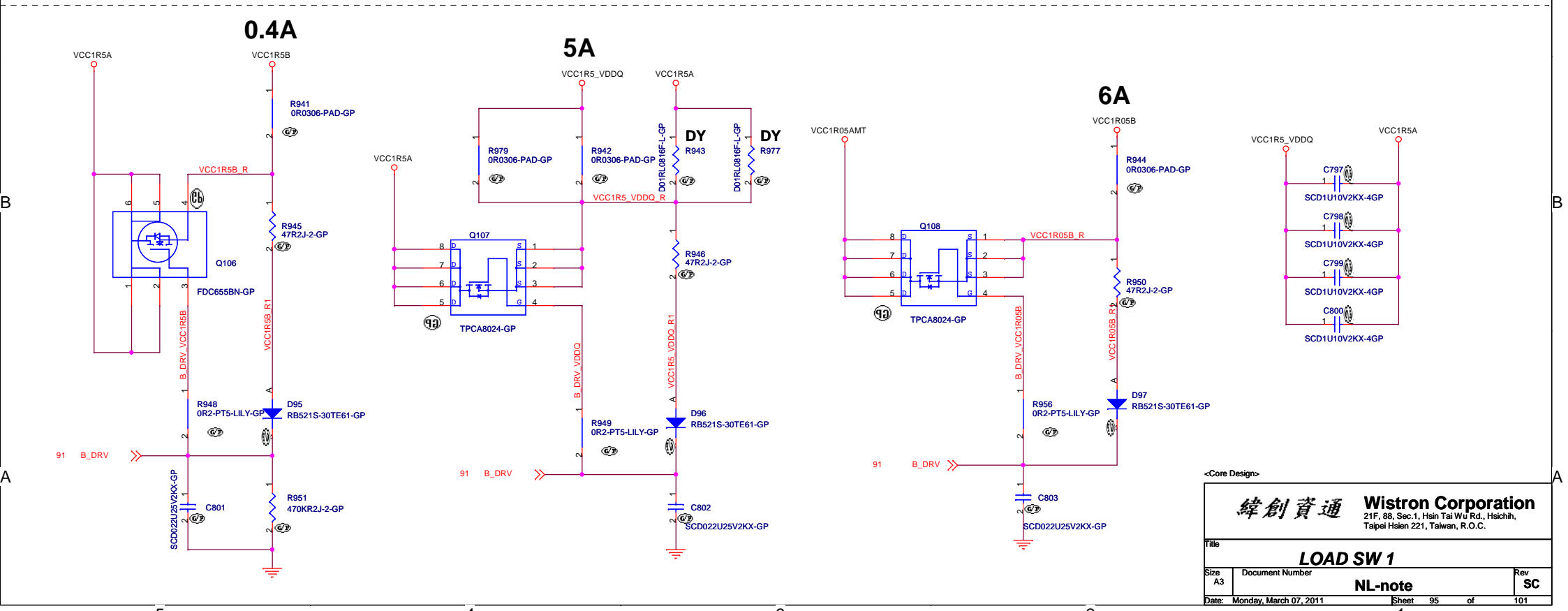
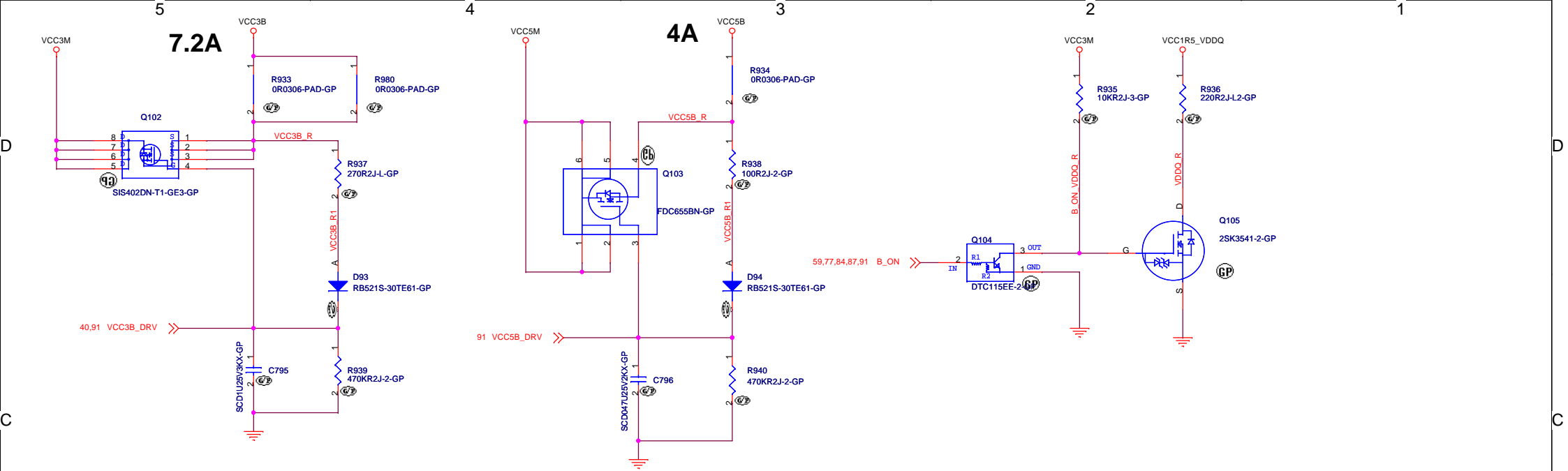
NL-note

Rev
SC

Date: Monday, March 07, 2011

Sheet 93 of 101





BLANK

<Core Design>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

BLANK

Size
A4

Document Number

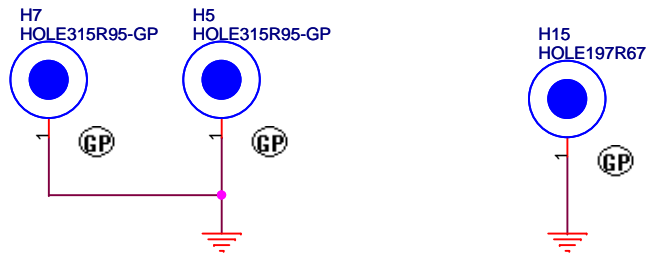
NL-note

Rev
SC

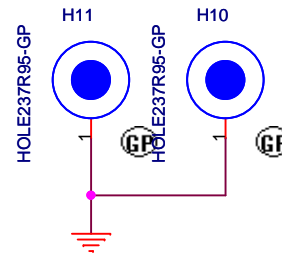
Date: Monday, March 07, 2011

Sheet 96 of 101

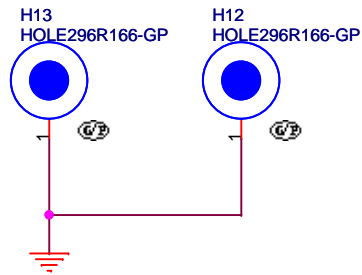
ZZ.00PAD.911



ZZ.00PAD.921



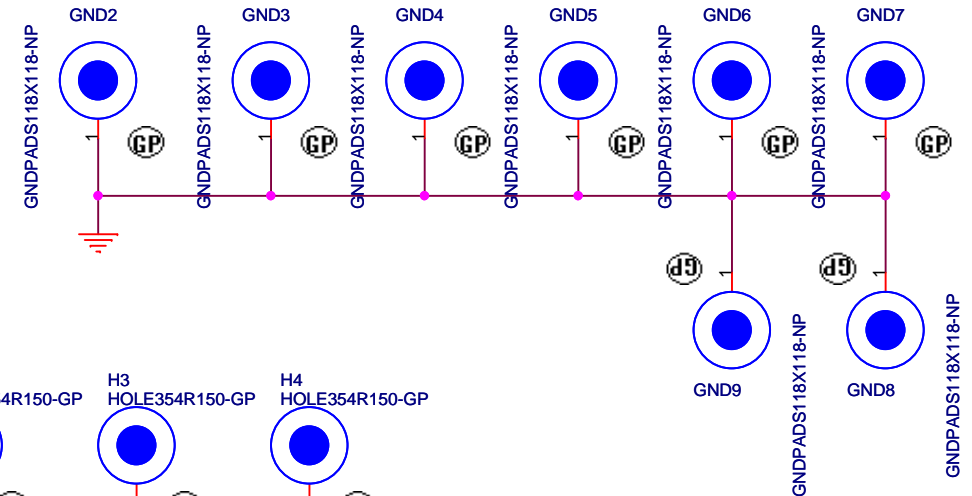
ZZ.00PAD.J11



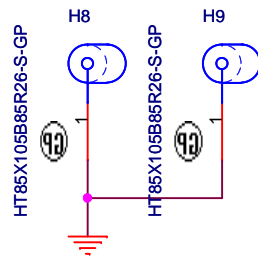
ZZ.00PAD.XXX



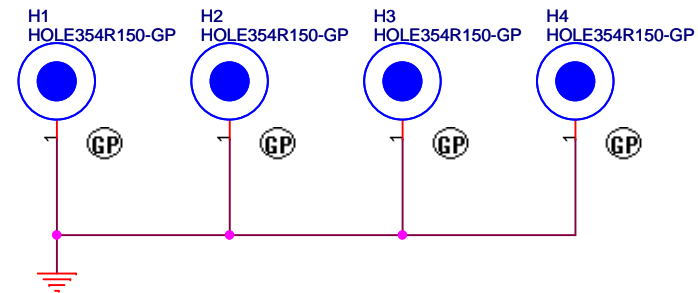
ZZ.00PAD.XXX



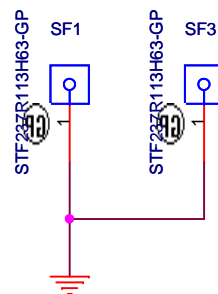
ZZ.SCREW.191



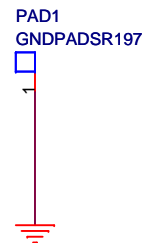
ZZ.SCREW.101



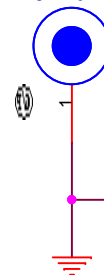
34.44P03.101



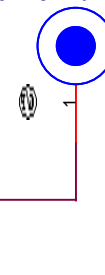
ZZ.NDPAD.XXX



SF2 STF237R113H103-GP



SF4 STF237R113H103-GP



<Core Design>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

HOLES/GND/PADS

Size
A4

Document Number

NL-note

Rev
SC

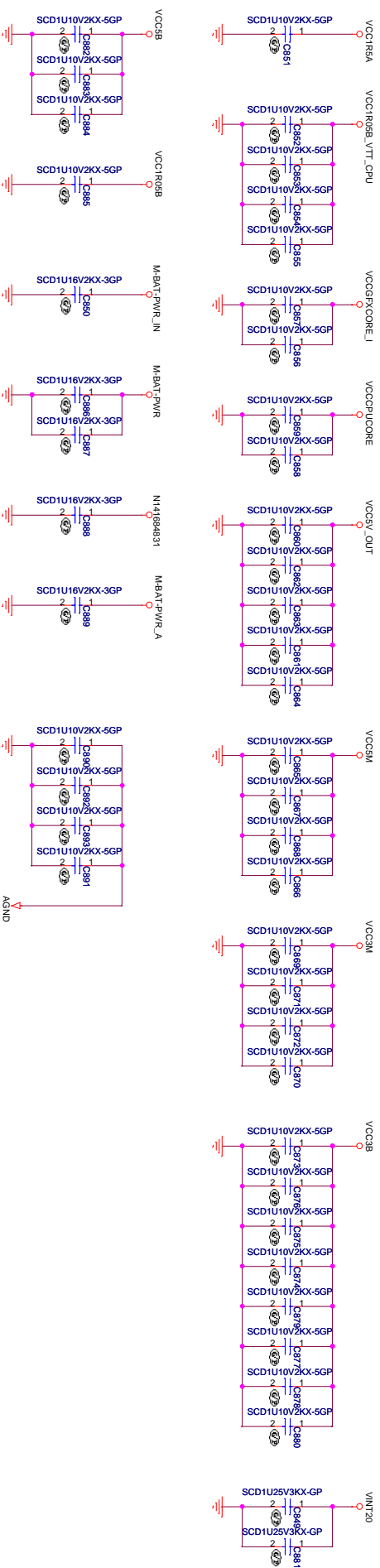
Date: Monday, March 07, 2011

Sheet 98 of 101

RF decoupling caps

named as RFCxxx

Long power trace EMI decoupling caps



<Core Design>

緯創資通

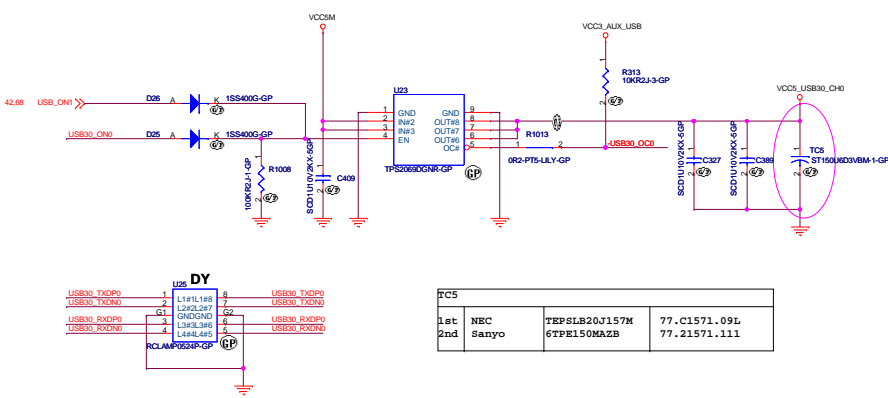
Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd, Hsichih,
Tapei Hsien 221, Taiwan, R.O.C.

EMI DECOUPLING

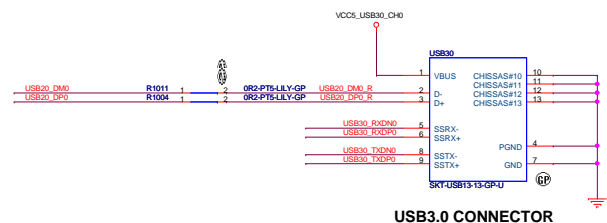
NL-note

Size	Document Number	Rev
Custm		SC

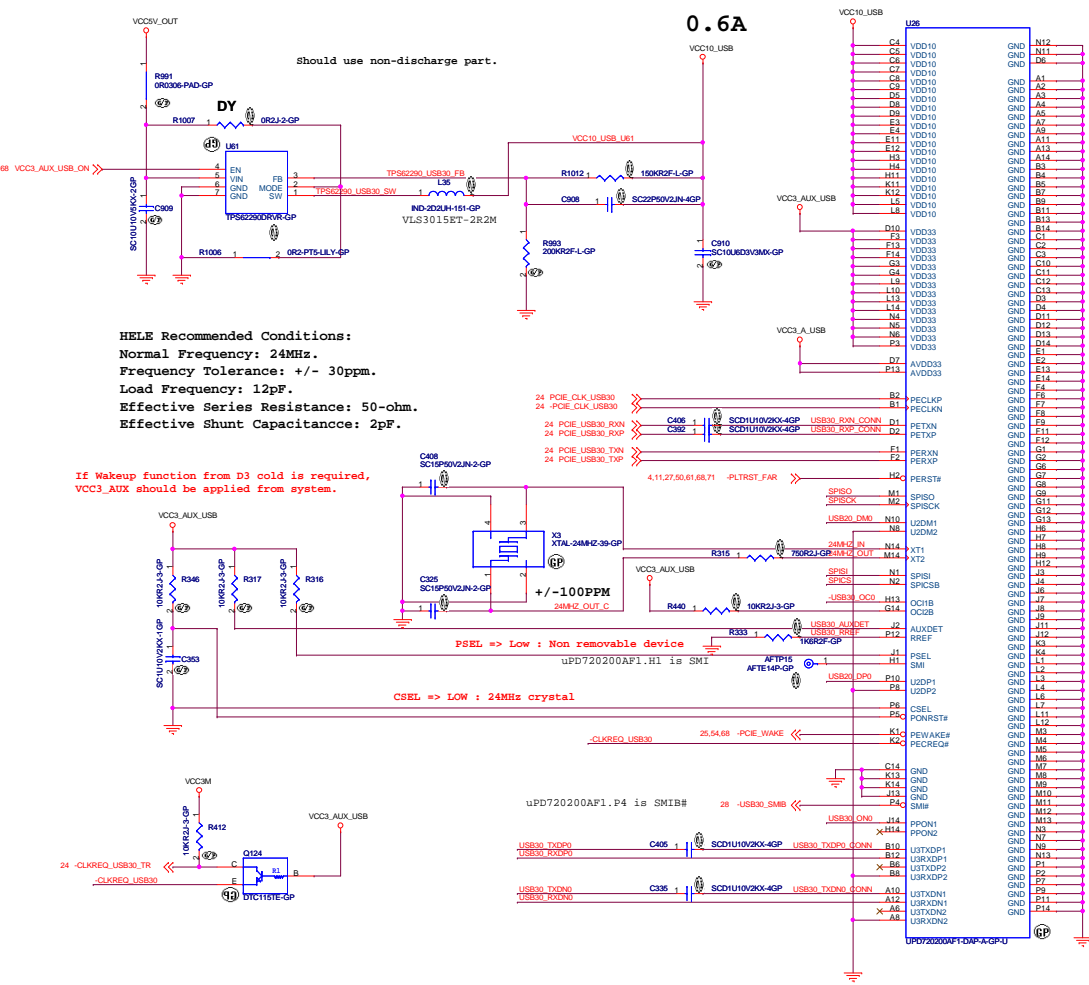
Date: Monday, March 07, 2011	Sheet 99 of 101
------------------------------	-----------------



FC5			
1st	NEC	TEPS8LB20J157M	77.C1571.09L
2nd	Sanyo	6TPE150MA2B	77.21571.111

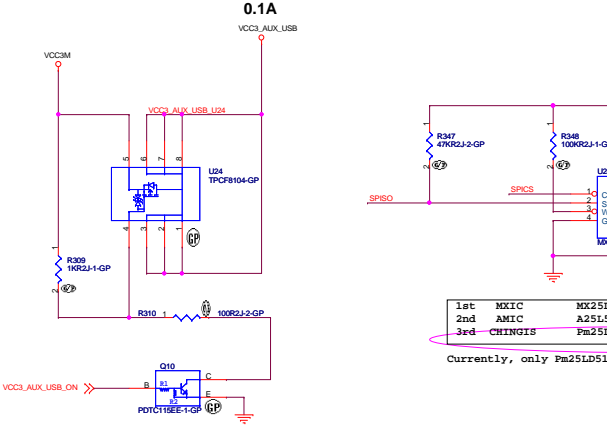


USB3.0 CONNECTOR



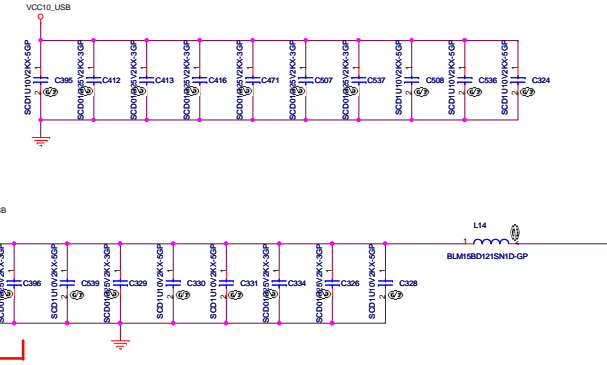
HELE Recommended Conditions:
 Normal Frequency: 24MHz.
 Frequency Tolerance: +/- 30ppm.
 Load Frequency: 12pF.
 Effective Series Resistance: 50-ohm.
 Effective Shunt Capacitance: 2pF.

If Wakeup function from D3 cold is required,
 VCC3_AUX should be applied from system.



1st	MXIC	MX25L512LEOC-20G	TSSOP8	72.25512.F09
2nd	AMIC	A25L512V-F	TSSOP8	72.25512.00Q
3rd	CHINGIS	Pm25LD512C-DCB	TSSOP8	72.25512.R09

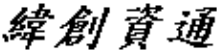
Currently, only Pm25LD512C is qualified by NEC



NOT MANDATORY TO PLACE.

BLANK

<Core Design>

		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title BLANK			
Size A4	Document Number NL-note		Rev SC
Date: Monday, March 07, 2011		Sheet 101 of	101